

5 Schematic Diagrams and PCB Silkscreen

5-1 Main Board

5-1-1 Schematic Diagrams

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- Sheet 2. Clock Chip
- Sheet 3. PII DIXON-1
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- Sheet 5. NORTHBRIDGE
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- Sheet 9. PIIX4E-1
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- Sheet 12. CARD BUS : RB5C478II
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- Sheet 25. AUDIO Chip & IRDA I/F
- Sheet 26. POWER & FDDs CONNECTER
- Sheet 27. Modem Connector
- Sheet 28. CPU_POWER
- Sheet 29~33 Audio board

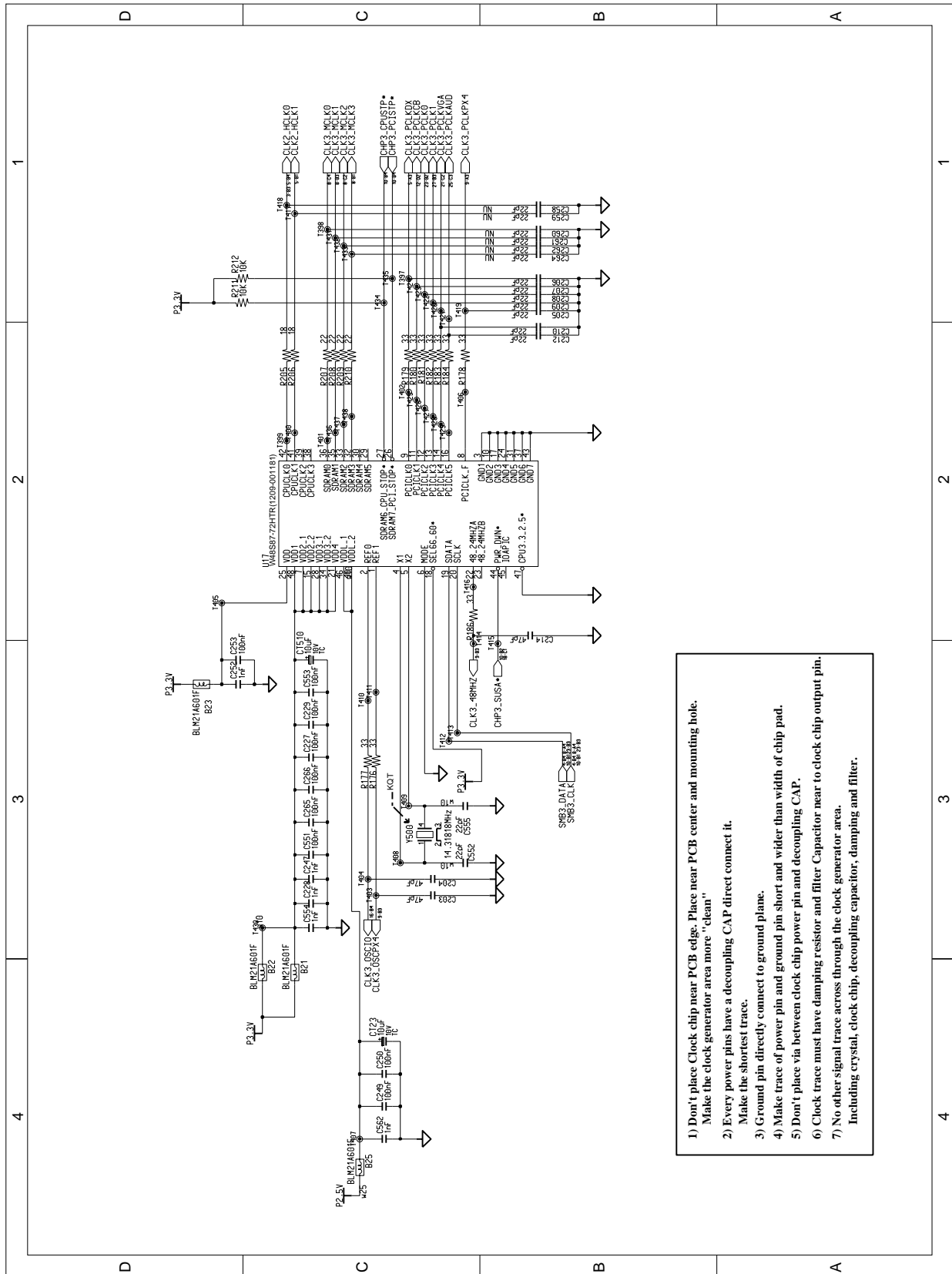
SONIC

CPU : CELERON
 Chip Set : 443DX
 Remarks :

Model Name : S630/S670
 PBA Name :
 PCB Code : BA41-00026A
 Dev. Step : MP
 Revision : 1.0
 T.R. Date : 1999.03.11.

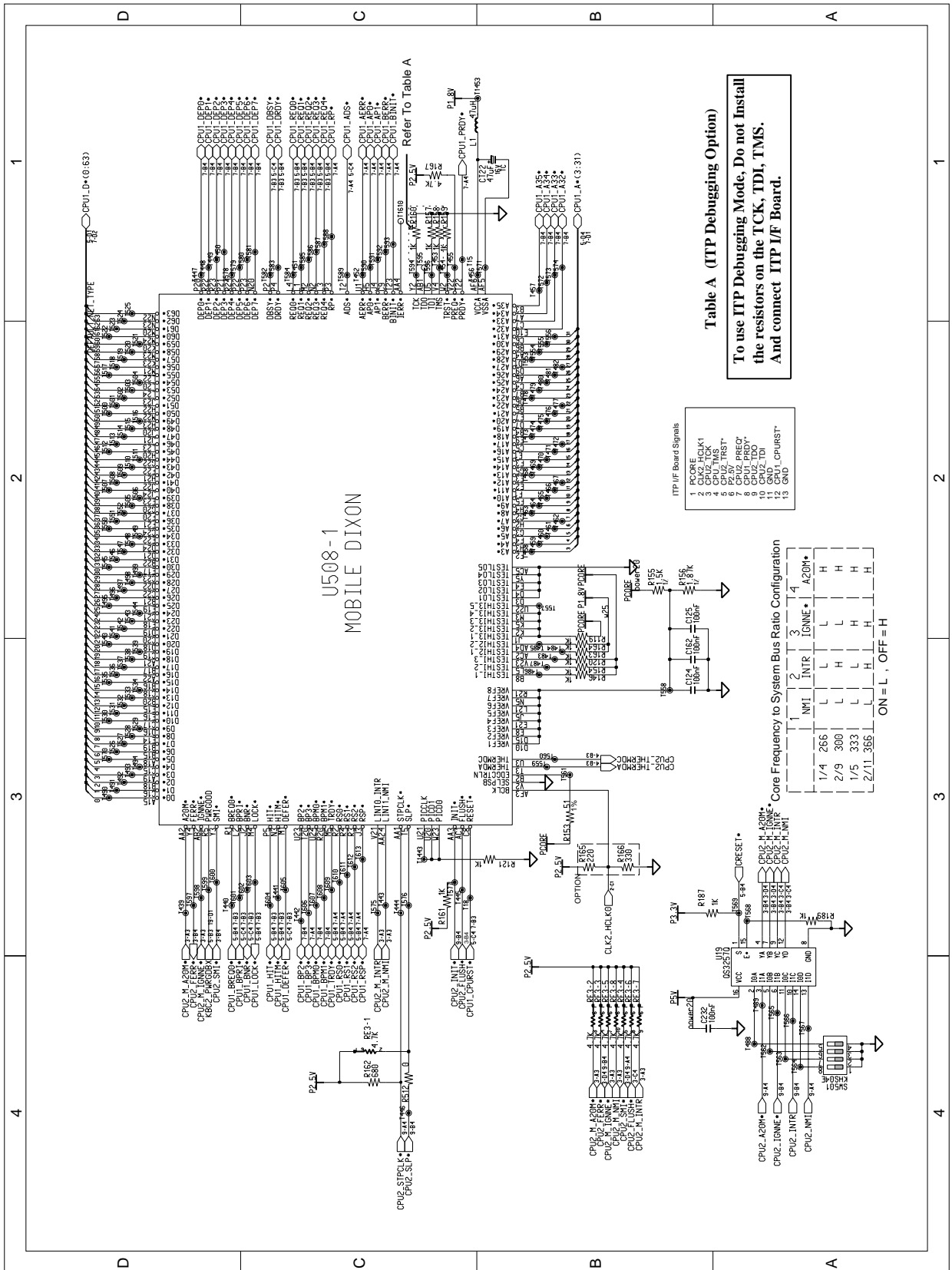
DRAW	CHECK	APPROVAL

5-1-1(a) Main Board Schematic Sheet 2 of 28(Clock Chip)

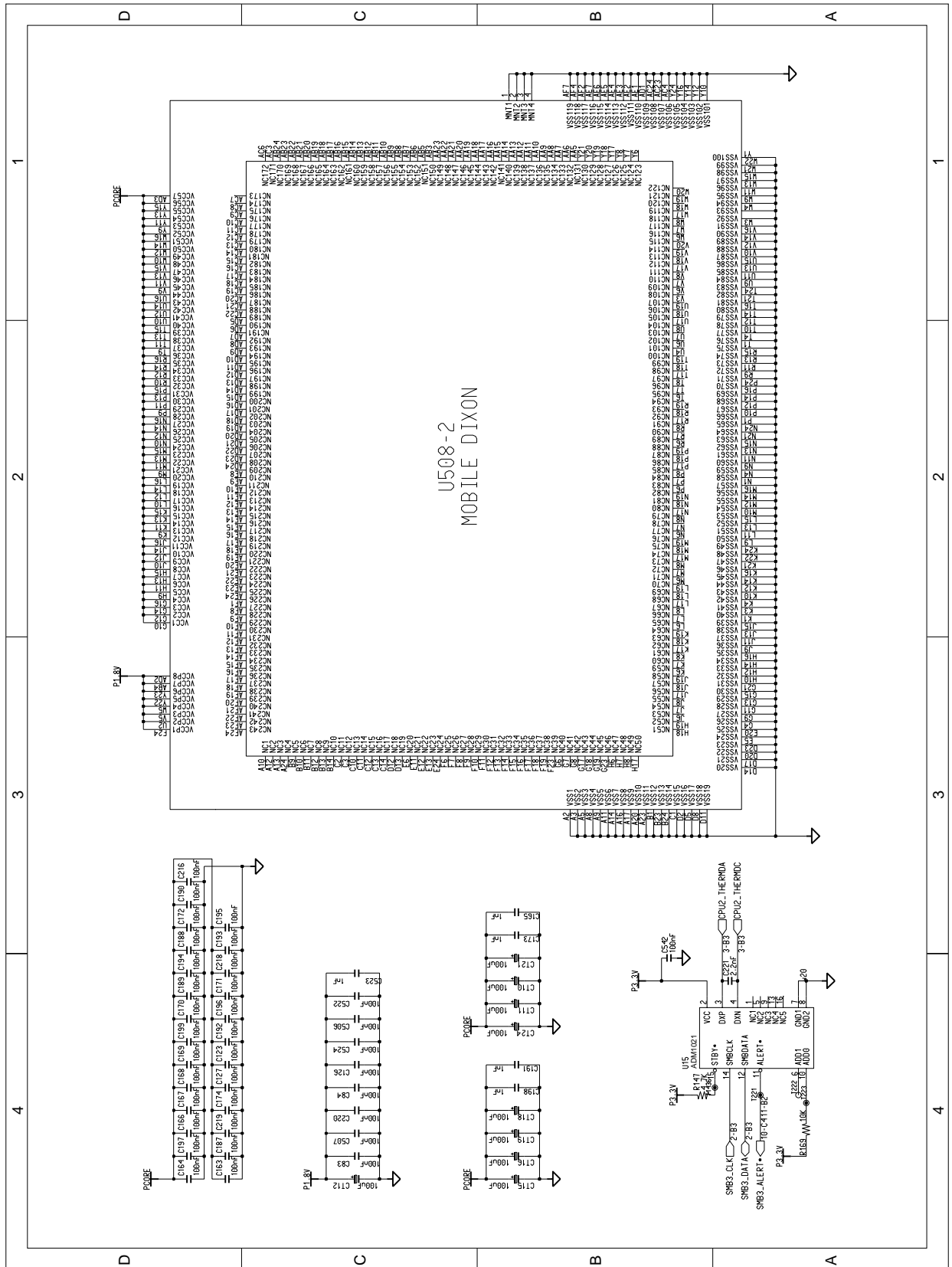


- 1) Don't place Clock chip near PCB edge. Place near PCB center and mounting hole.
Make the clock generator area more "clean"
- 2) Every power pins have a decoupling CAP direct connect it.
Make the shortest trace.
- 3) Ground pin directly connect to ground plane.
- 4) Make trace of power pin and ground pin short and wider than width of chip pad.
- 5) Don't place via between clock chip power pin and decoupling CAP.
- 6) Clock trace must have damping resistor and filter Capacitor near to clock chip output pin.
- 7) No other signal trace across through the clock generator area.
Including crystal, clock chip, decoupling capacitor, damping and filter.

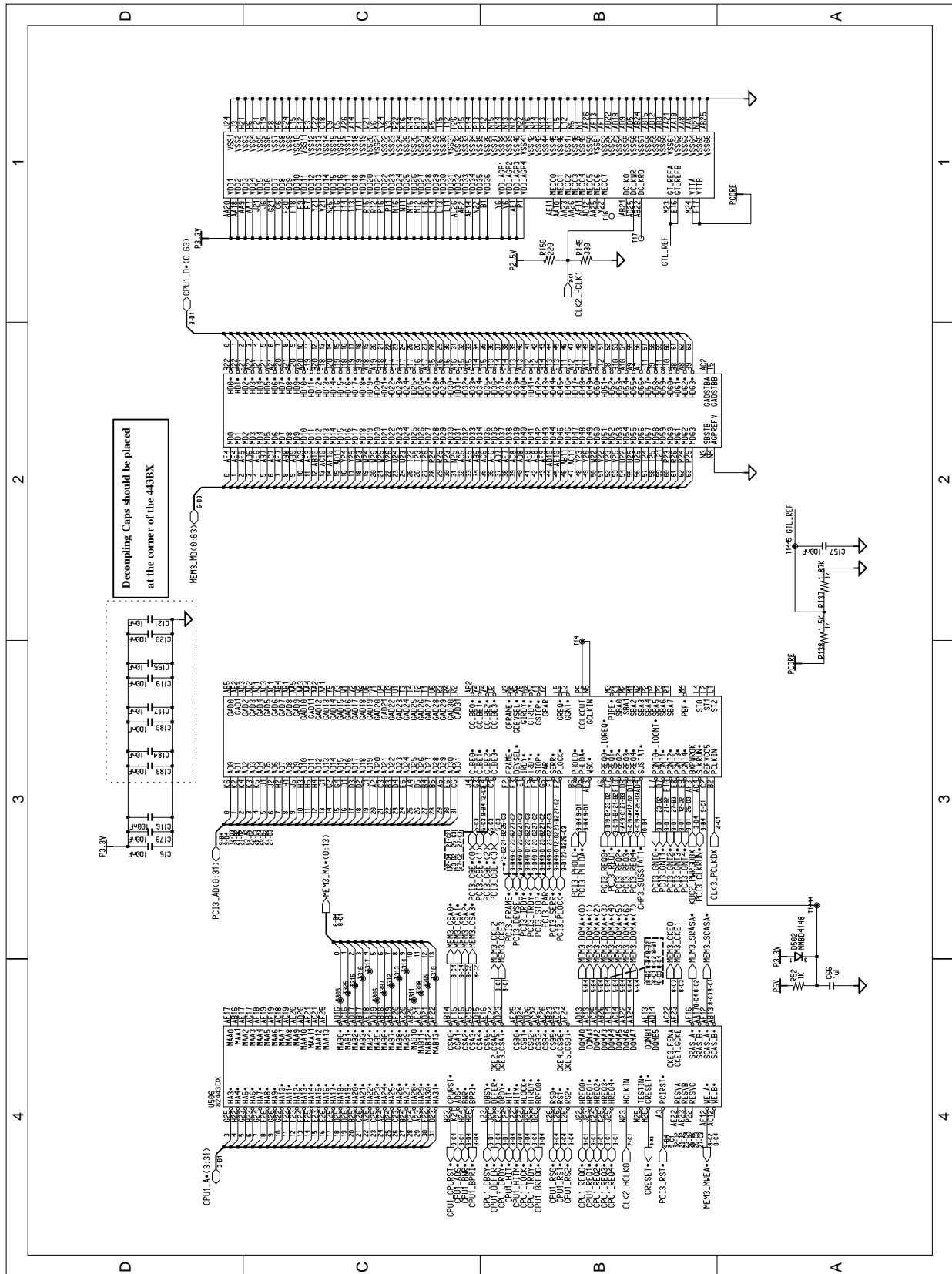
5-1-1(b) Main Board Schematic Sheet 3 of 28(PII DIXON-1)



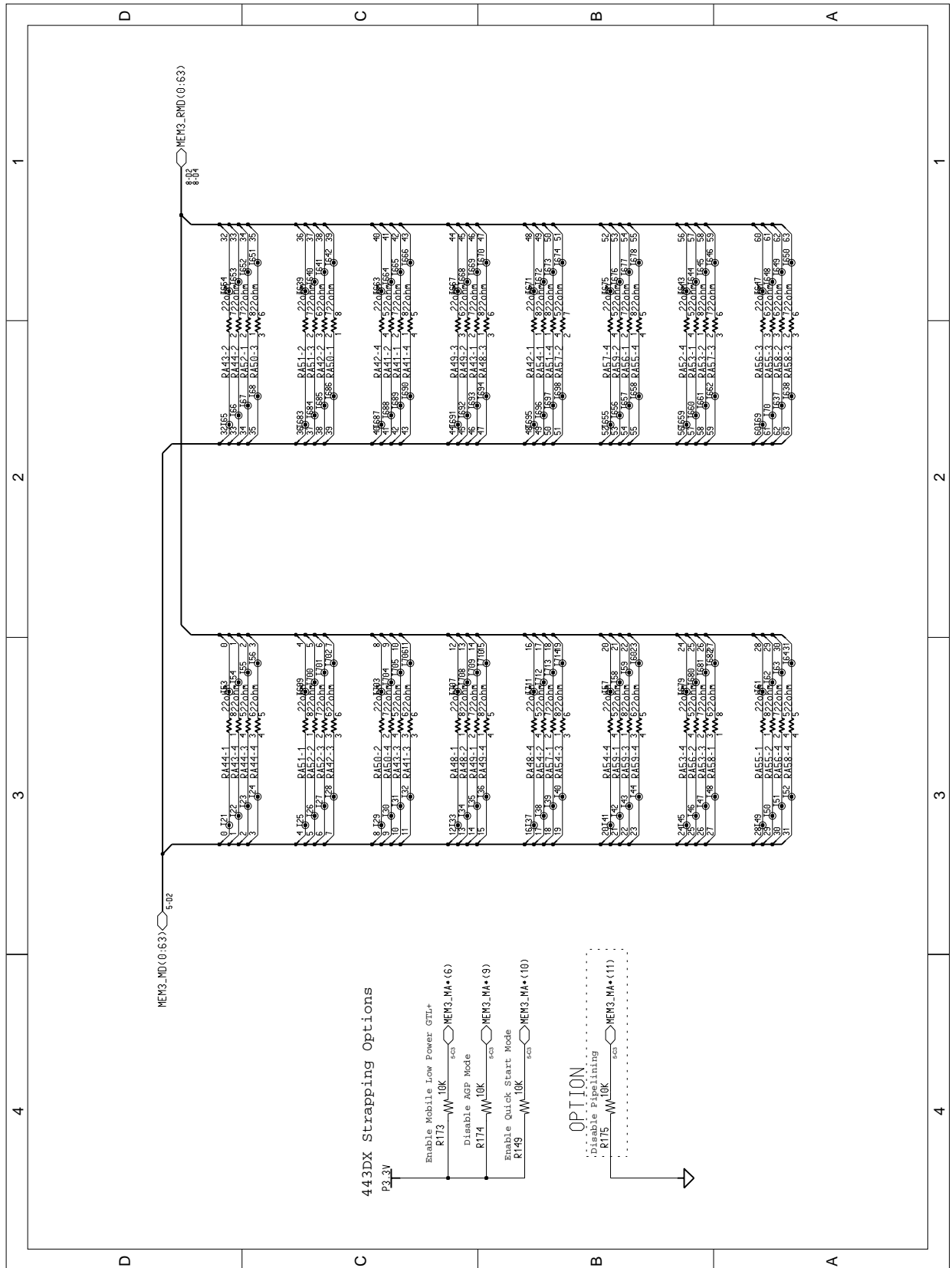
5-1-1(c) Main Board Schematic Sheet 4 of 28(PII DIXON-2)



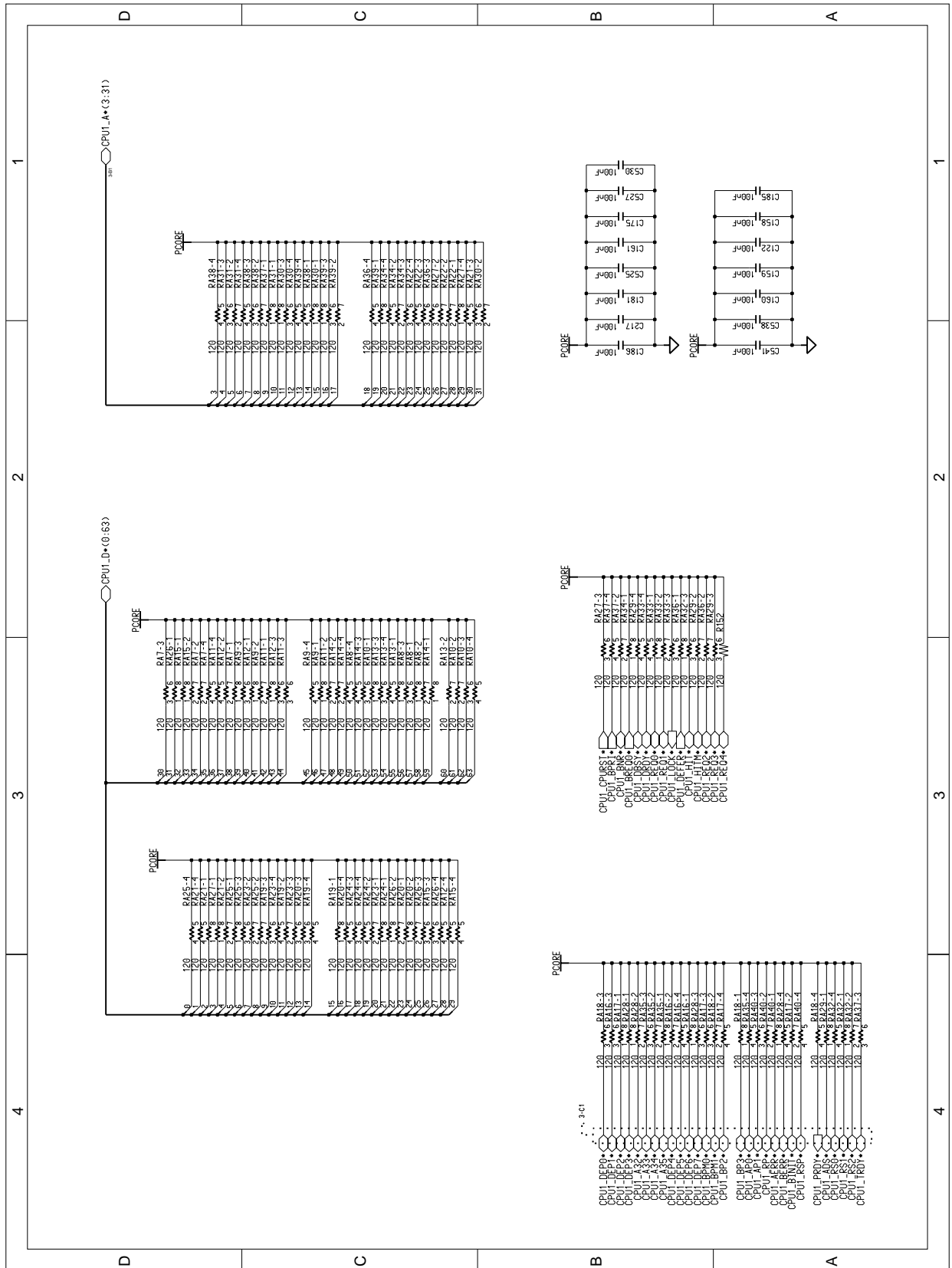
5-1-1(d) Main Board Schematic Sheet 5 of 28(NORTHBRIDGE)



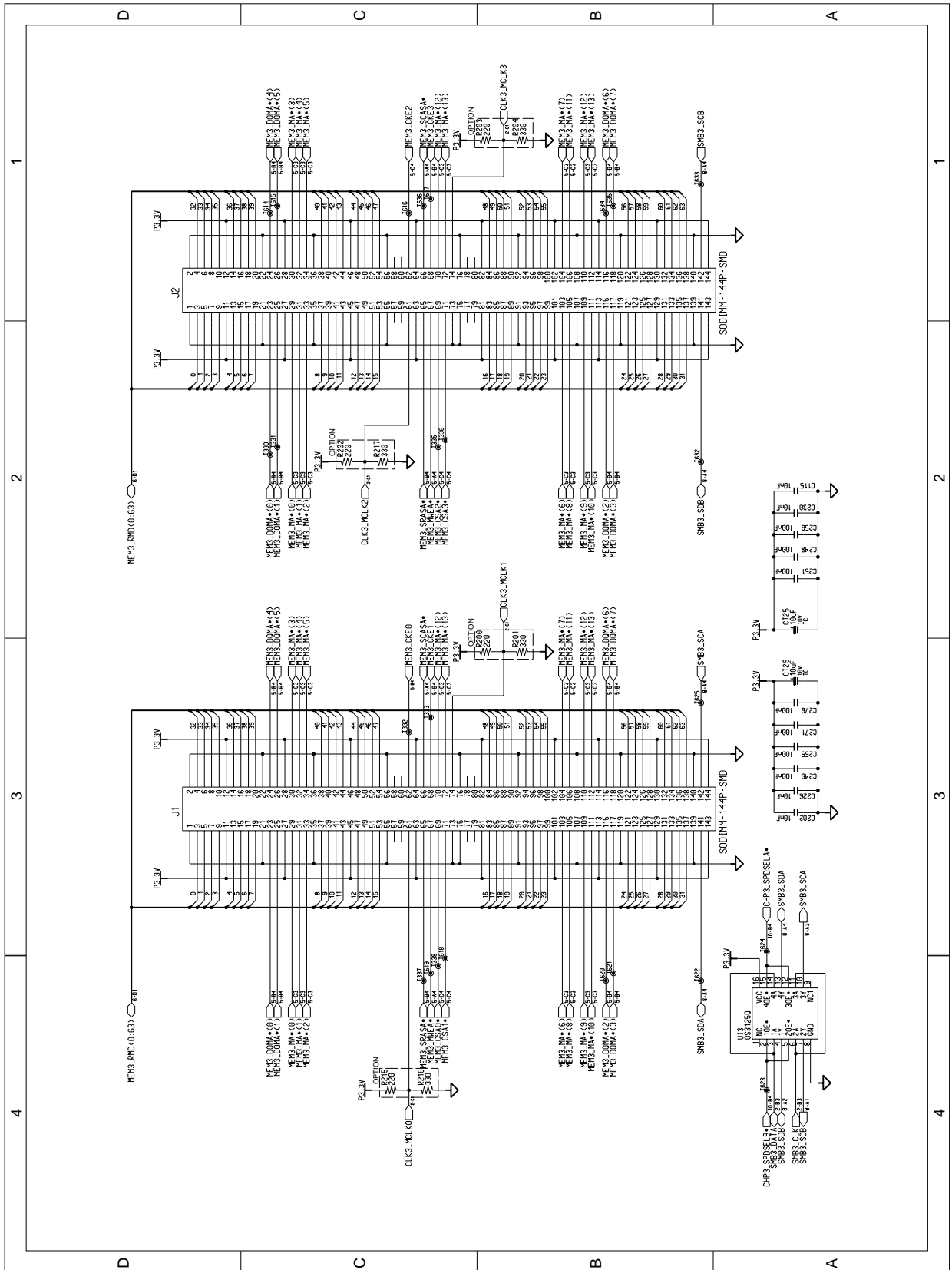
5-1-1(e) Main Board Schematic Sheet 6 of 28(Termination resistor 1)



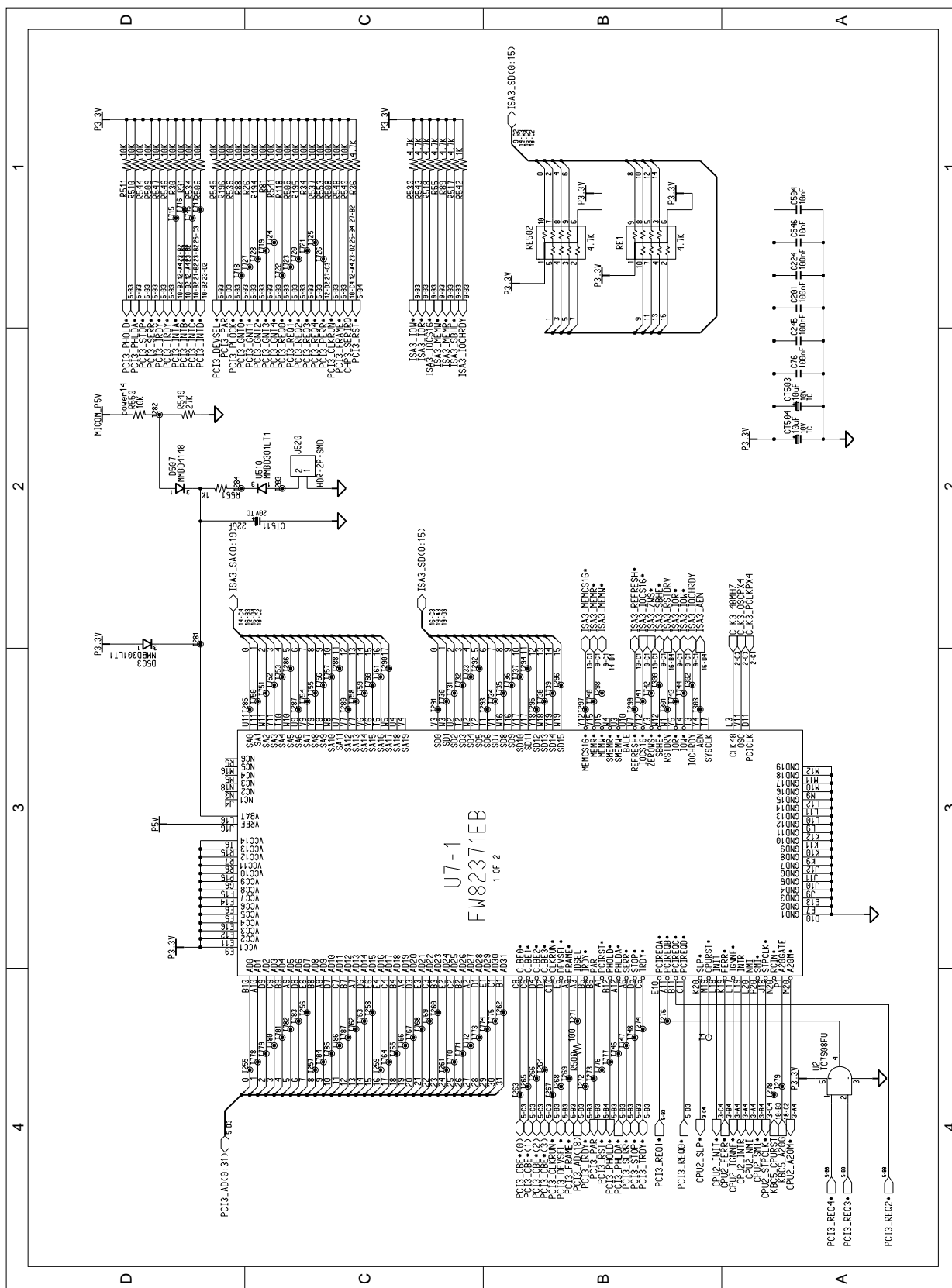
5-1-1(f) Main Board Schematic Sheet 7 of 28(Termination resistor 1)



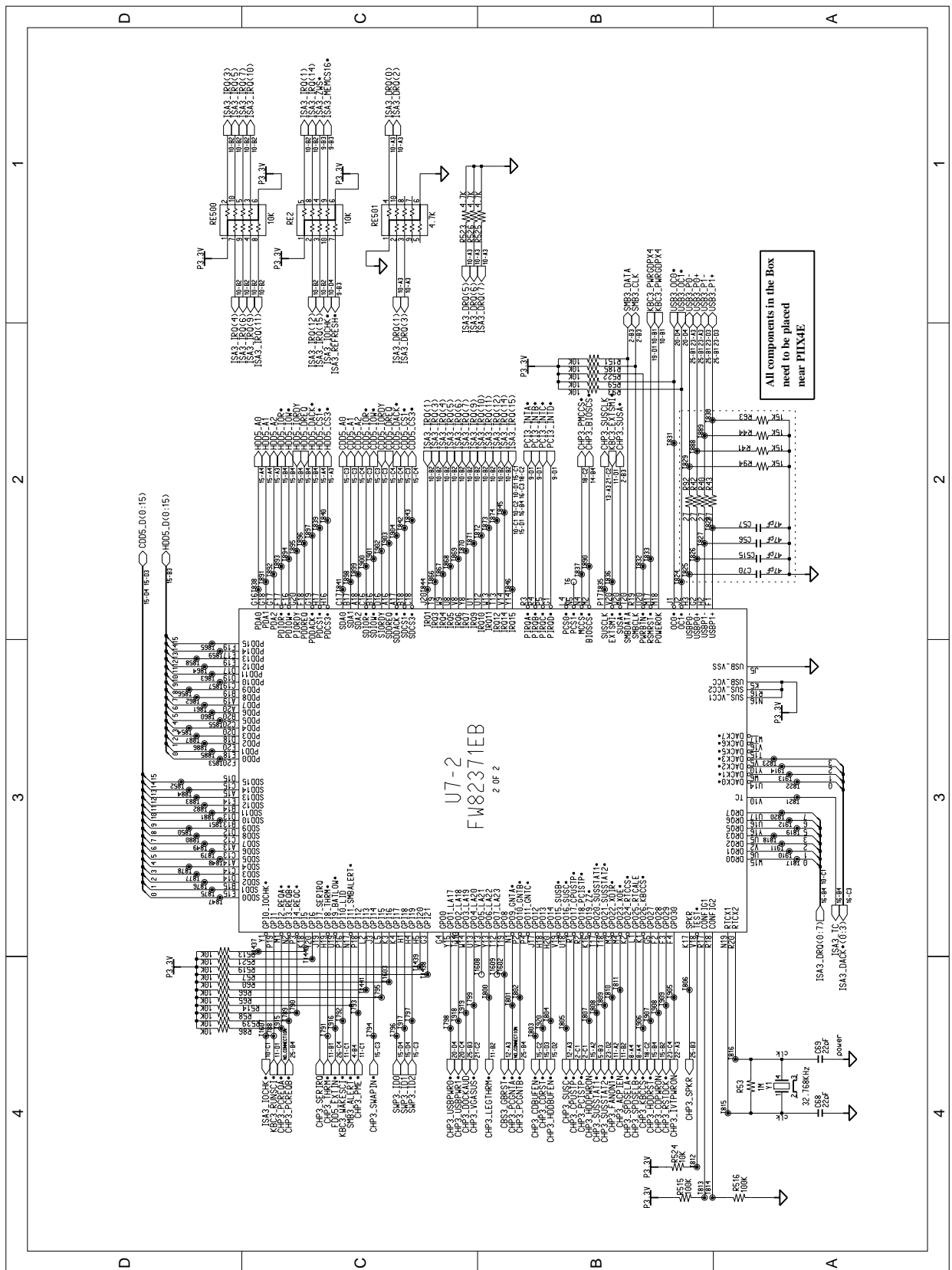
5-1-1(g) Main Board Schematic Sheet 8 of 28(SODIMM)



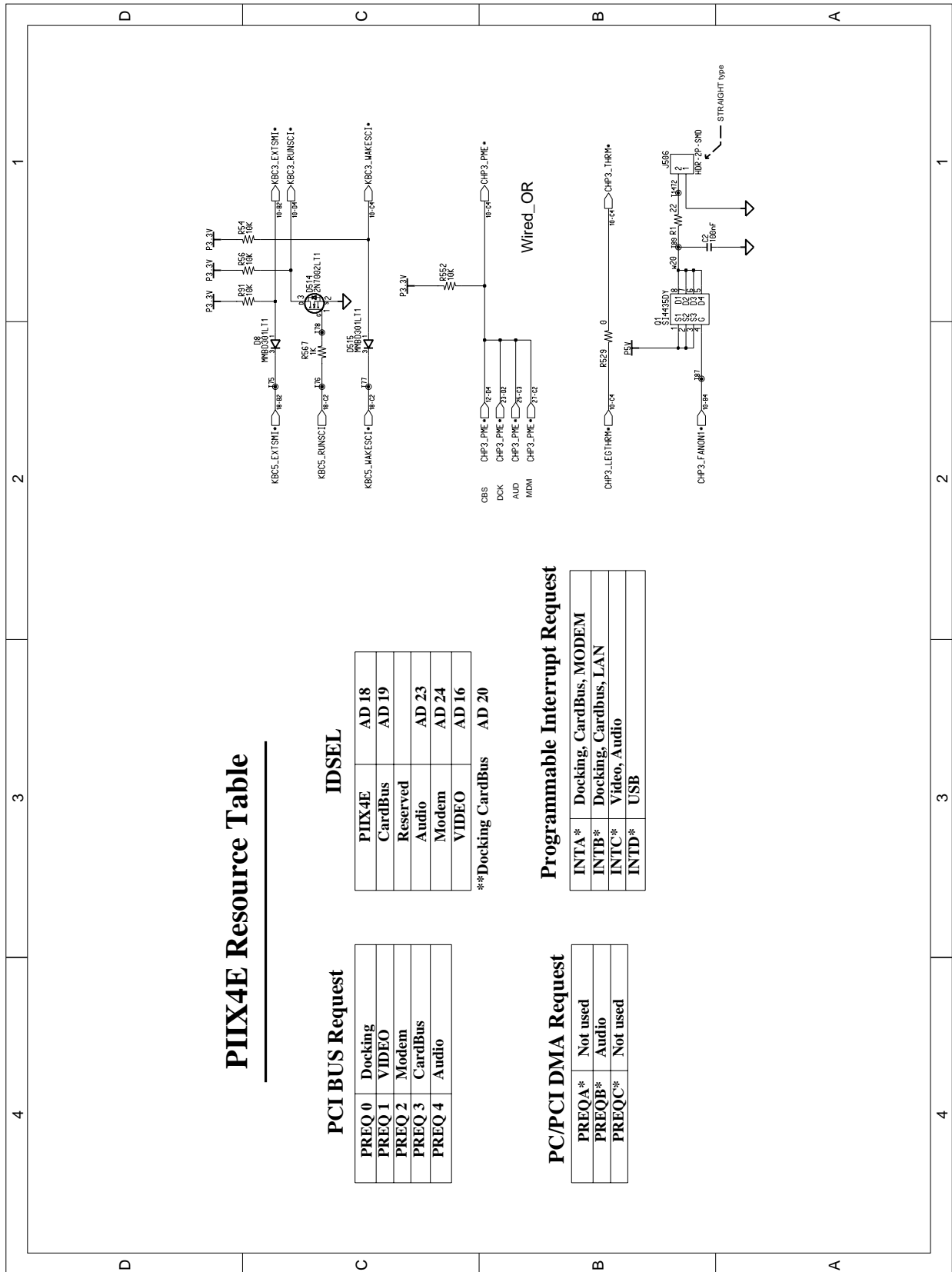
5-1-1(h) Main Board Schematic Sheet 9 of 28(PIIX4E-1)



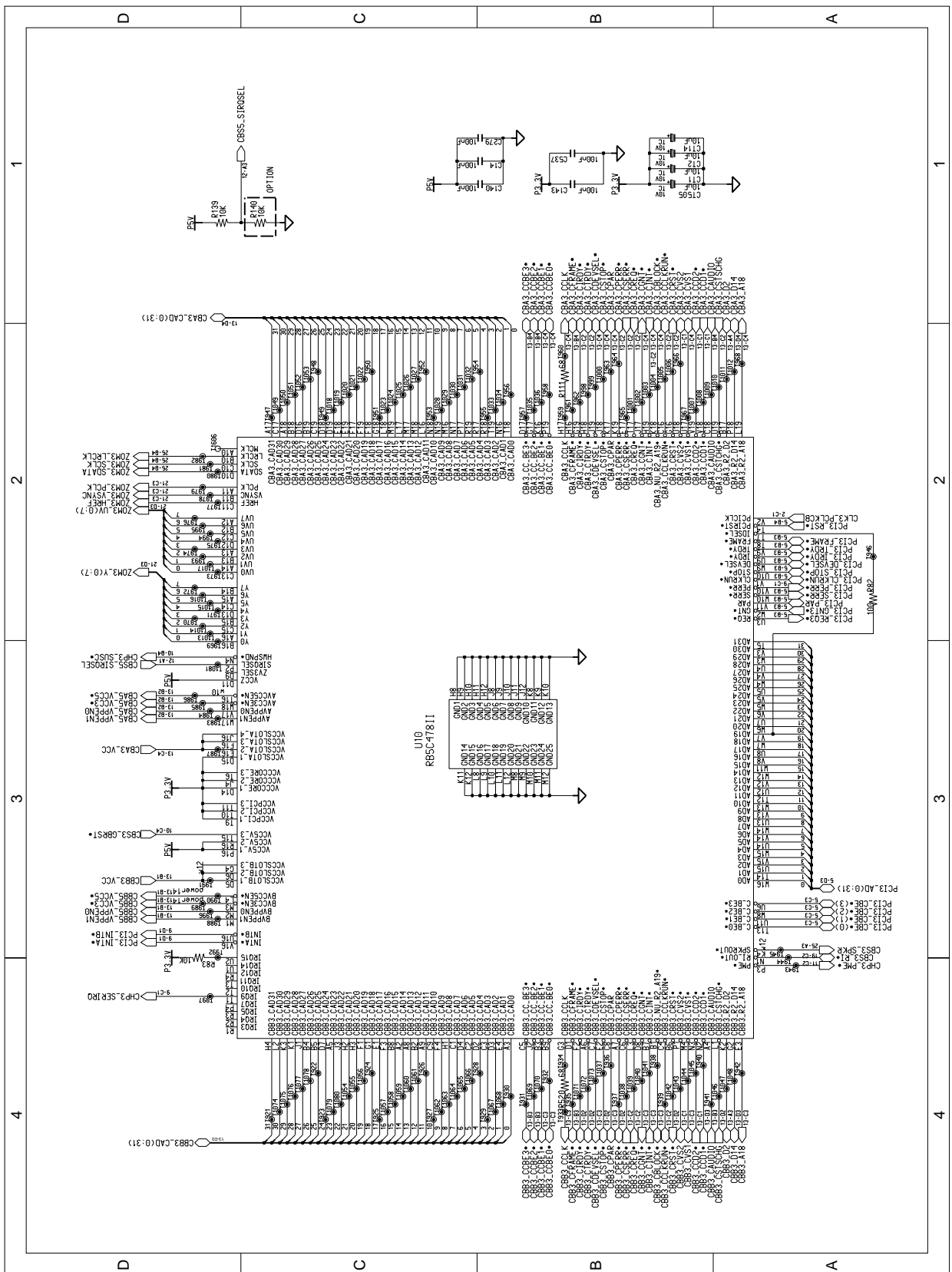
5-1-1(i) Main Board Schematic Sheet 10 of 28(PIIX4E-2)



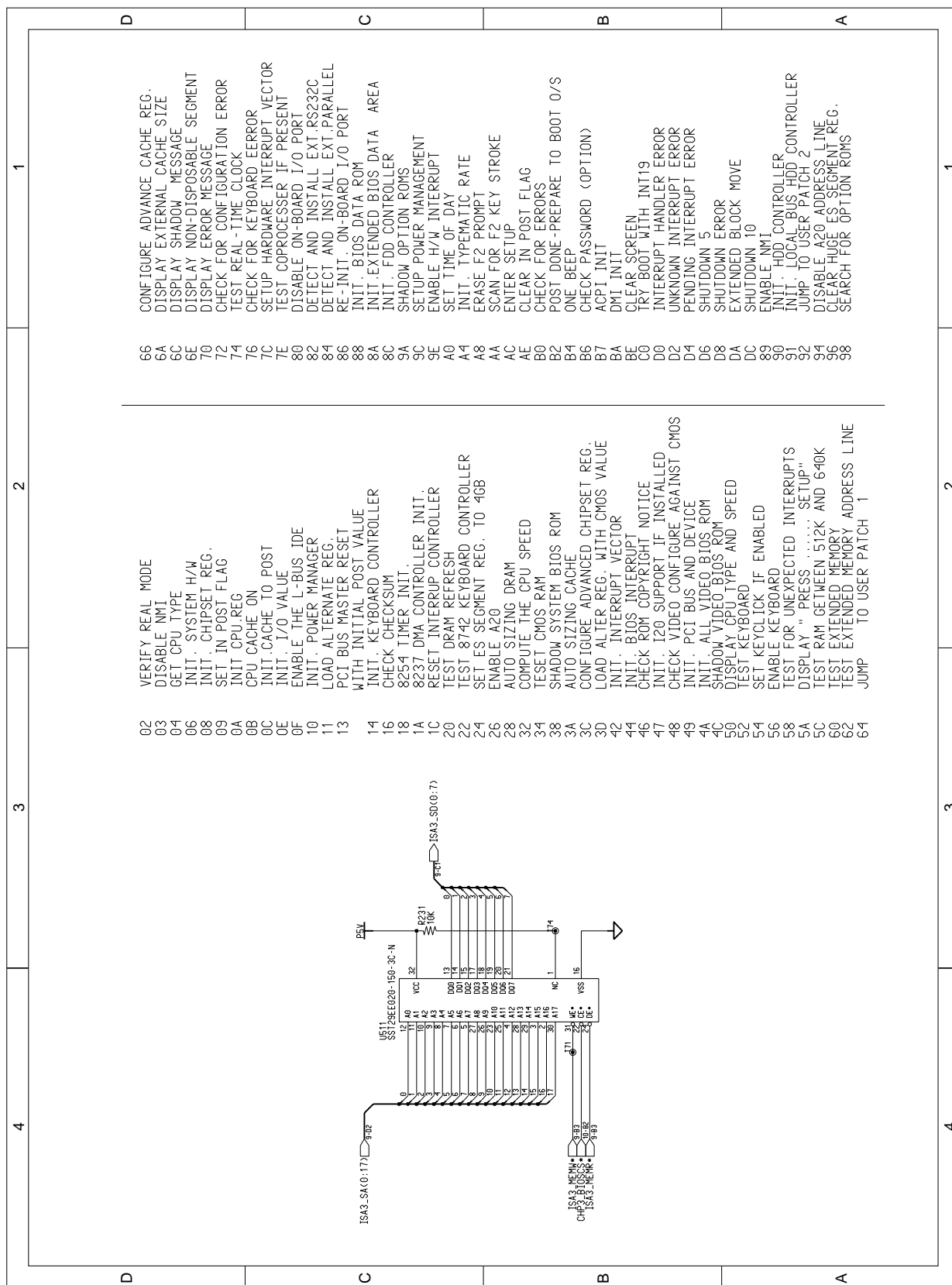
5-1-1(j) Main Board Schematic Sheet 11 of 28(PIIX4E-3)



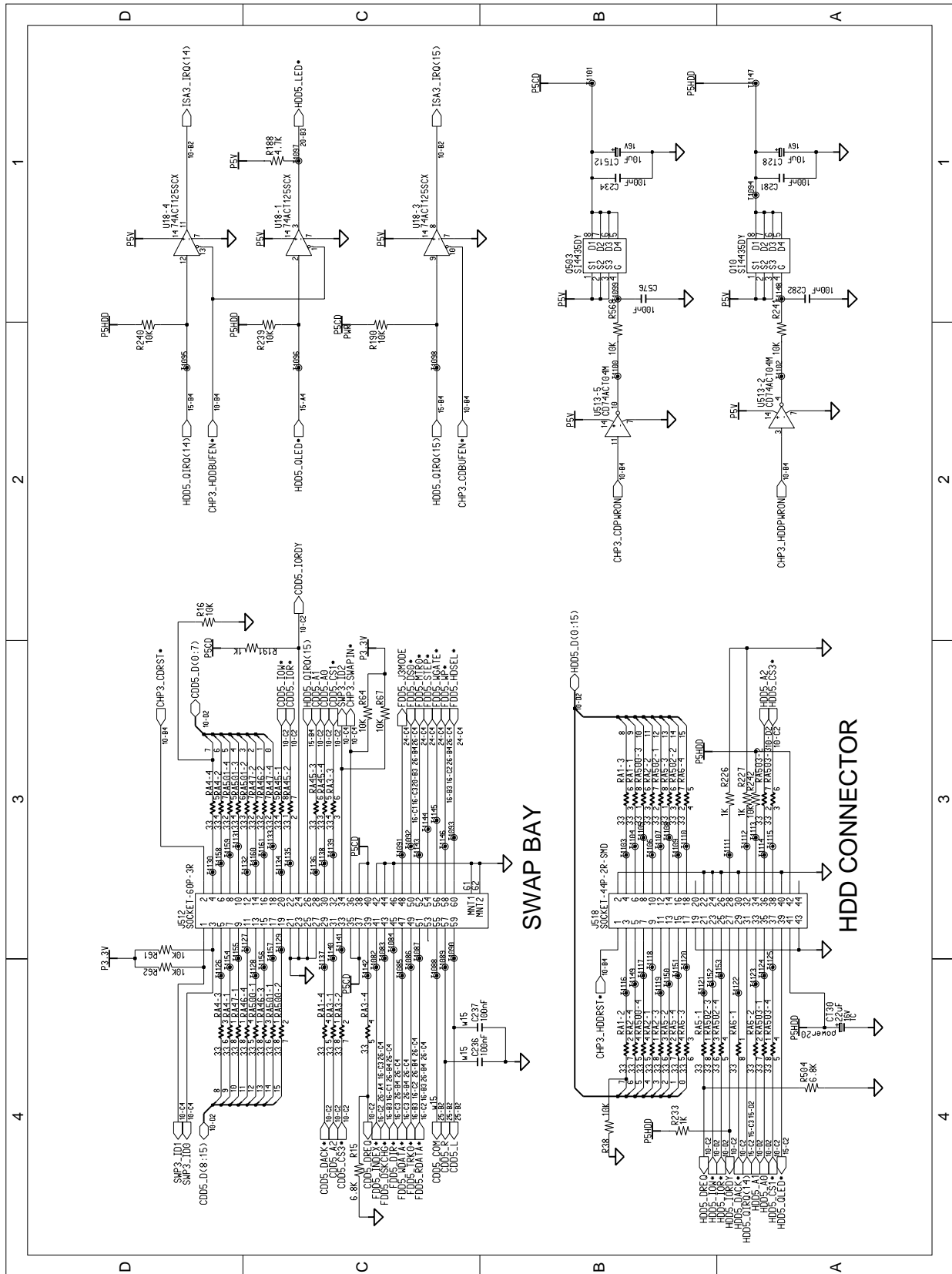
5-1-1(k) Main Board Schematic Sheet 12 of 28(CARD BUS: RB5C478II)



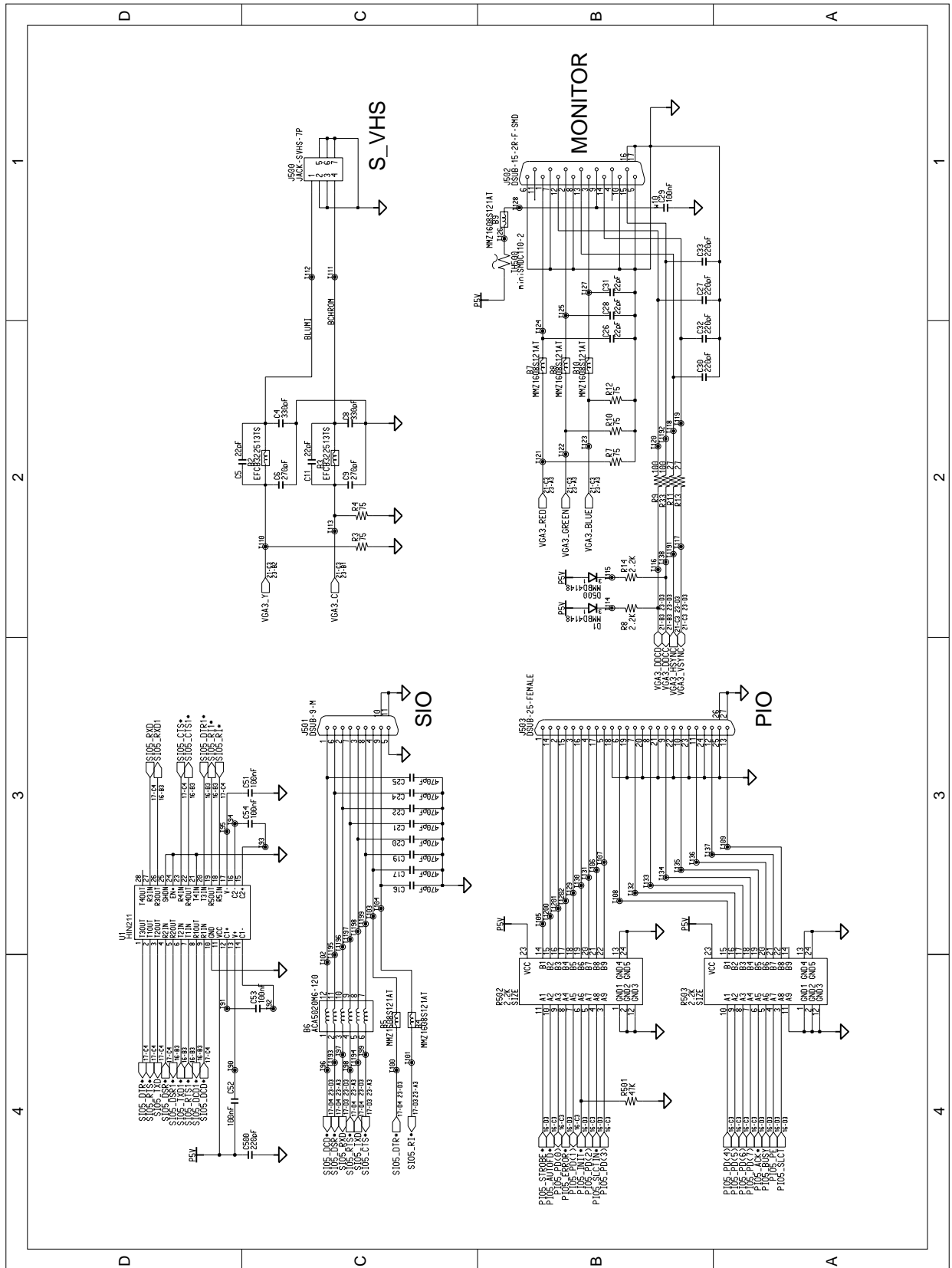
5-1-1(m) Main Board Schematic Sheet 14 of 28(BIOS)



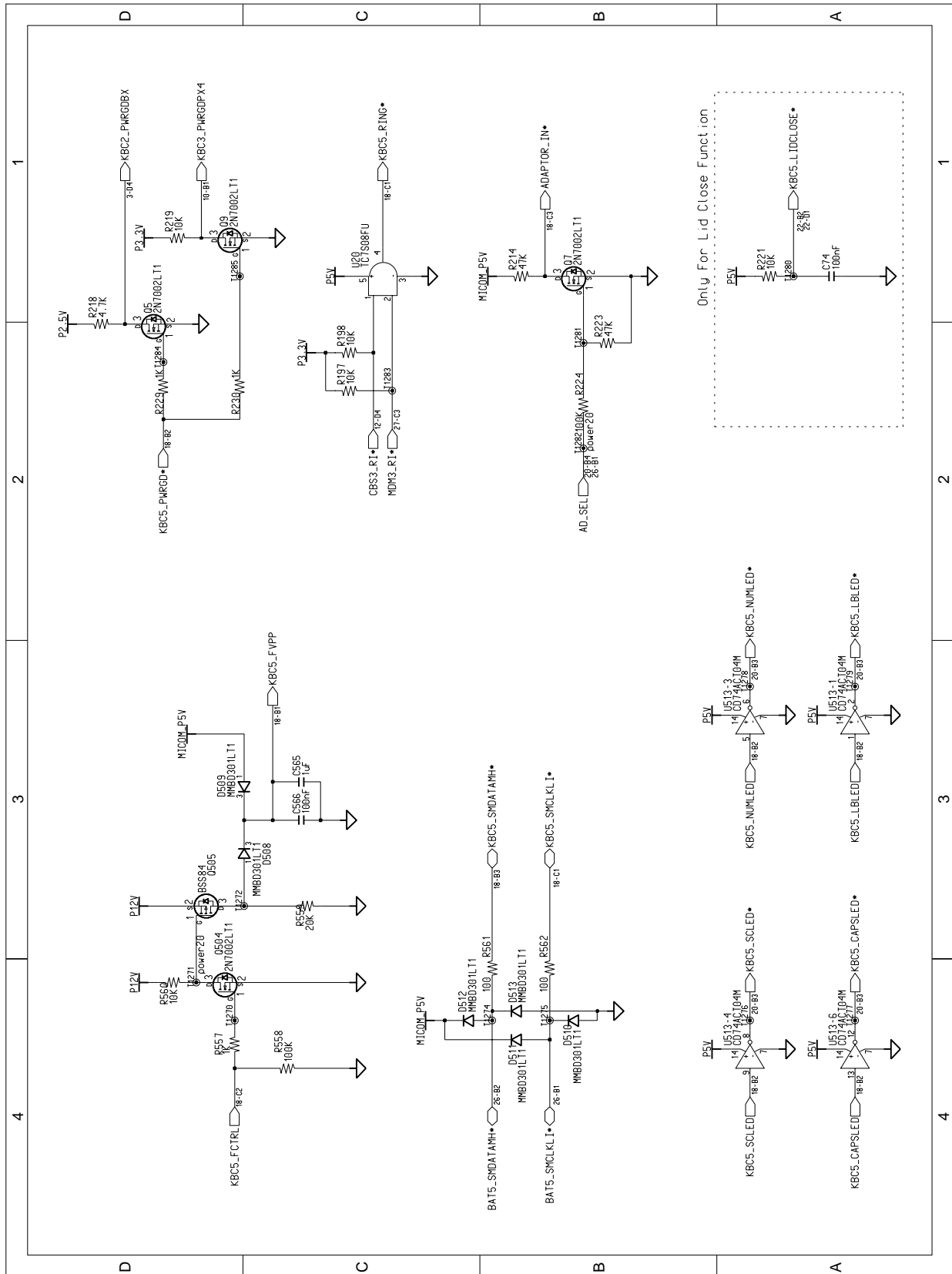
5-1-1(n) Main Board Schematic Sheet 15 of 28(HDD CONNECTOR & SWAP BAY)



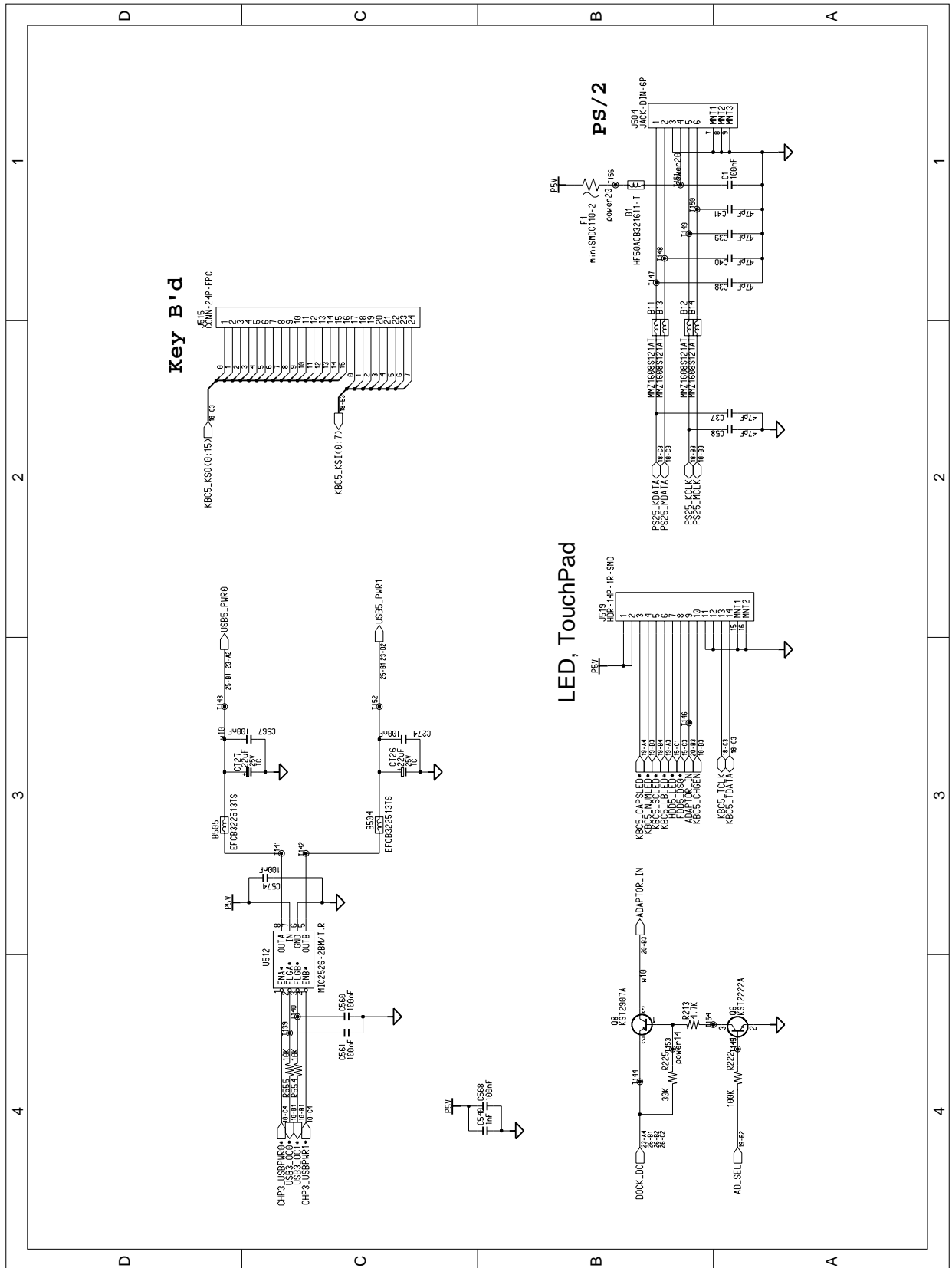
5-1-1(p) Main Board Schematic Sheet 17 of 28(PIO, SIO, CRT, S_VHS PORT)



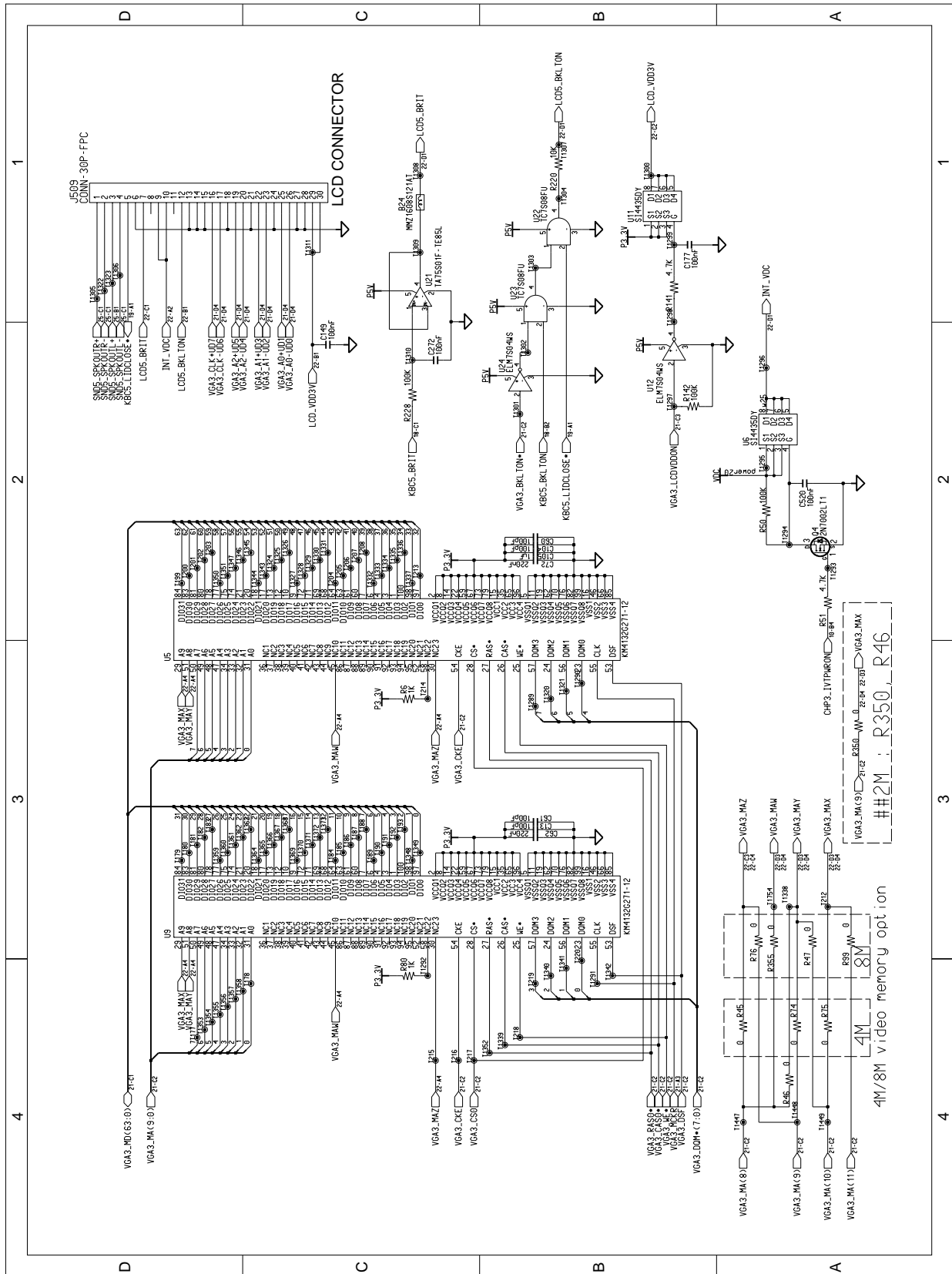
5-1-1(u) Main Board Schematic Sheet 19 of 28(MICOM 2)



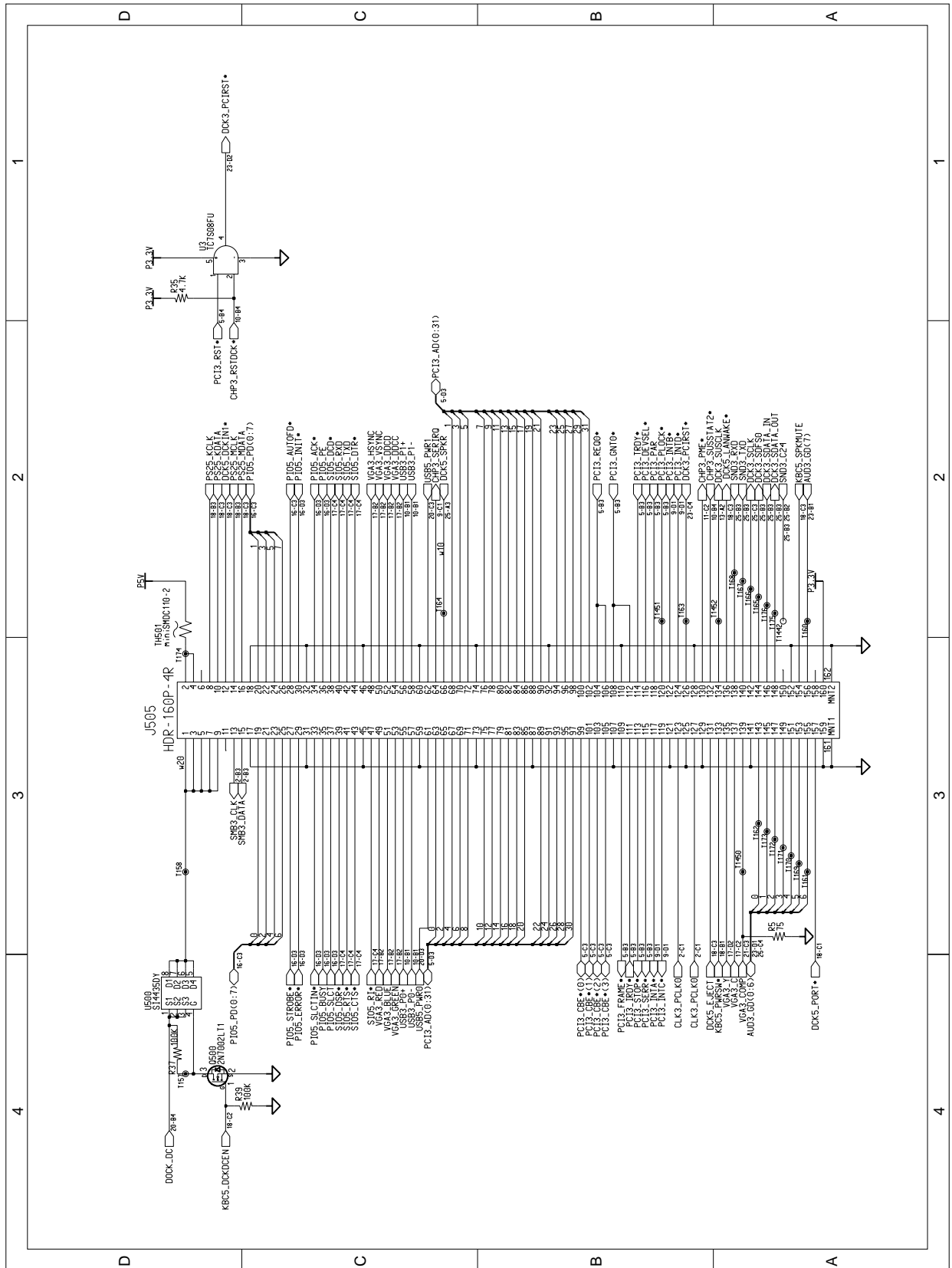
5-1-1(r) Main Board Schematic Sheet 20 of 28(USB, IR, Touchpad & Keyboard)



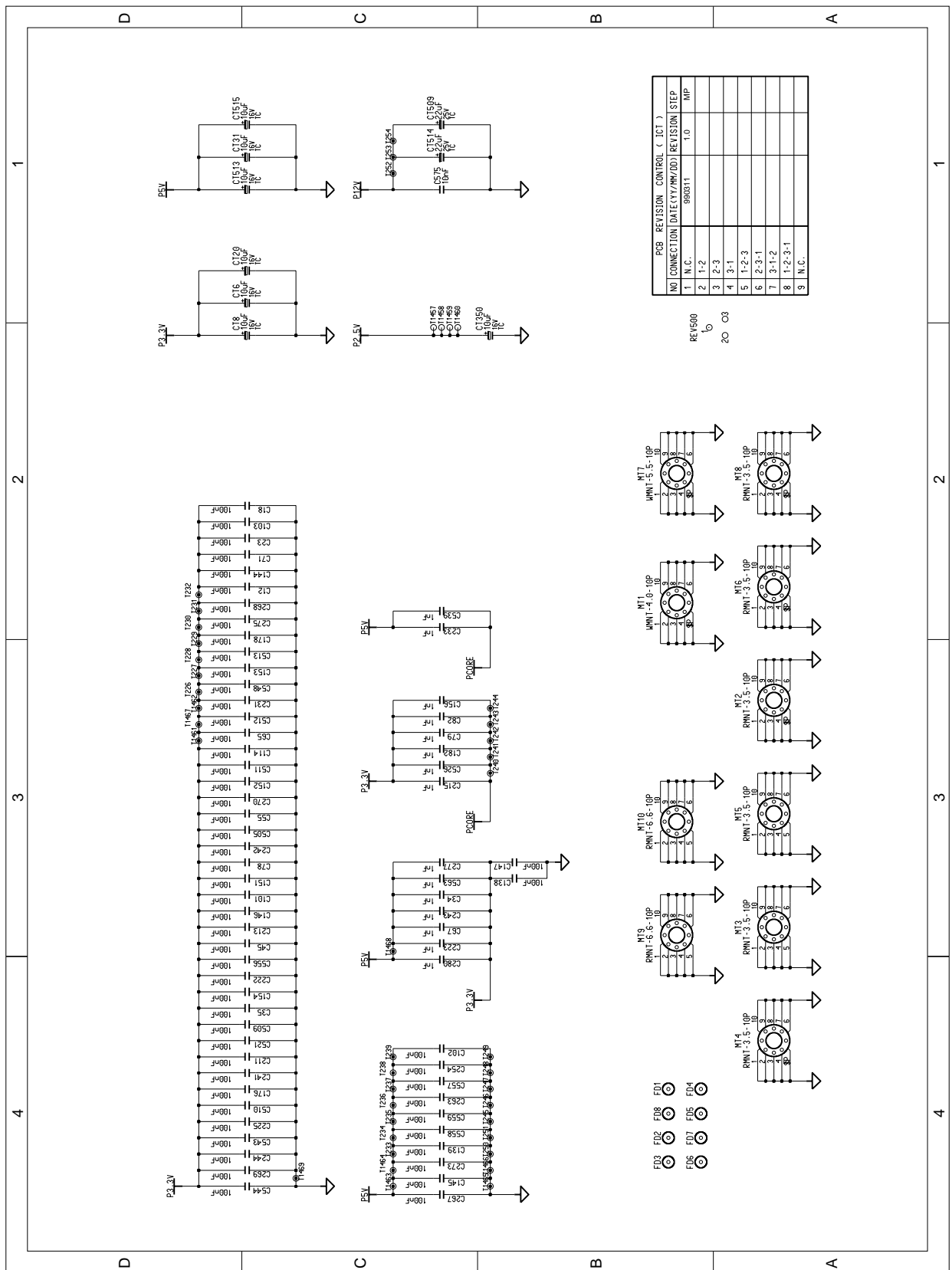
5-1-1(t) Main Board Schematic Sheet 22 of 28(SGRAM)



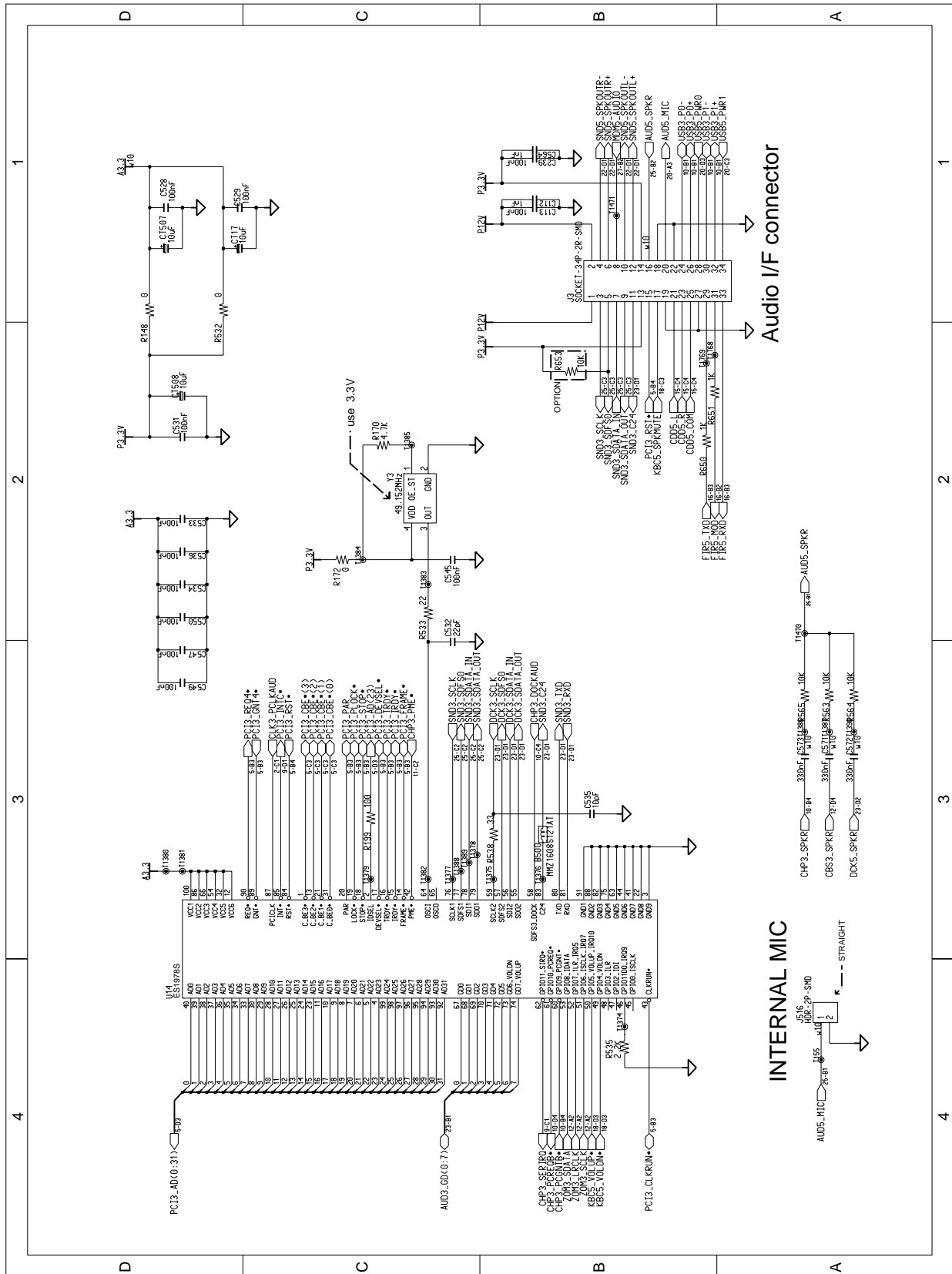
5-1-1(u) Main Board Schematic Sheet 23 of 28(DOCKING)



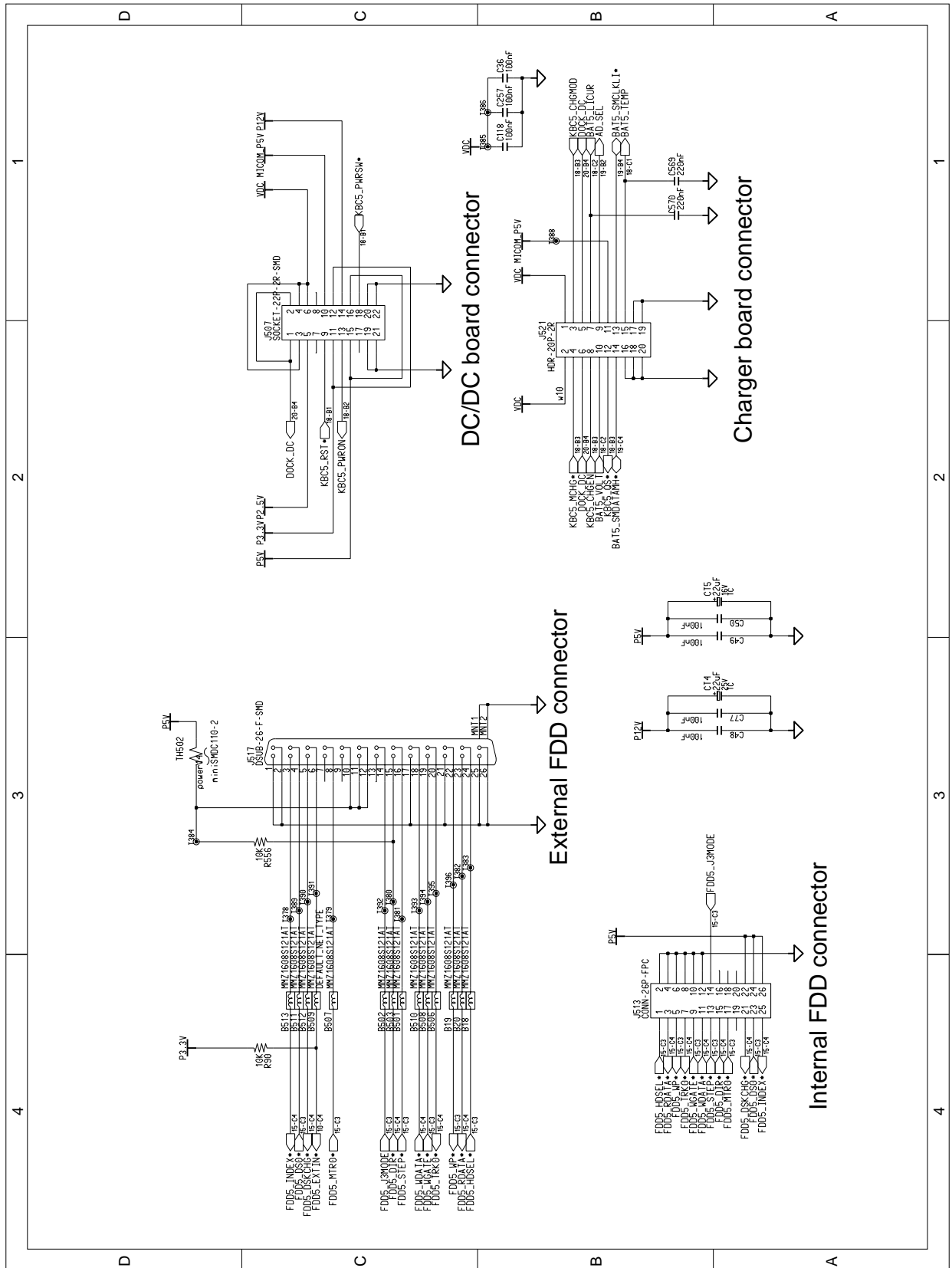
5-1-1(v) Main Board Schematic Sheet 24 of 28(Mounting Hole & CAP)



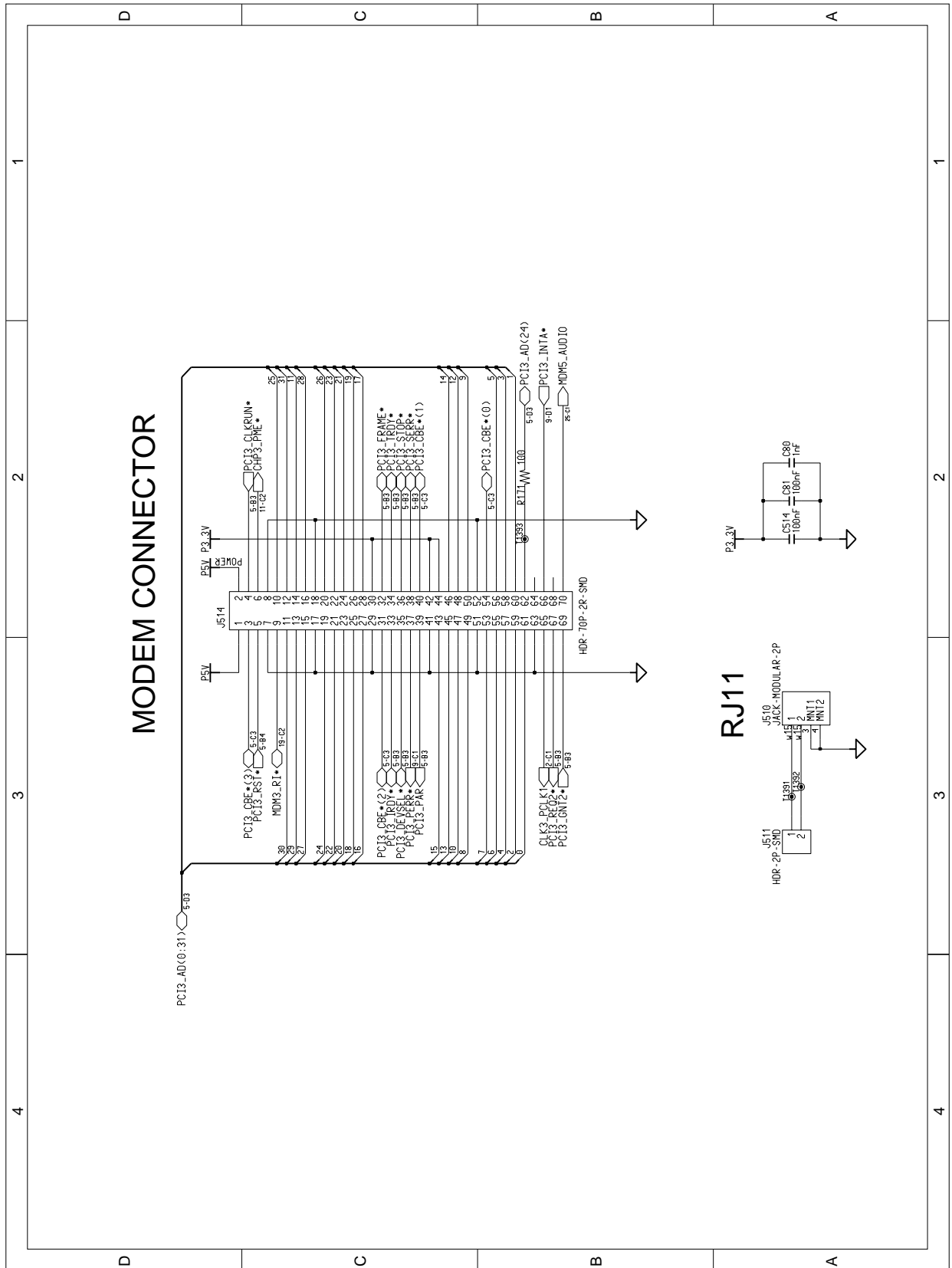
5-1-1(w) Main Board Schematic Sheet 25 of 28(AUDIO Chip & IRDA I/F)



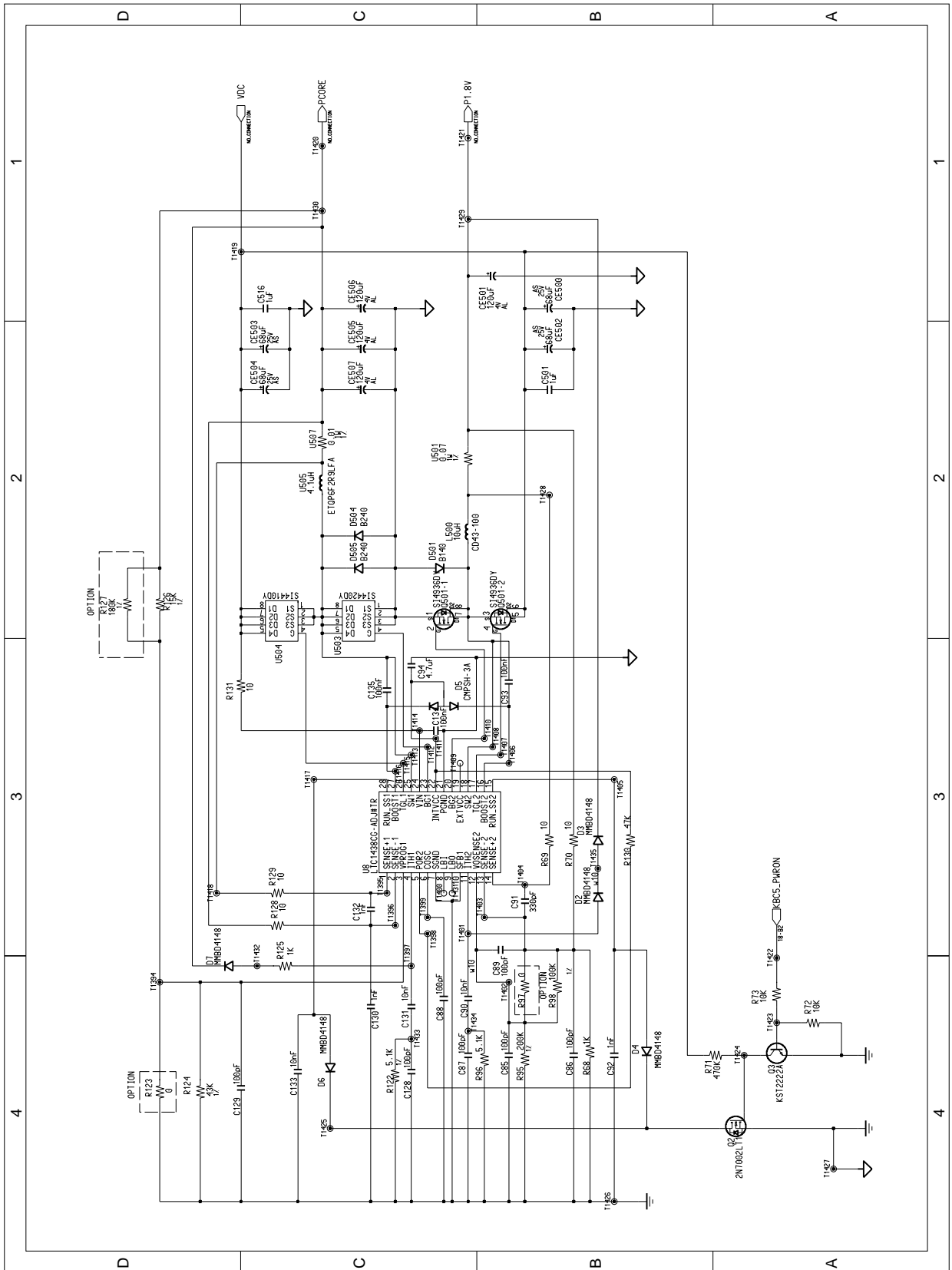
5-1-1(x) Main Board Schematic Sheet 26 of 28(POWER & FDDs CONNECTER)



5-1-1(y) Main Board Schematic Sheet 27 of 28(Modem Connector)



5-1-1(z) Main Board Schematic Sheet 28 of 28(CPU_POWER)



5-2 Audio Board

5-2-1 Schematic Diagrams

SONIC AUDIO

CPU :
 Chip Set :
 Remarks :

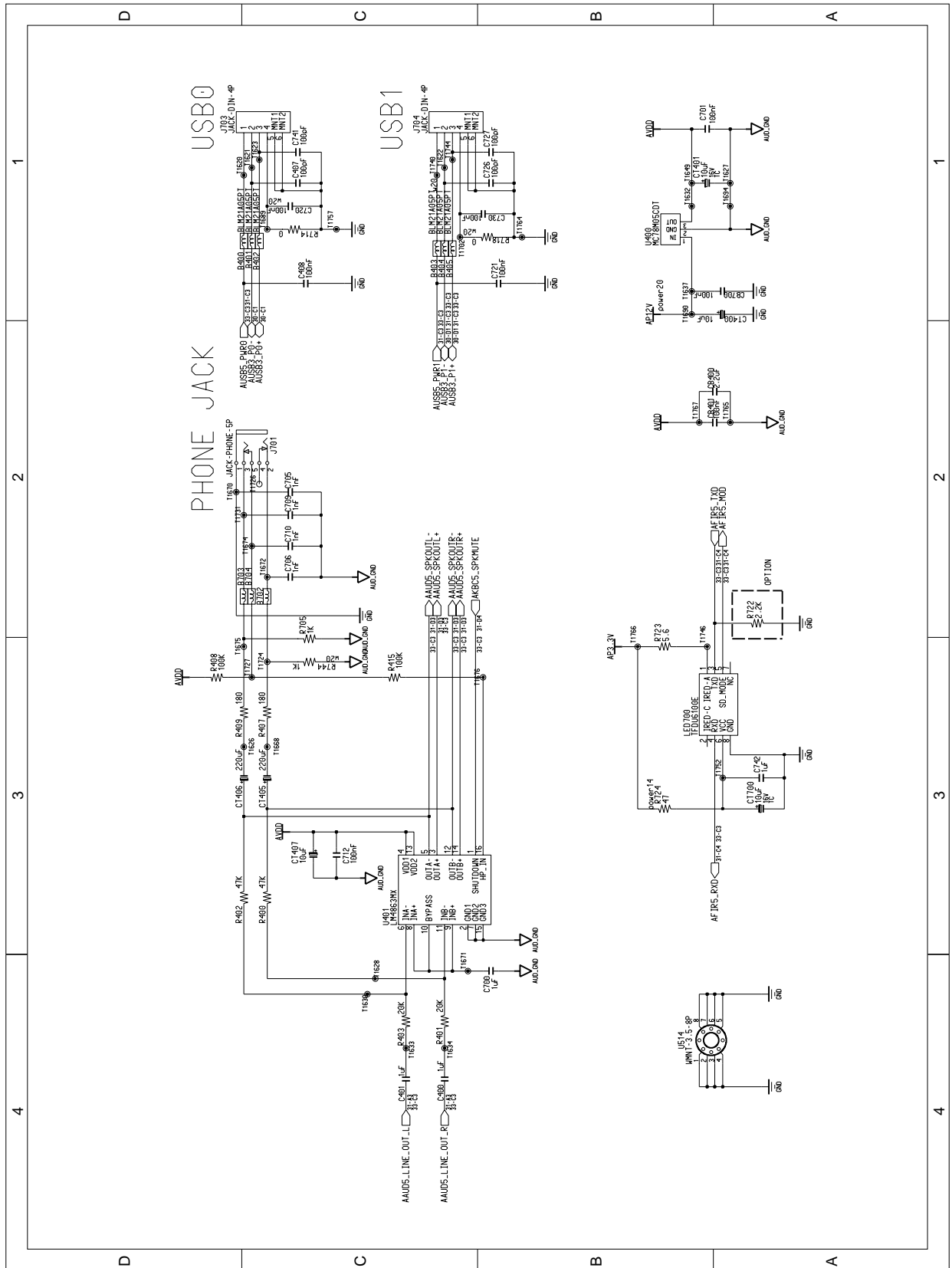
Model Name : S630/S670
 PBA Name :
 Ass'y Code : BA93-10071Y
 Dev. Step : MP
 Revision : 1.0
 T.R. Date : 1999.03.11.

DRAW	CHECK	APPROVAL

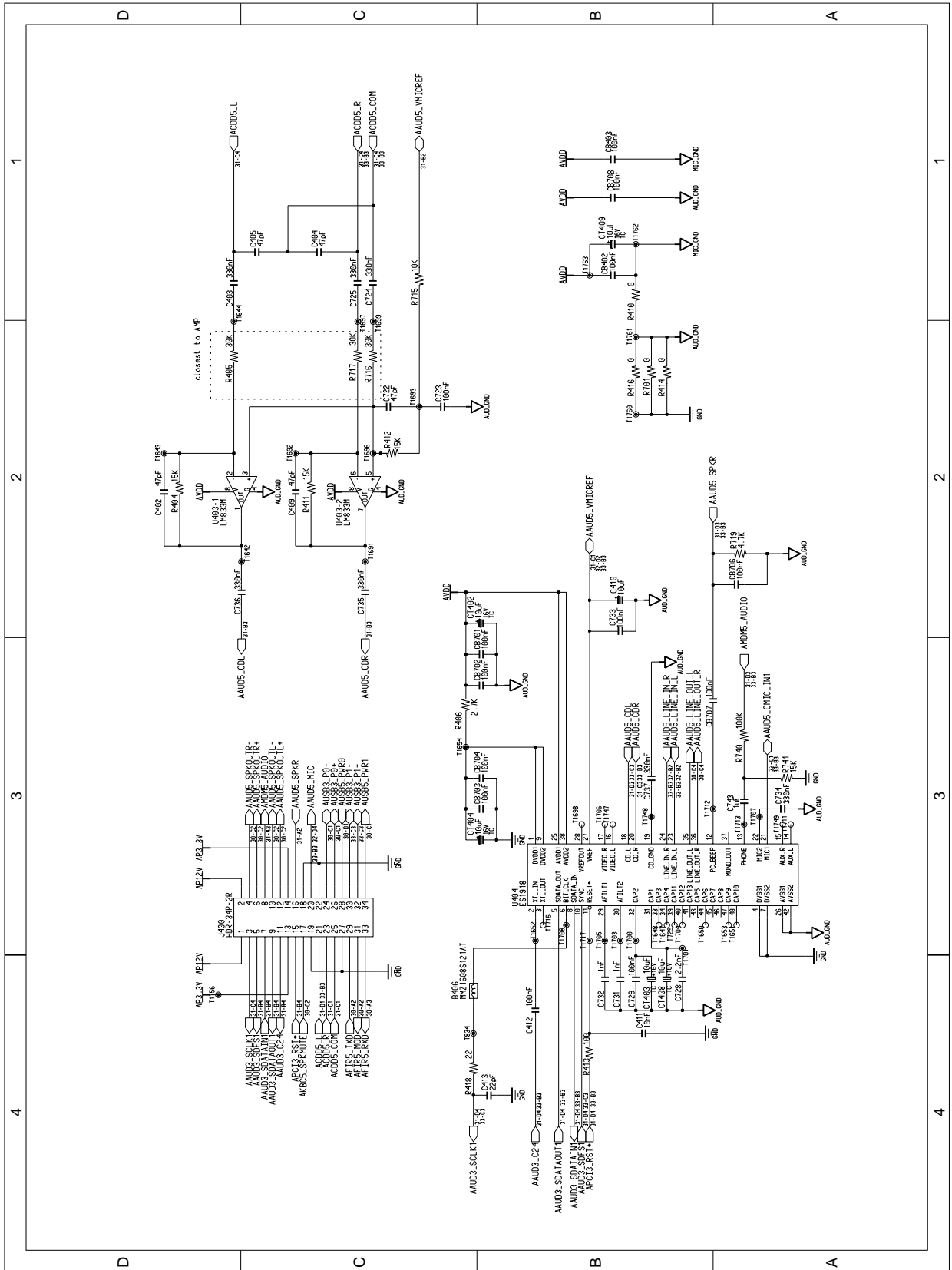
Table of Contents

1. Cover --(sheet29)
2. USB JACK & FIR --(sheet30)
3. AUDIO CODEC --(sheet31)
4. MIC JACK & LINE IN JACK --(sheet32)
5. TEST POINT --(sheet33)

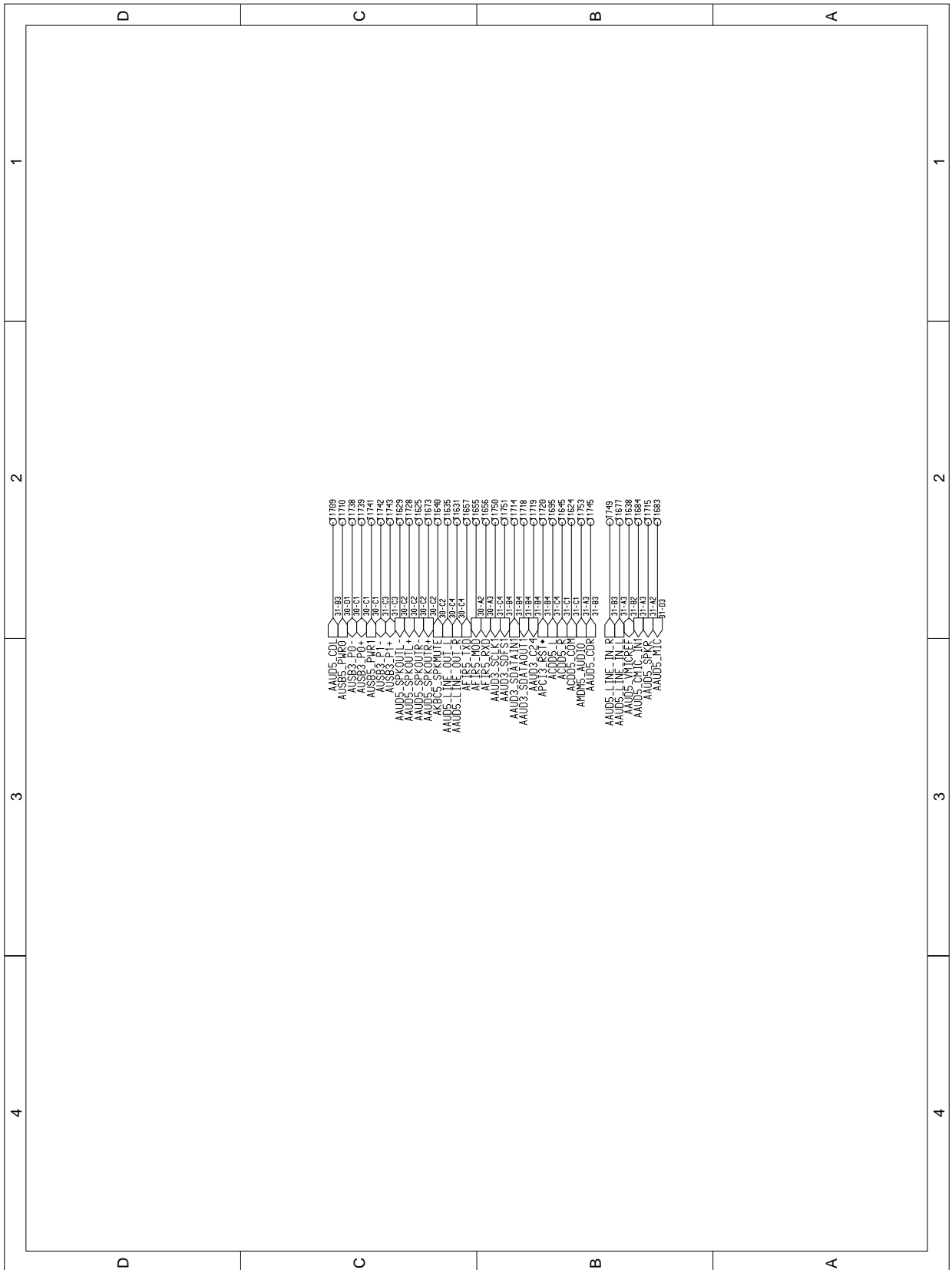
5-2-1(a) Audio Board Schematic Sheet 2 of 5(USB JACK & FIR)



5-2-1(b) Audio Board Schematic Sheet 3 of 5(AUDIO CODEC)



5-2-1(d) Audio Board Schematic Sheet 5 of 5(TEST POINT)



5-3 Modem Board

5-3-1 Schematic Diagrams

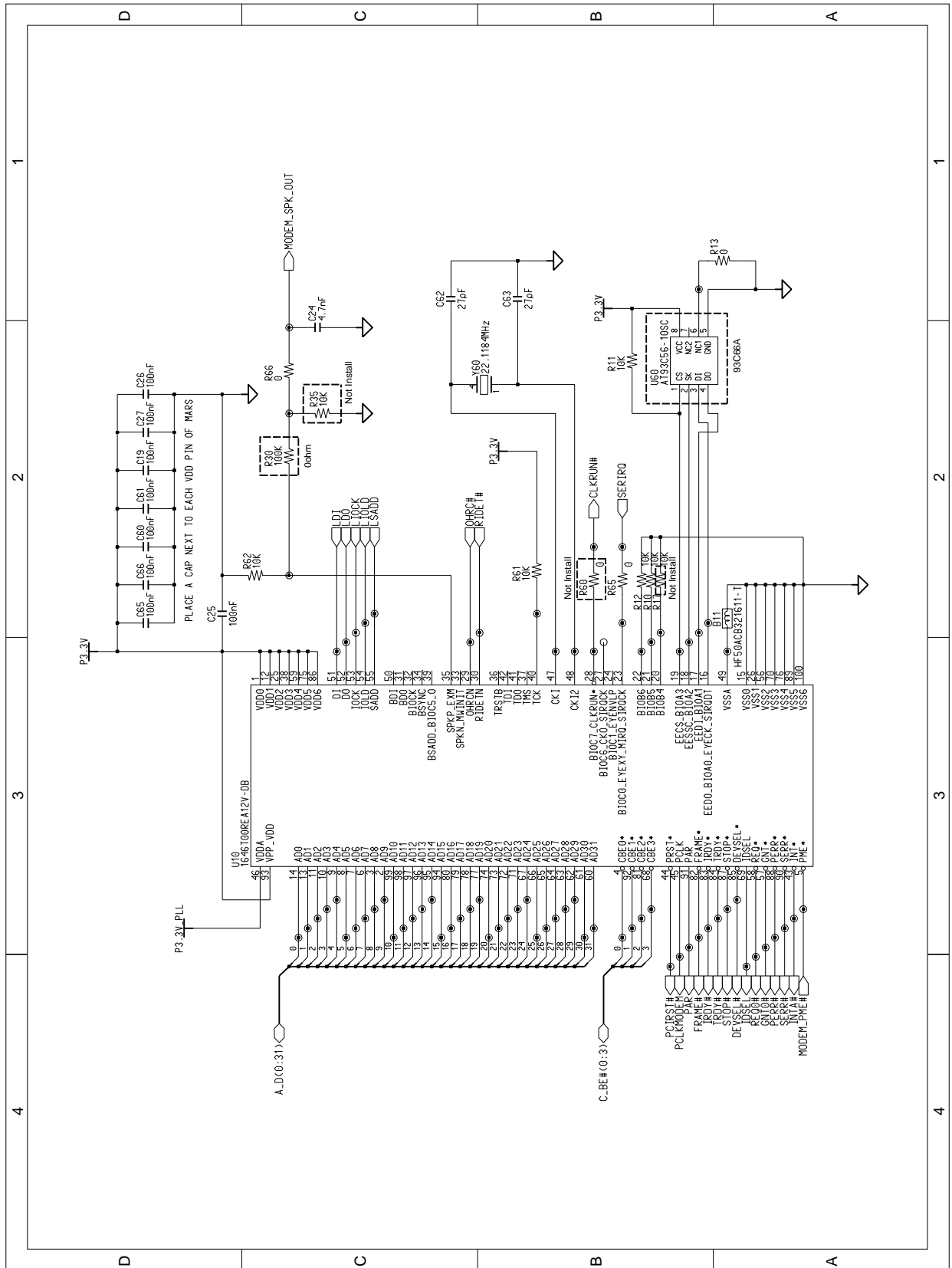
T-shot

CPU : Lucent DSP1646
 Chip Set : Krypton DAA Chipset
 Remarks :

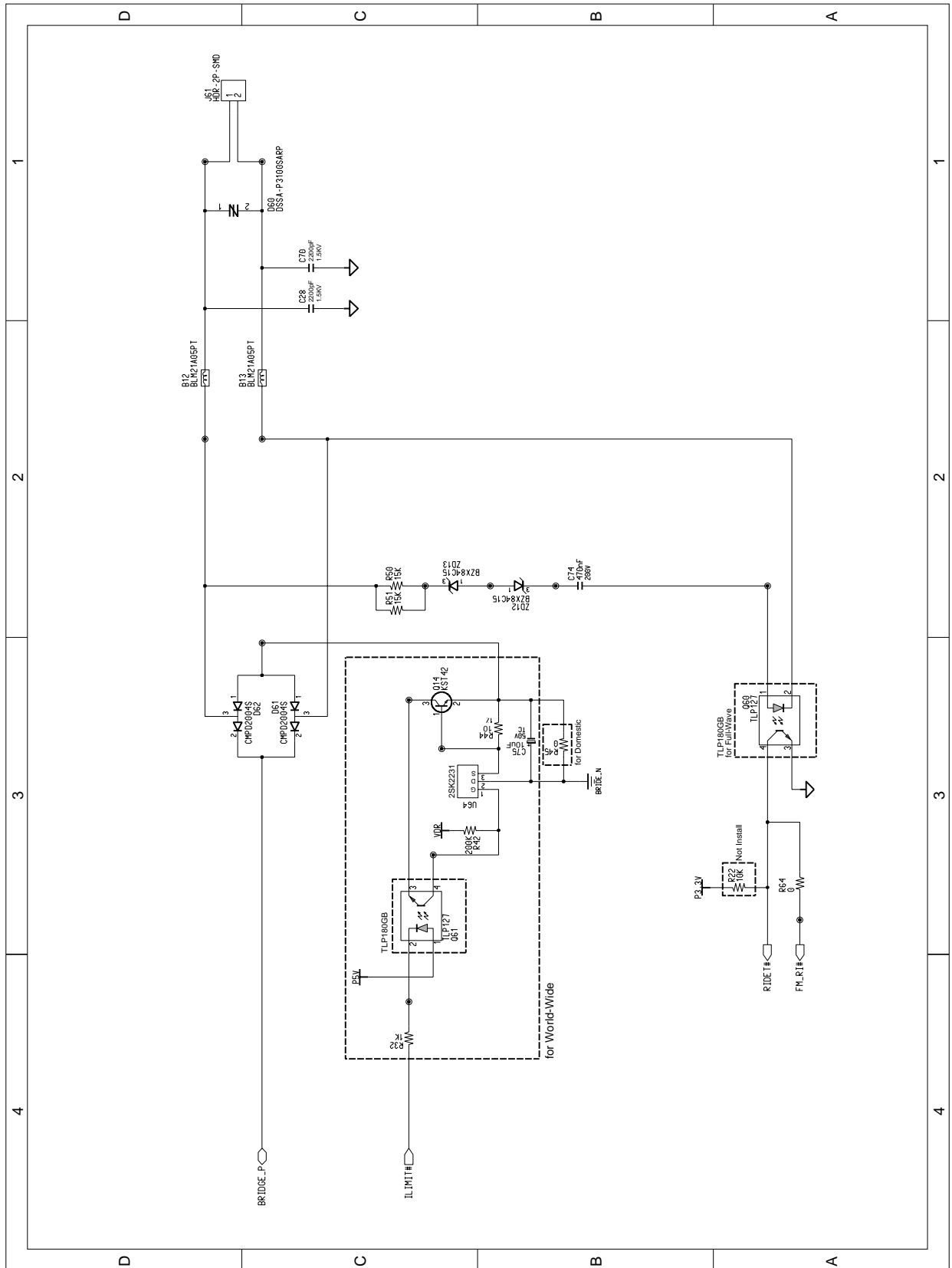
Model Name : SFM-1400IW
 PBA Name :
 PCB Code : BA41-00037A
 Dev. Step : MP
 Revision : 1.0
 T.R. Date : 1999/02/24

DRAW	CHECK	APPROVAL

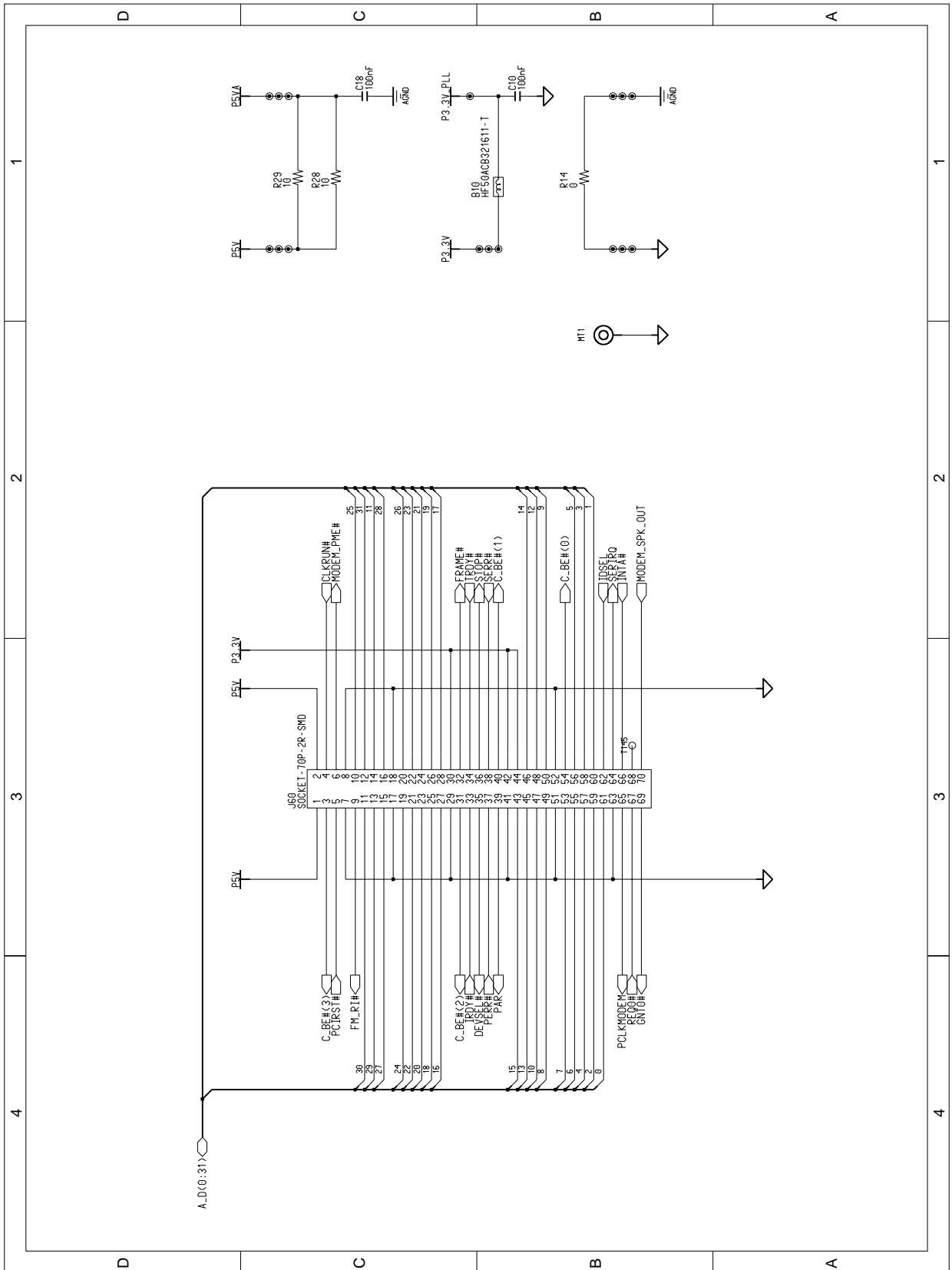
5-3-1(a) Modem Board Schematic Sheet 2 of 5(USB JACK & FIR)



5-3-1(c) Modem Board Schematic Sheet 4 of 5(USB JACK & FIR)

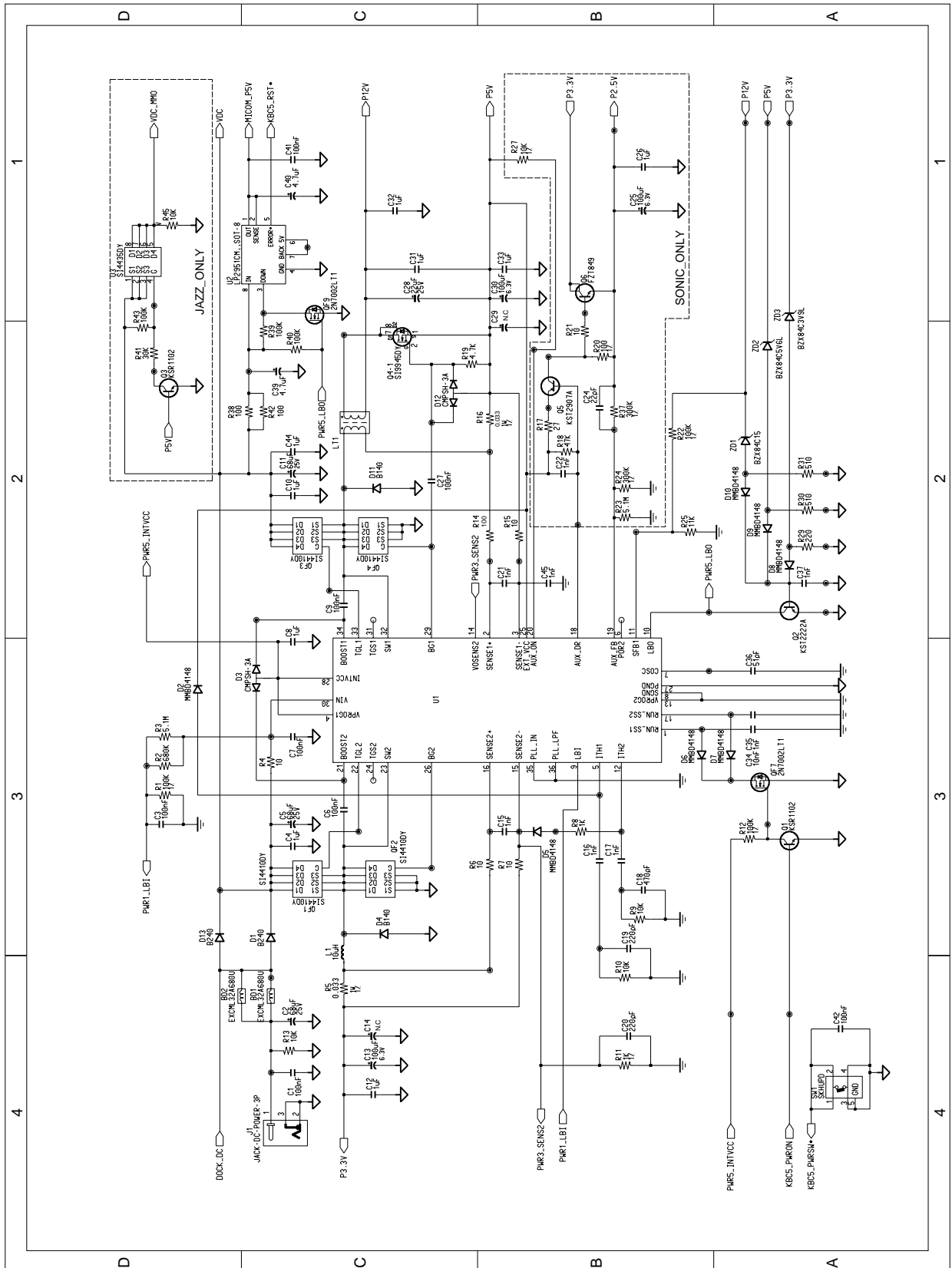


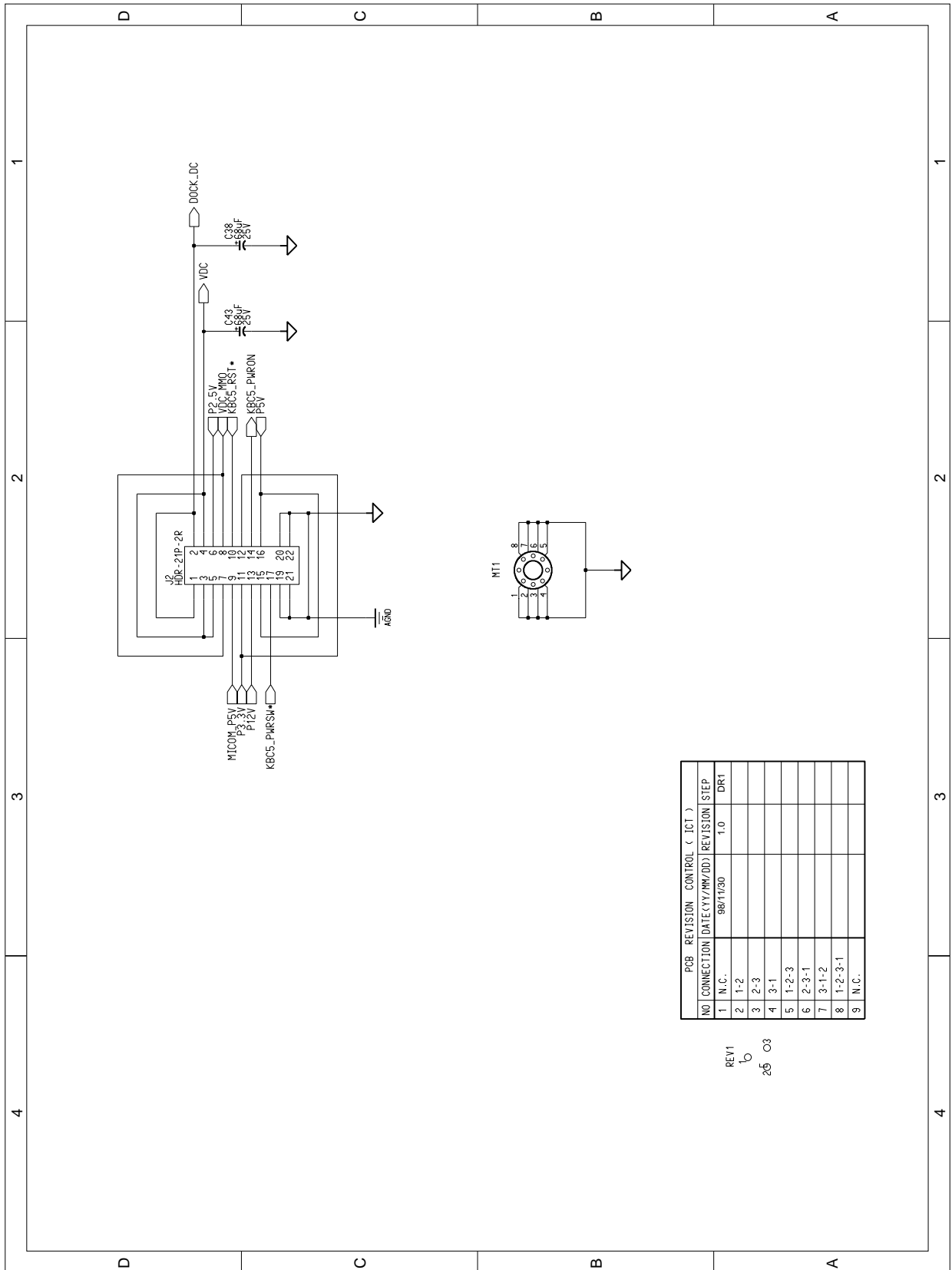
5-3-1(d) Modem Board Schematic Sheet 5 of 5(USB JACK & FIR)



5-4 DC/DC Board

5-4-1 Schematic Diagrams





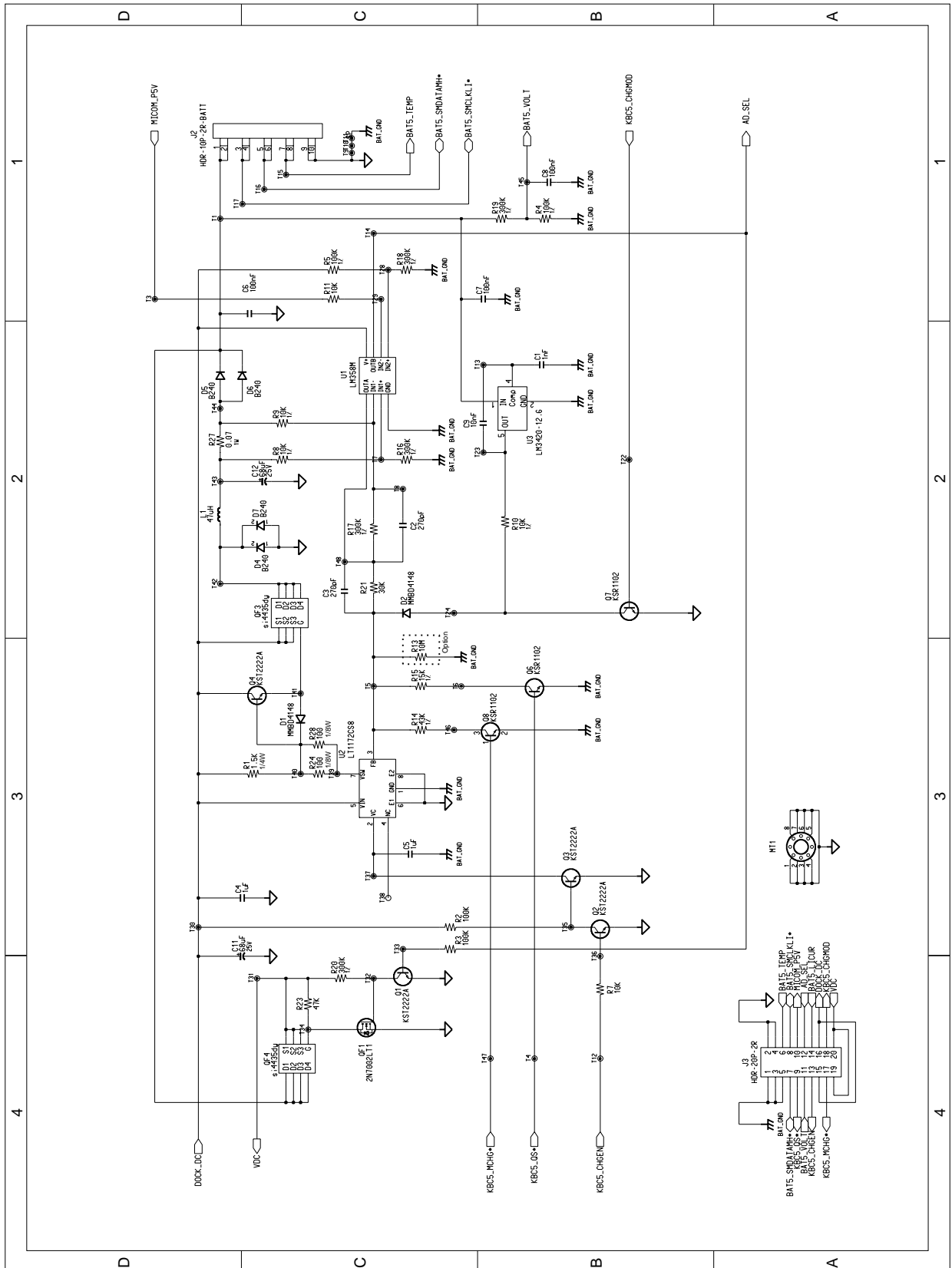
PCB REVISION CONTROL (ICT)

NO	CONNECTION	DATE	YY/MM/DD	REVISION	STEP
1	N.C.	98	7/1/20	1.0	DRT
2	1-2				
3	2-3				
4	3-1				
5	1-2-3				
6	2-3-1				
7	3-1-2				
8	1-2-3-1				
9	N.C.				

REV1
10
25 03

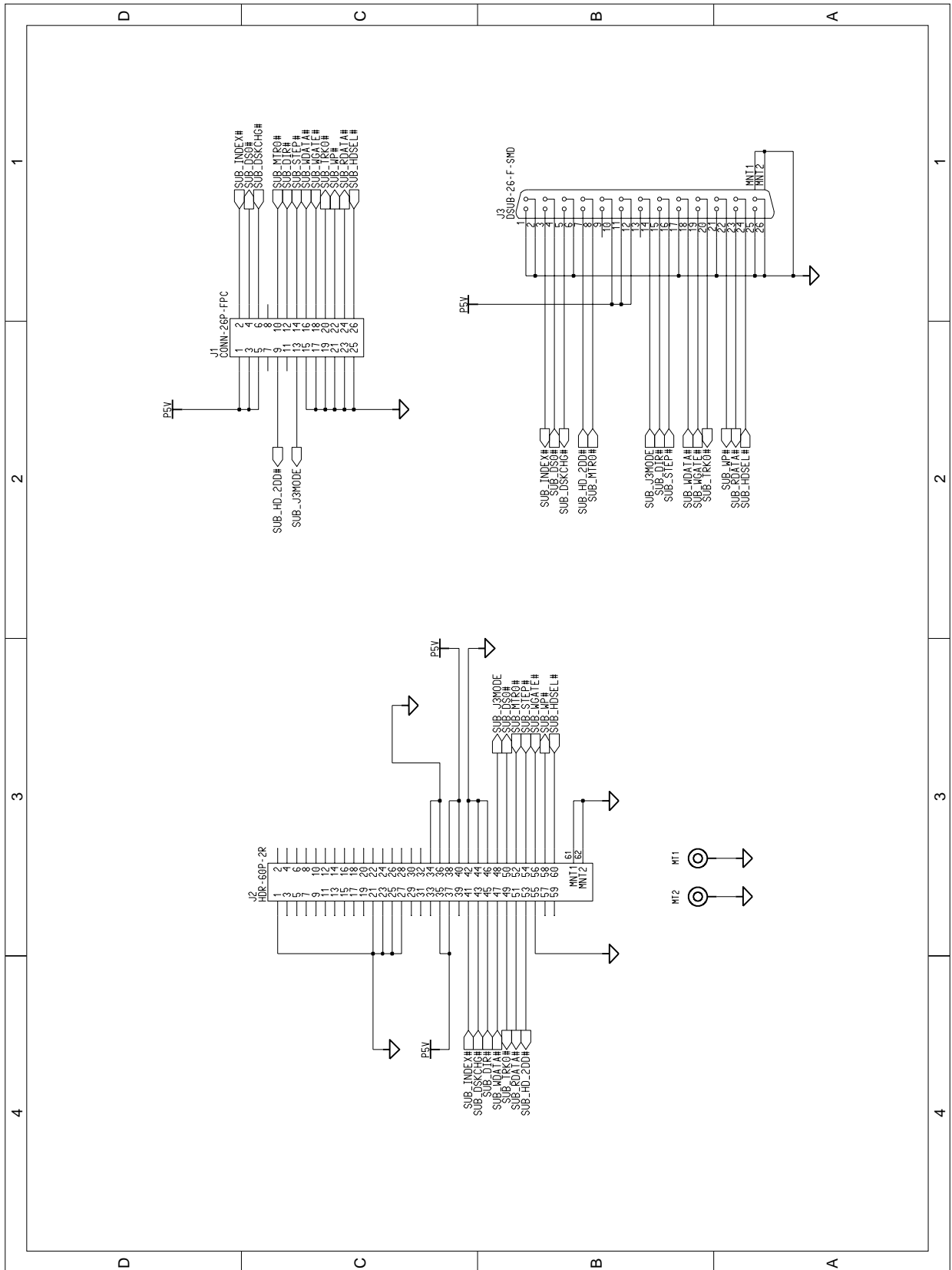
5-5 Charger Board

5-5-1 Schematic Diagrams and PCB



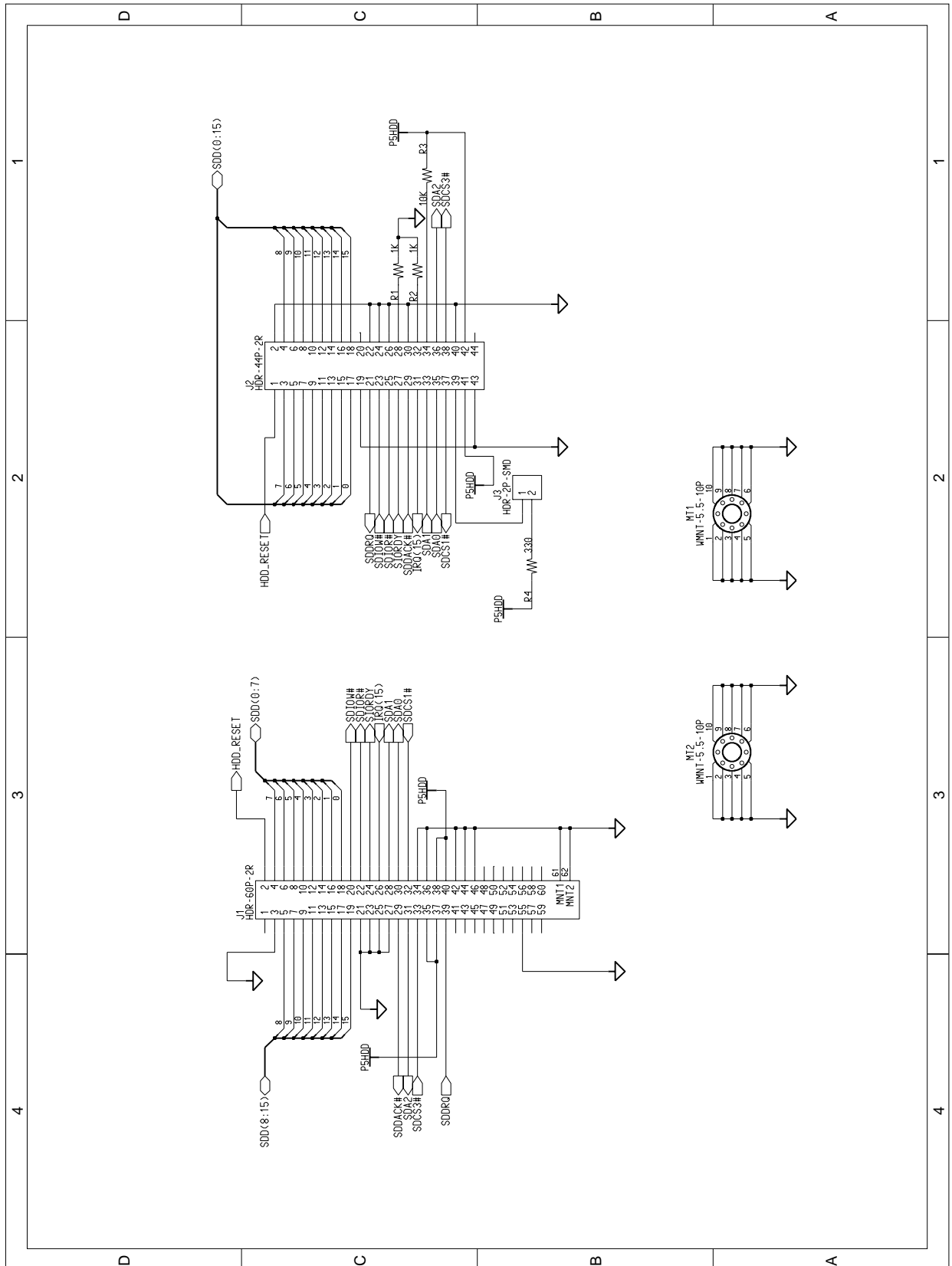
5-6 FDD Interface Board

5-6-1 Schematic Diagrams and PCB



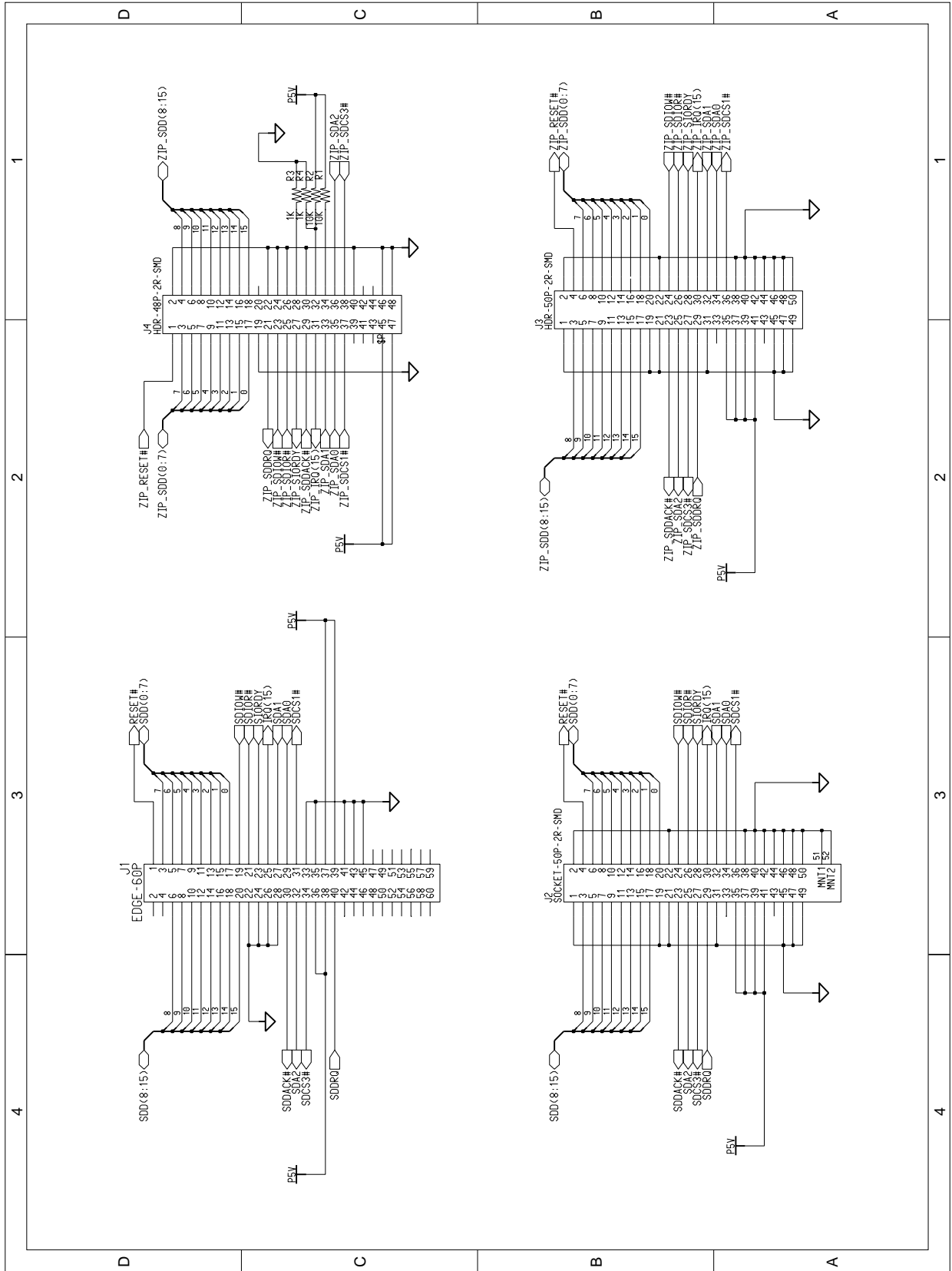
5-8 2'nd HDD Interface Board

5-8-1 Schematic Diagrams and PCB



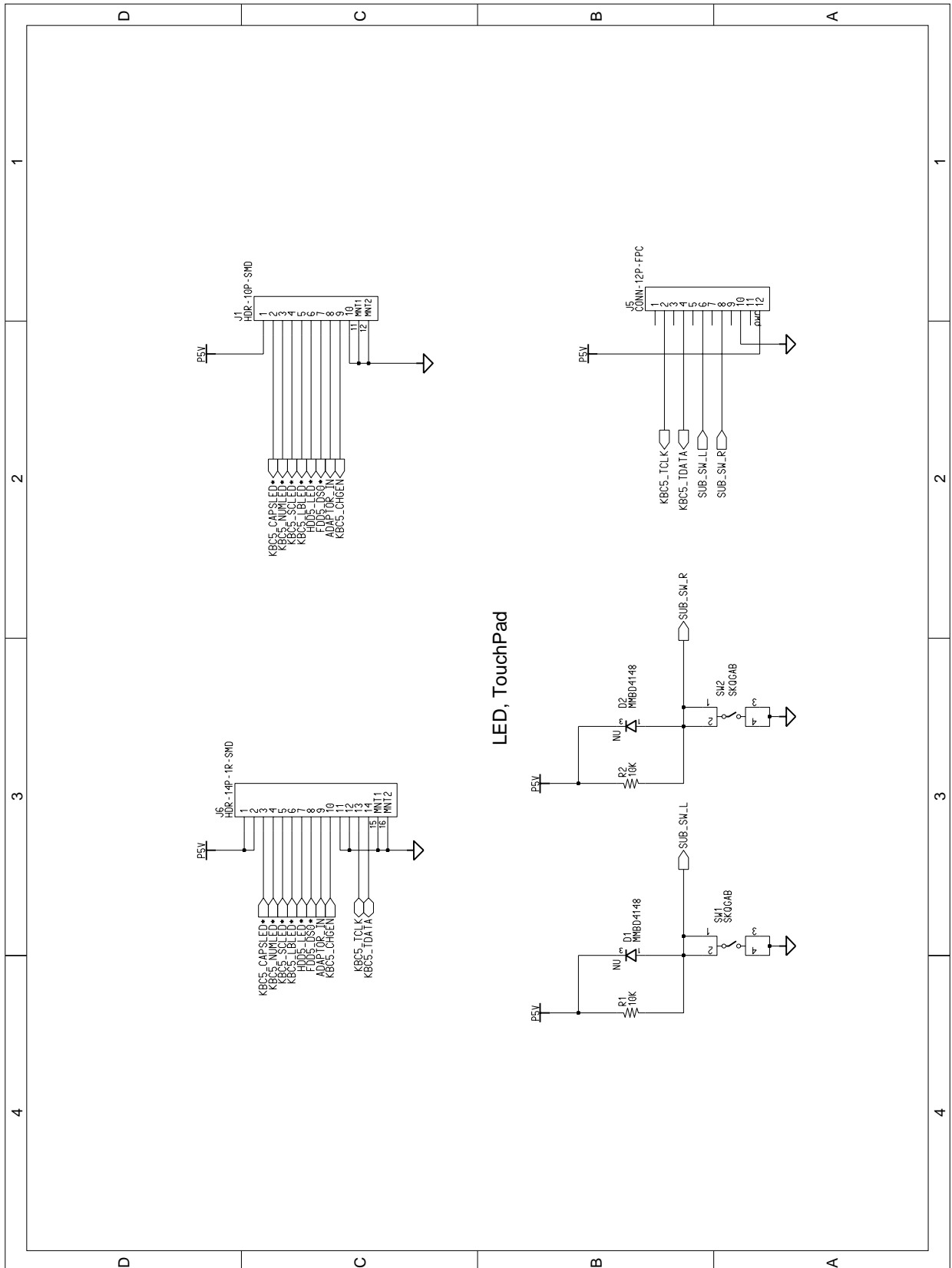
5-9 ZIP Drive Interface Board

5-9-1 Schematic Diagrams and PCB



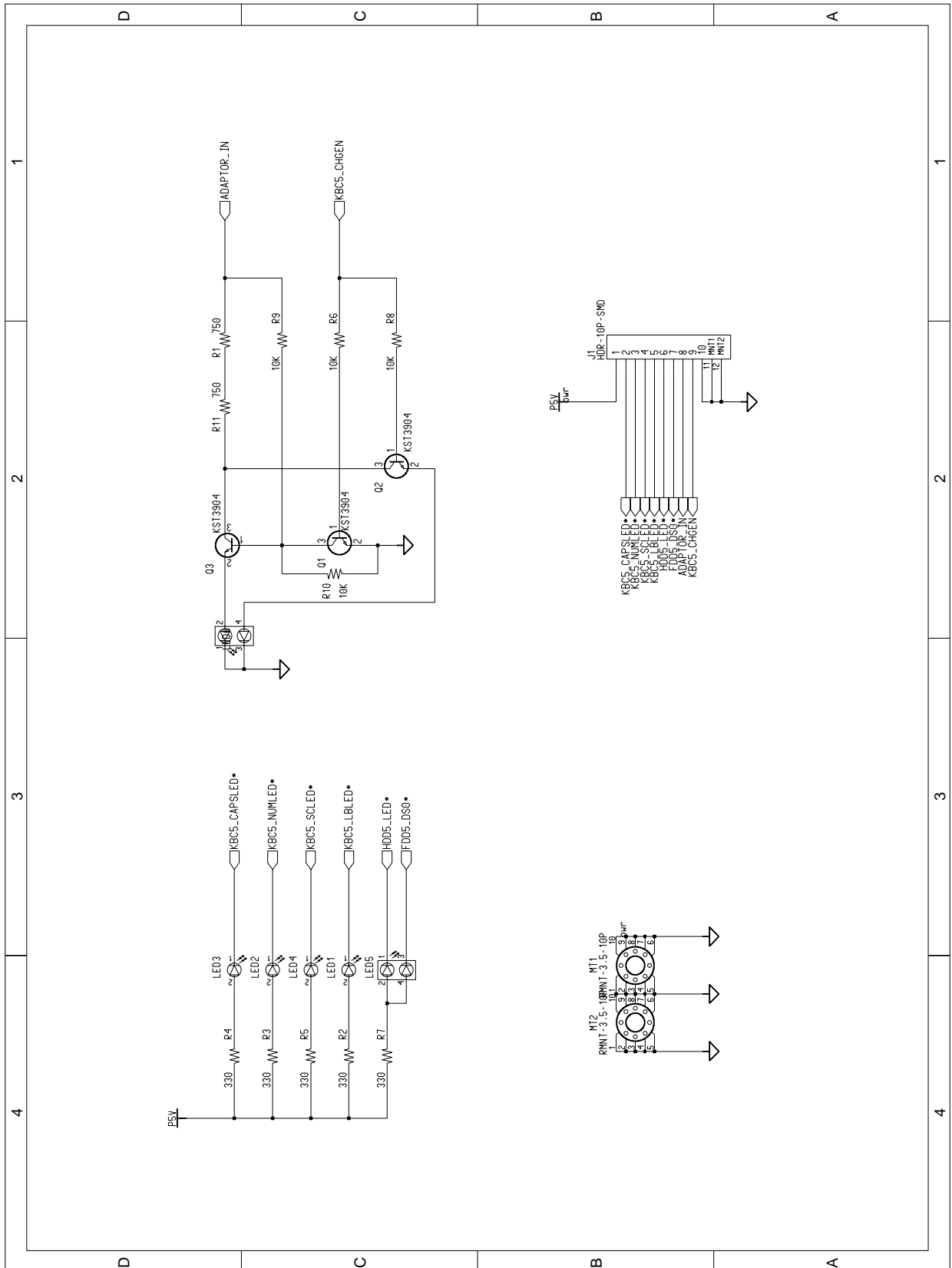
5-10 Touch pad Interface Board

5-10-1 Schematic Diagrams and PCB



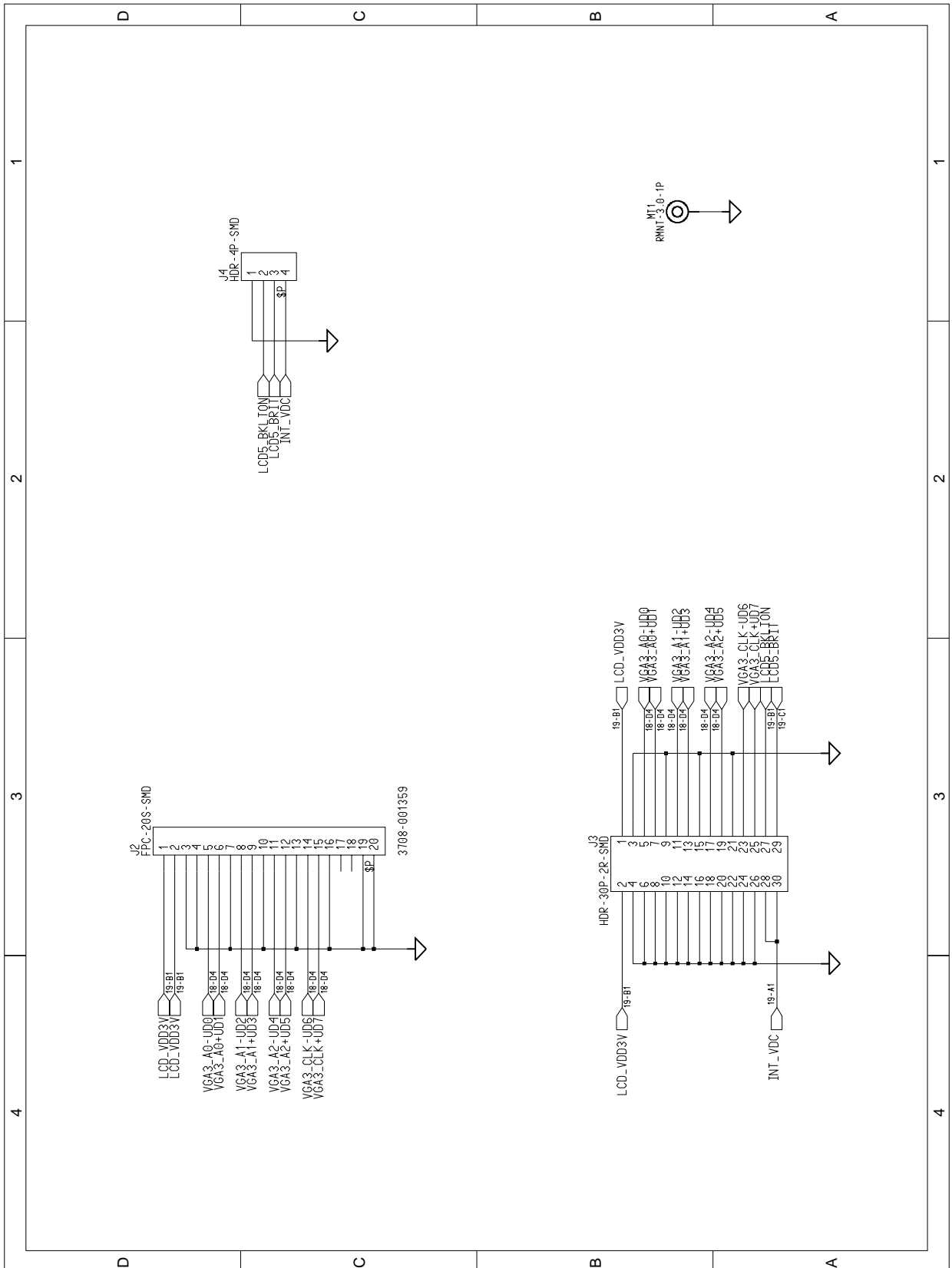
5-11 LED Interface Board

5-11-1 Schematic Diagrams and PCB



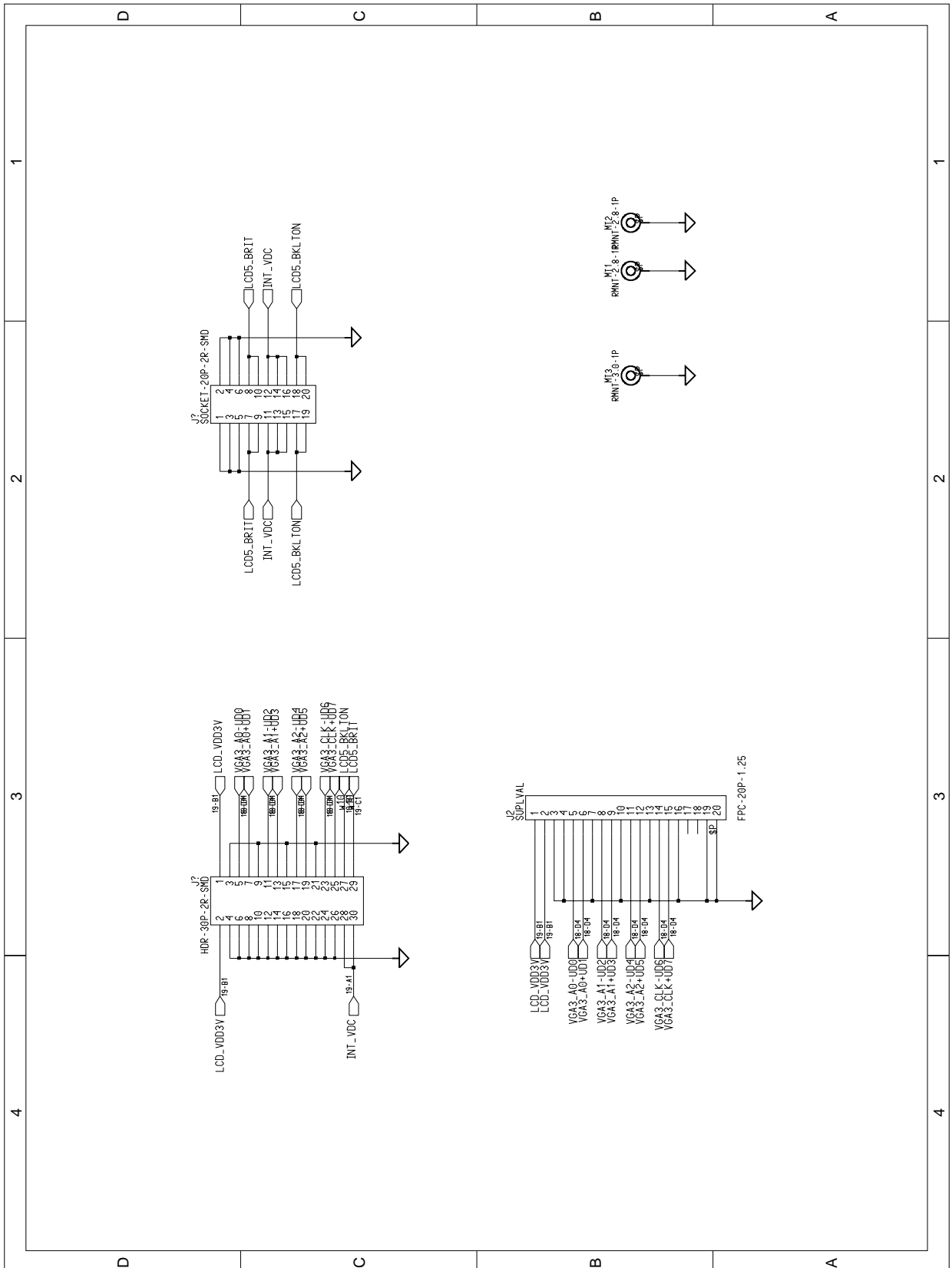
5-12 12.1" LCD FPC Board

5-12-1 Schematic Diagrams



5-13 13.3" LCD FPC Board

5-13-1 Schematic Diagrams



5-14 Ontop FPC Board

5-14-1 Schematic Diagrams

