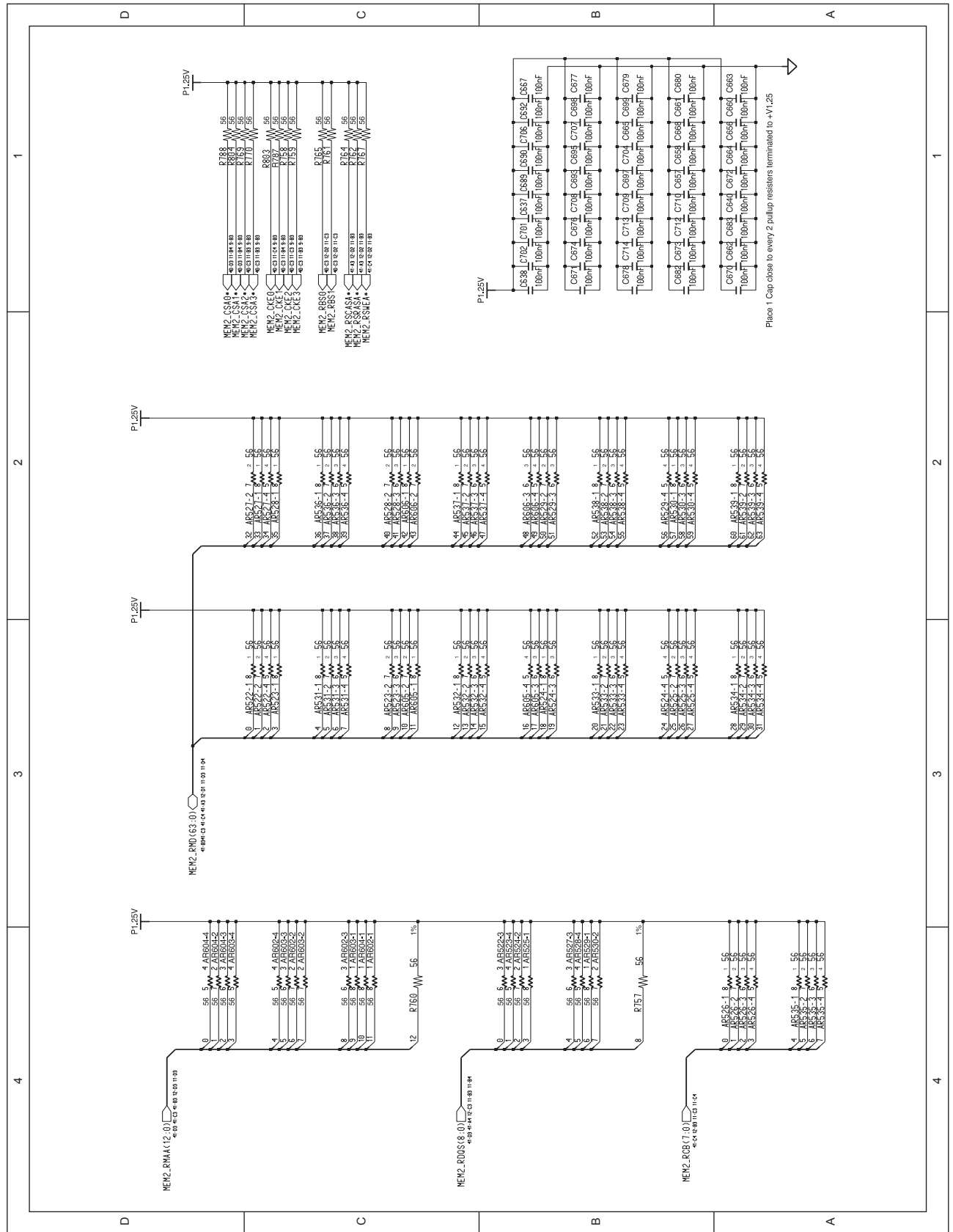
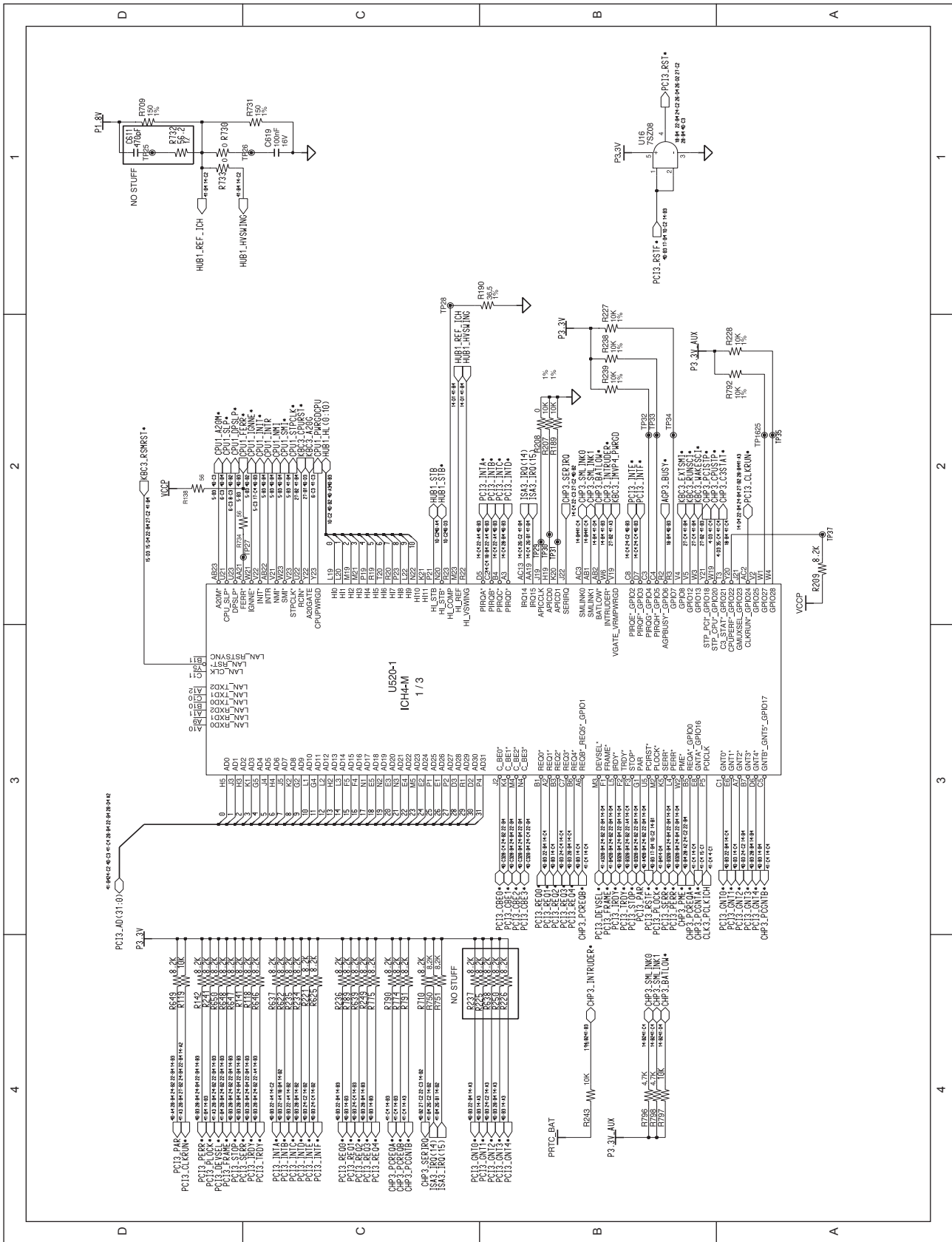


7-1-1(I) Main Board Schematic Sheet 13 of 42 (DDR-P TERMINATION)

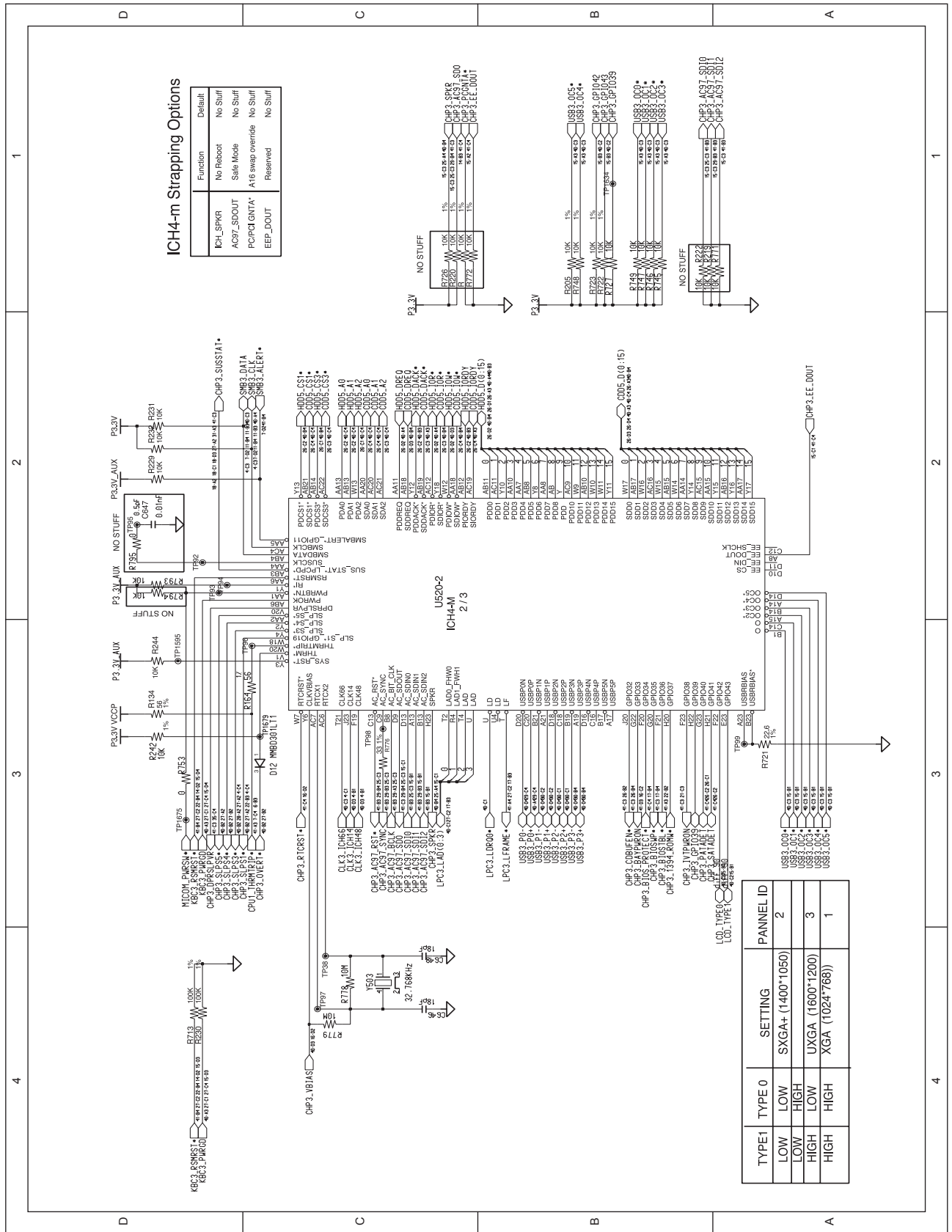


7 Schematic Diagrams and PCB Silkscreen

7-1-1(m) Main Board Schematic Sheet 14 of 41 (ICH-4)

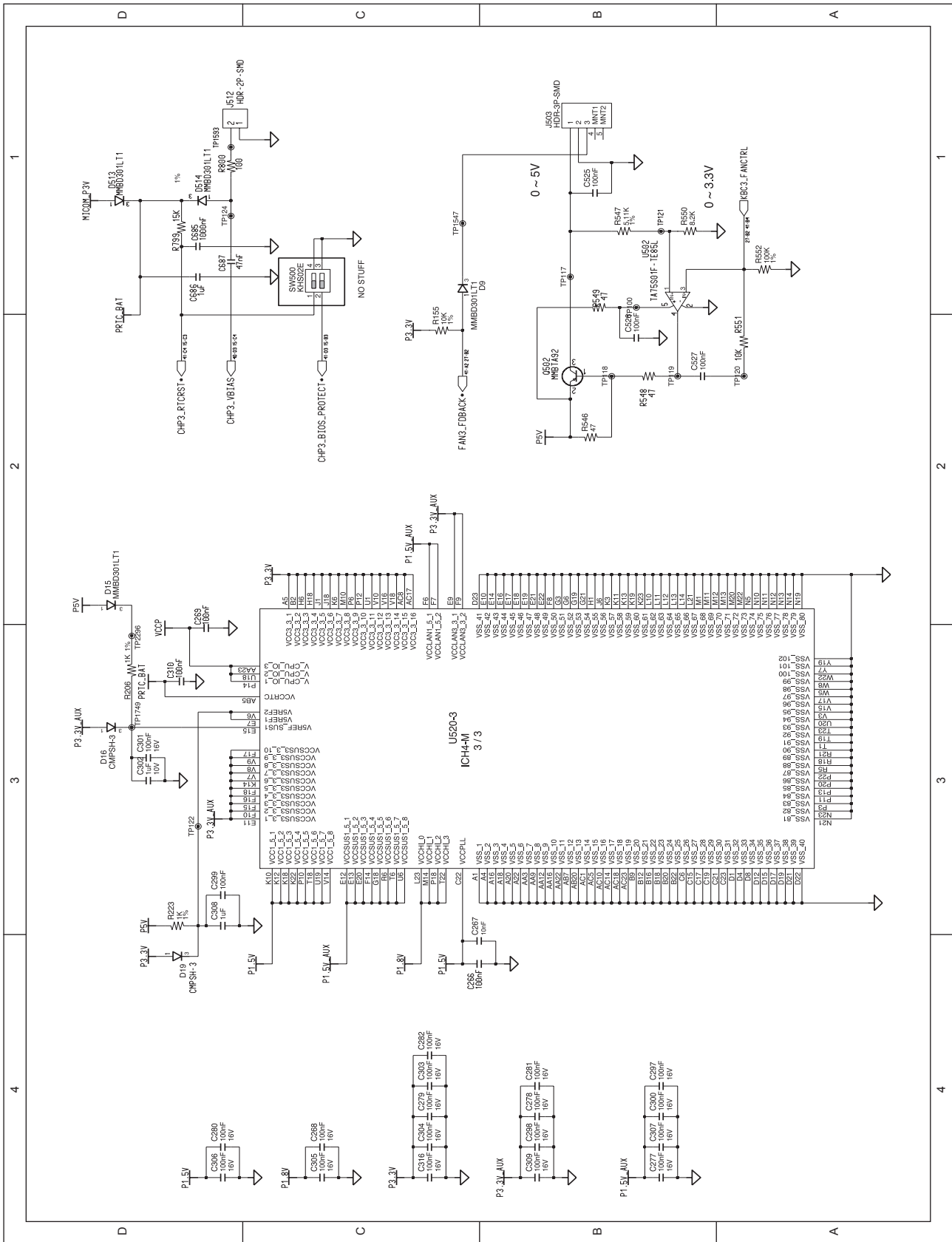


7-1-1(n) Main Board Schematic Sheet 15 of 41 (ICH-4)

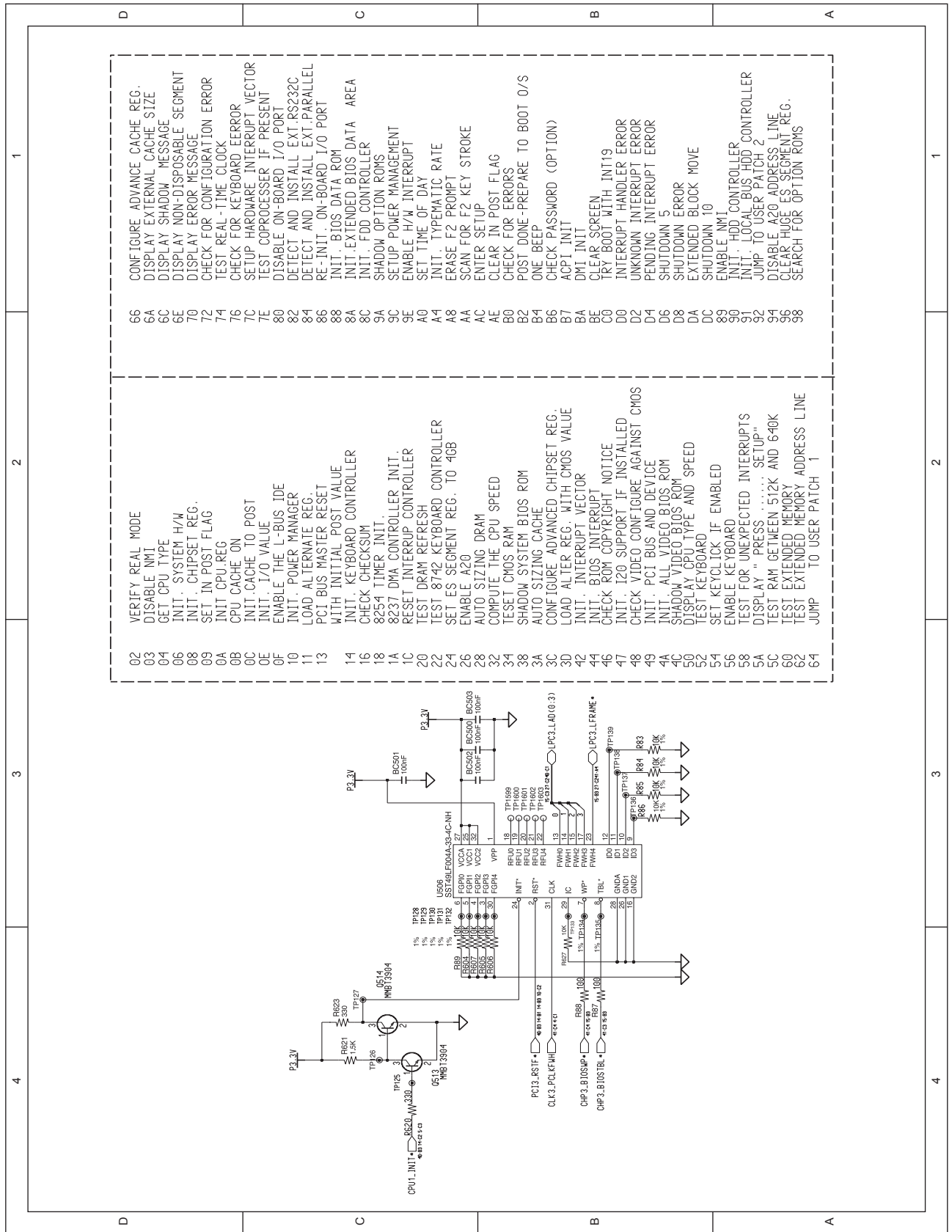


7 Schematic Diagrams and PCB Silkscreen

7-1-1(o) Main Board Schematic Sheet 16 of 41 (ICH-4)

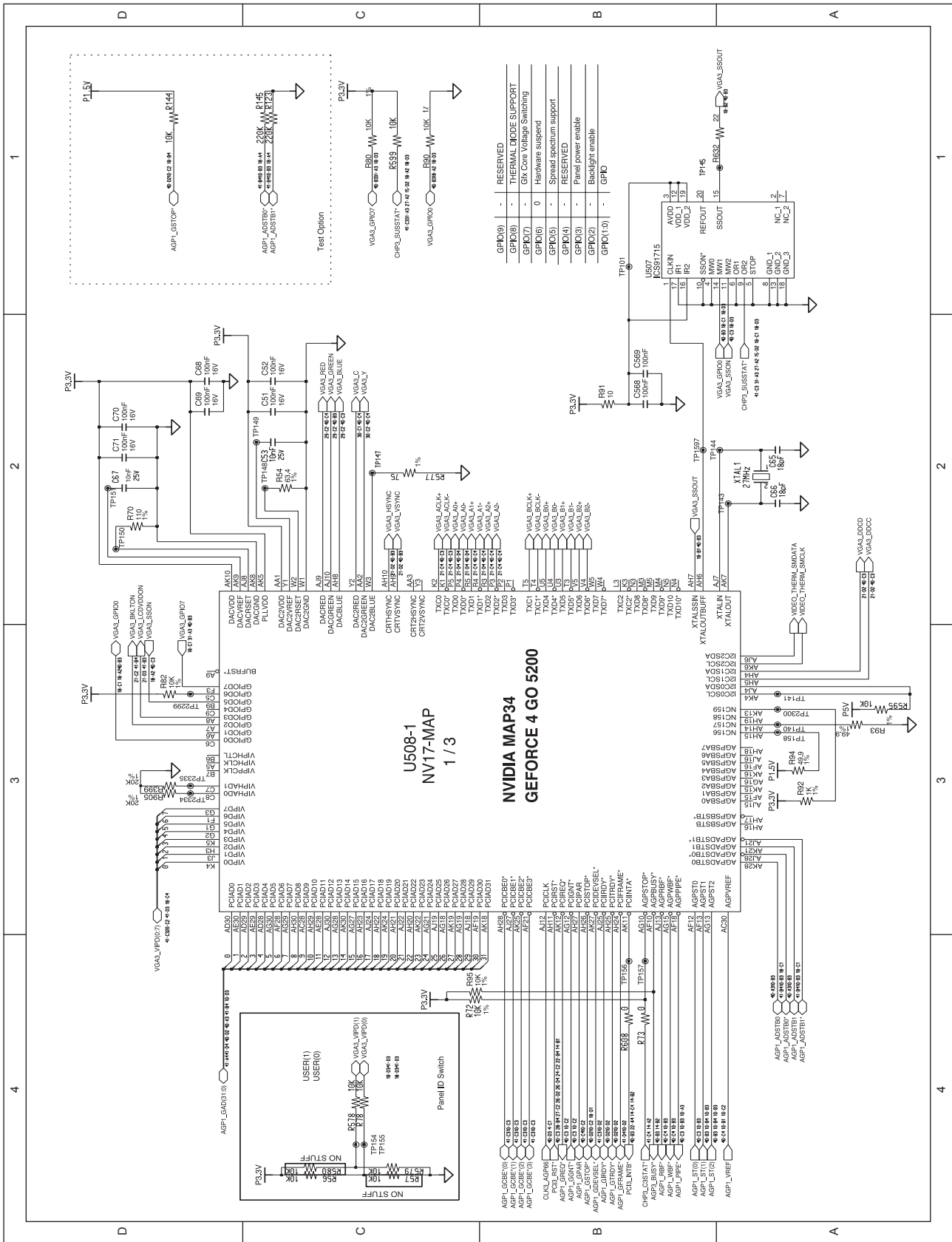


7-1-1(p) Main Board Schematic Sheet 17 of 41 (FWH)



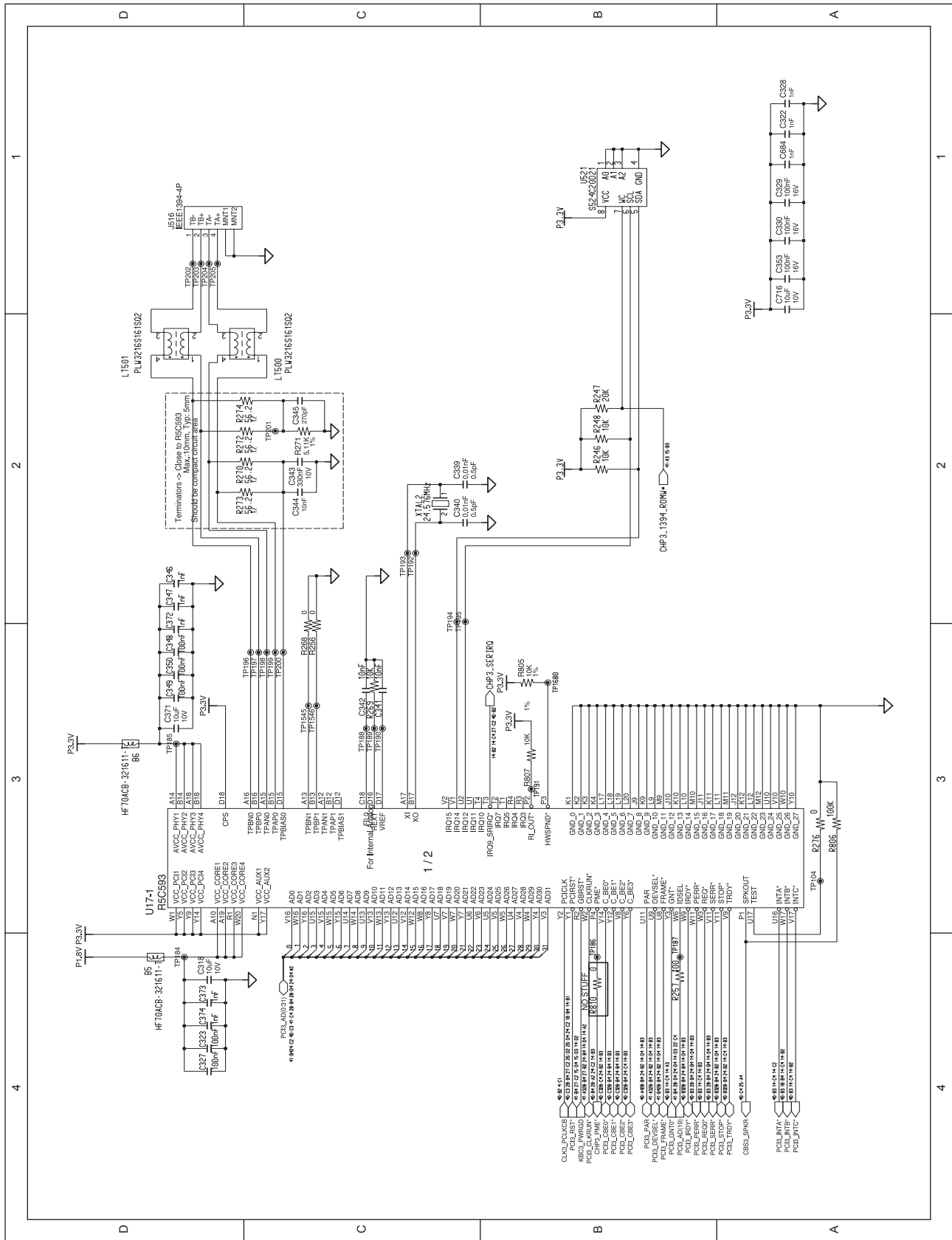
7 Schematic Diagrams and PCB Silkscreen

7-1-1(q) Main Board Schematic Sheet 18 of 41 (NVIDIA MAP34)



7 Schematic Diagrams and PCB Silkscreen

7-1-1(u) Main Board Schematic Sheet 22 of 41 (CARDBUS & 1394)



7-1-1(v) Main Board Schematic Sheet 23 of 41 (CARDBUS SOCKET)

