

8I845GE775-G

Revision 1.0

SHEET TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	P4_775 CPU
05	P4_775 CPU
06	P4_775 CPU
07	P4_775 CPU
08	GMCH-BROOKDALE-G_A
09	GMCH-BROOKDALE-G_B
10	GMCH-BROOKDALE-G_C
11	DDR SERIAL TERMINATION
12	DDR1 & DDR2
13	DDR3 & DDR TERM.
14	AGP SLOT
15	ICH4_1
16	ICH4_2
17	FWH
18	ICS950211AF CLOCK GEN
19	PCI1 & PCI2 SLOT
20	PCI3 & PCI4 SLOT
21	PCI5 SLOT
22	LPC IO ITE 8702 & FLOPPY & FAN
23	FRONT USB2.0

SHEET TITLE

24	FRONT PANEL
25	IDE CONNECTOR
26	PS/2 K/B & M/S
27	COM & PRT PORT
28	AC97 CODEC
29	LINE OUT/IN/MIC/FRONT AUDIO & GAME
30	VGA CONNECTOR
31	MISC. PWR & ATX POWER
32	VCORE PWM(FAN5093)
33	DDR POWER
34	REALTEK8110C
35	PCI ROUNTING
36	GPIO PIN LIST

GIGABYTE			
Title COVER SHEET			
Size	Document Number	Rev	
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Model Name: GA-8I845GE775-G

Version:1.0

**Circuit or PCB layout change
for next version**

Component value change history

293.95x210.5 mil 60±15% C Type

DATE	Change Item	Reason
2005.01.17	0.1 NEW BOM RELEASE	
2005.03.11	1.0A BOM RELEASE	
	ATX USE: 11NH4-020020-61/-62 CPU RM REMOVE	
	Add EMI solution. ACN1,ACN2=180P	
	Add U12 SST 49LF003B 2nd source	
	Clock Gen change to RTM350-110R	
	Add 9701 for USB Drop Issue	
	Add 0.1u at SLP_S3- but no pop BC9	
	Add VCC E-CAPx2 but pop 1 EC1	
	DL1 change to 11NH2-011507-01 wire.	
2005.03.31	1.0B BOM	Main stream 1.4 or performance
	DU2 change to 10TA1-605019-10	Homer: DC8:150P, DR32:100ohm, DR28:0, DR31:120K, DR6,DR7,DR8:100K
	Add Q28 to minimum Q27 temperature. Only Q27 run 3D will reach 100C	
	Package	

DATE	Change Item	Reason
2005.01.13	0.1 GERBER OUT.	
2005.03.10	1.0 GERBER OUT.	

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BLOCK DIAGRAM

INTEL Pentium4 (775)

VICORE = 1.75V / SLEEP: 1.3V
VCC3

PAGE 4, 5, 6, 7

PWM/OTHER POWER

VID0-4

VICORE = 1.75V (60-110MHZ) / SLEEP: 1.3V
5VSB=12V,+12V,VCC,VCC3,3VDUAL
VTI_DDR2_5VSTR

PAGE 31, 32, 33

CLOCK GENERATOR

CKVDD = 3.3V

PAGE 18

**GMCH
BROOKDALE-G-DDR**

VICORE = 1.75V / SLEEP: 1.3V
2_5VSTR = 3.3V(MEMORY)
VDDQ = 1.5V (AGP POWER 4X, HUBLINK)

PAGE 8, 9, 10

DDR SDRAM DIMM X 3

2_5VSTR = 2.5V(MEMORY,SUSPEND POWER)
VTI_DDR = 1.25V

PAGE 11, 12, 13

AGP SLOT 4X

VDDQ = 1.5V (AGP POWER 4X)
VCC3 = 3.3V
+12V = 12V
3VDUAL = 3.3V
VCC = 5V

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GAD0-31
ADSTB0, ADSTB0-
ADSTB1, ADSTB1-
SBA0-7
SBSTB, SBSTB-
GCBE0-3-
ST0-2

AGP BUS

MAA0-14
MAA_CPC1-5
MAB_CPC1-5
MDD0-63
-DQSD0-7
DM0-7

ICH4

VCC25 = 2.5V(I/O, MEMORY, VLINK)
3VDUAL = 3.3V(SUSPEND POWER)
VCC3 = 3.3V
RTCVD0 = 3.3V

PAGE 15, 16

FRONT USB CONN.

PAGE 23

IDE Primary and Secondary

VCC = 5V

PAGE 25

FWH

VCC = 5V
VCC3 = 3V

PAGE 17

REAR USB PORTS

VCC = 5V
5VSB = 5V
5VUSB = 5V

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PCI SLOT 1, 2, 3, 4, 5

+12 = 12V
+12 = 12V
VCC = 5V
VCC3 = 3V
3VDUAL = 3V

PAGE 19, 20, 21

REALTEK 8110S LAN

+12 = 12V
+12 = 12V
VCC = 5V
VCC3 = 3V
3VDUAL = 3V

PAGE 34

LPC I/O ITE8712
FDD IR/CIR S_IRQ

VCC = 5V
VCC3 = 3V

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I/O PORTS :
COMA COMB LPT PS2

PAGE 27

FRONT PANEL /FANS

VCC = 5V
5VSB = 5V
+12 = 12V
5VPC = 5V

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AC97 CODEC ALC850

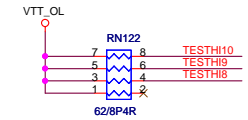
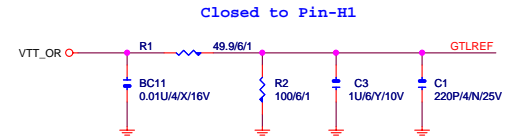
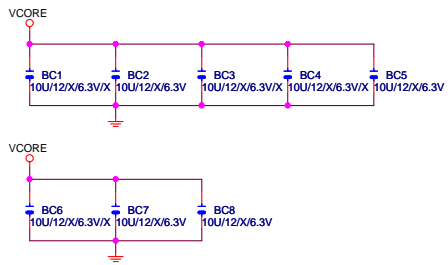
+12V = 12V
VCC3 = 3.3V
VCC = 5V
VDDQ = 5V

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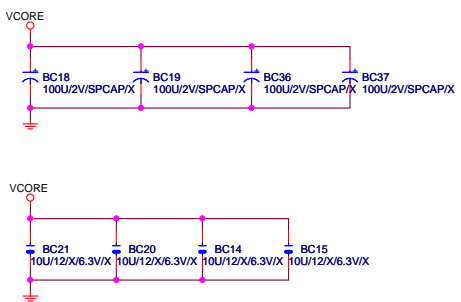
AUDIO PORTS : FRONT AUDIO
LIN_OUT LINE_IN MIC
TELE CD_IN

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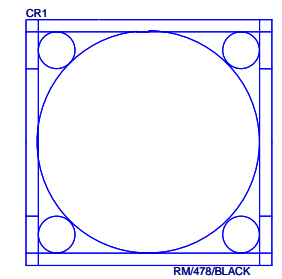
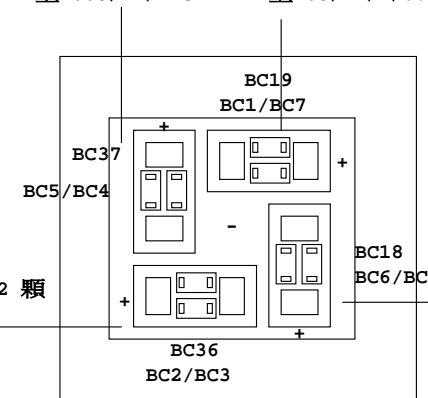
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上100U/2V/SPCAP 上10U/12/X/6.3V X2 顆

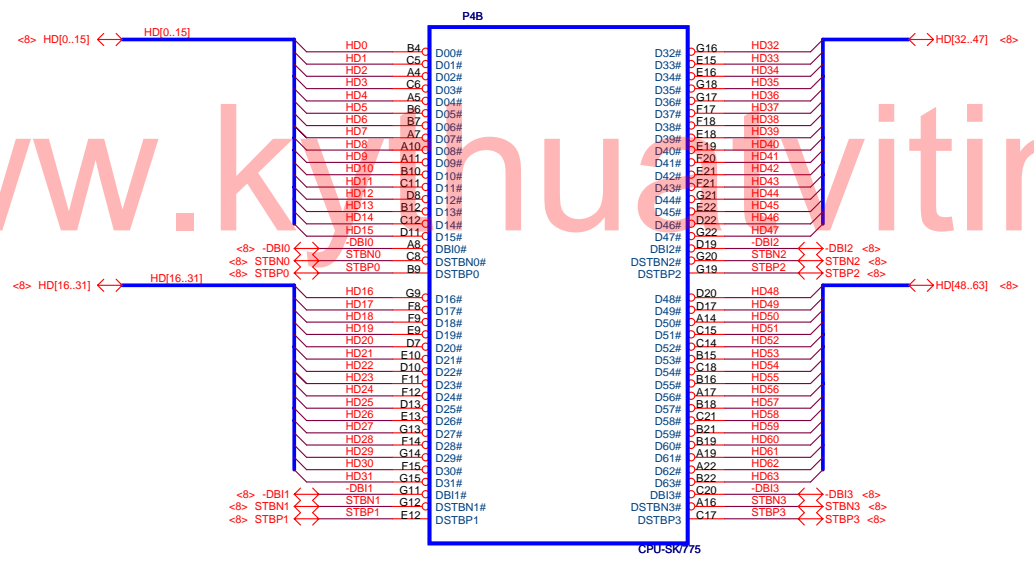
上10U/12/X/6.3V X2 顆

上100U/2V/SPCAP



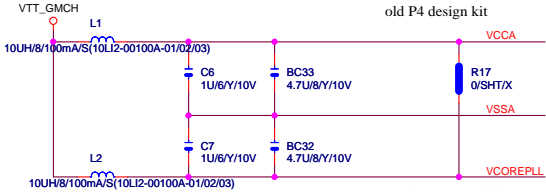
GIGABYTE			
Title P4 LGA775-A			
Size Custom	Document Number	81845GE775-G	
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GIGABYTE			
Title			
P4_LGA775-C			
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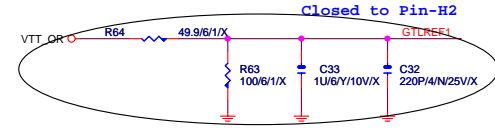
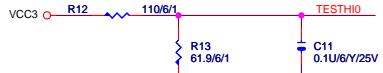
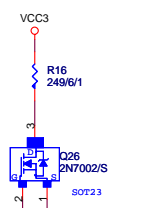
Note:
VCCA & VCOREPLL
define doesn't same as
old P4 design kit



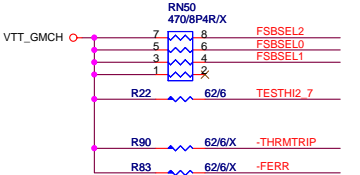
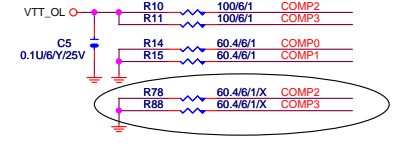
As close as possible to
CPU socket

Trace width doesn't
less than 12 Mil

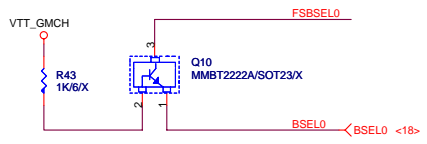
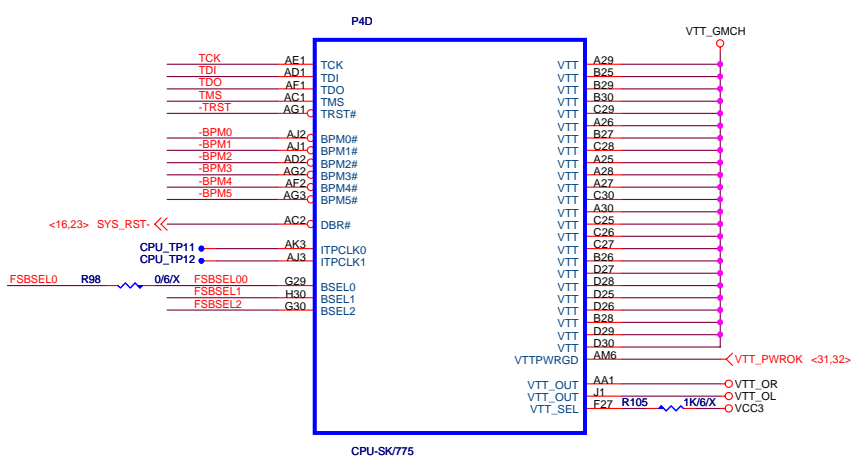
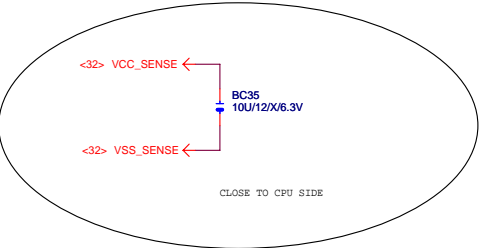
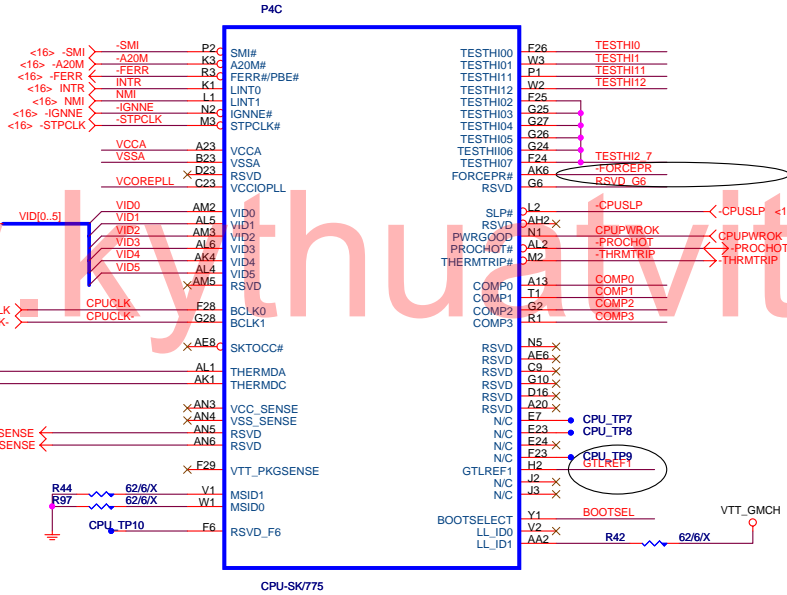
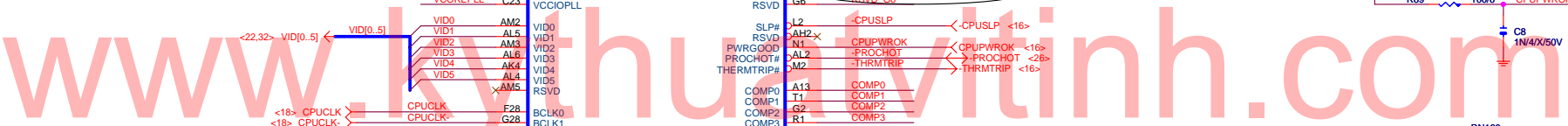
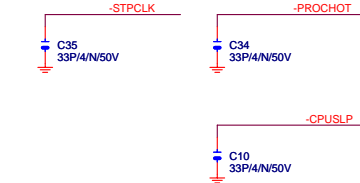
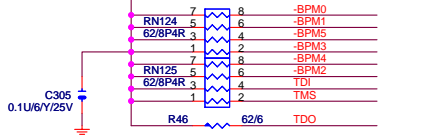
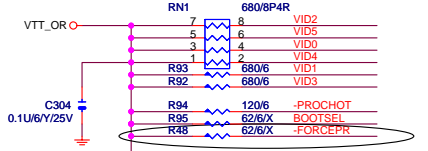
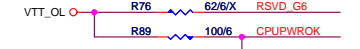
<7.8> GTL_DET

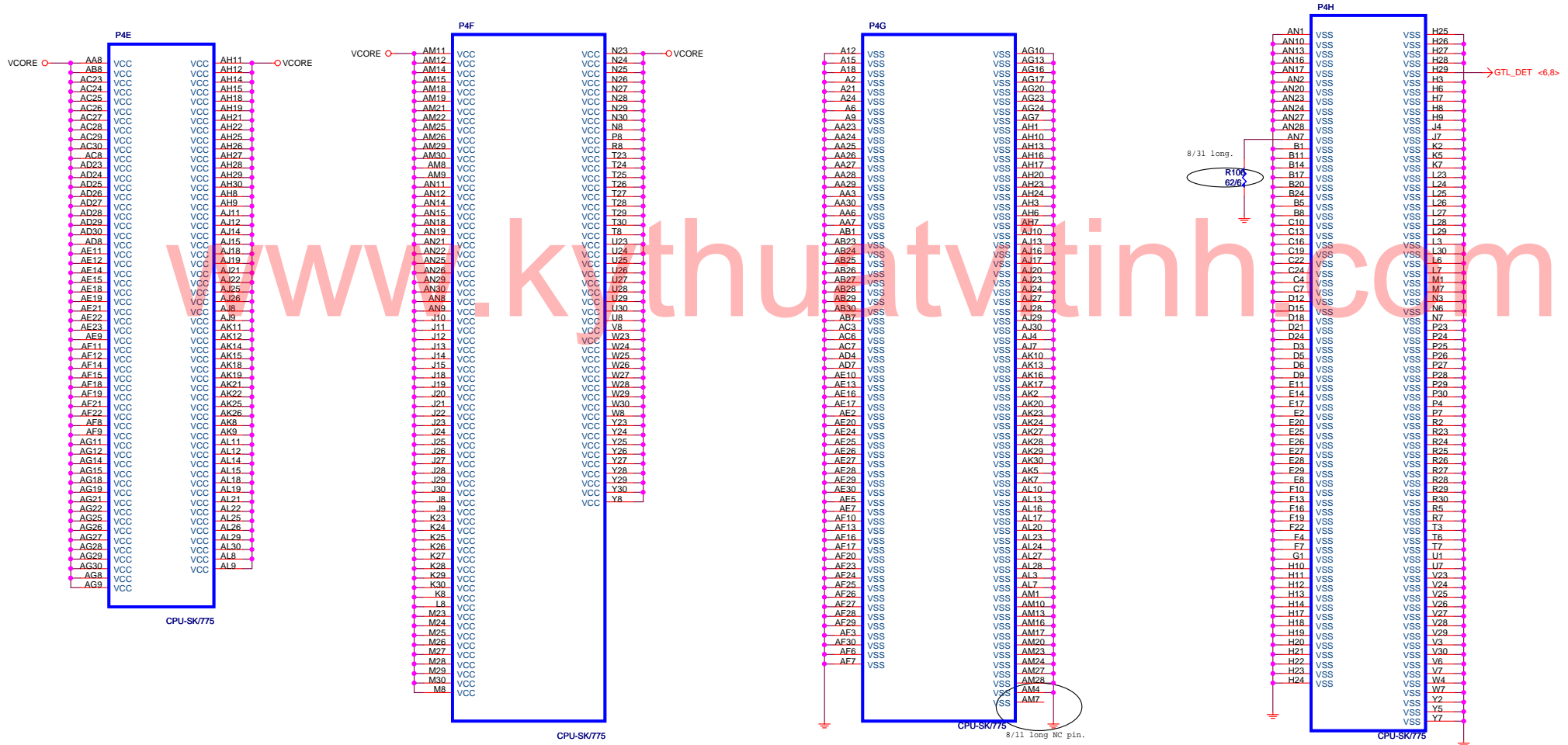


Place outside of CPU socket

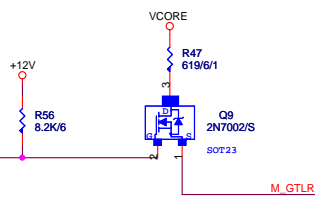
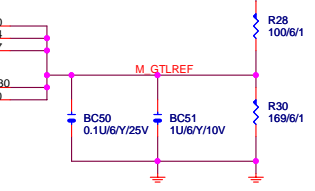
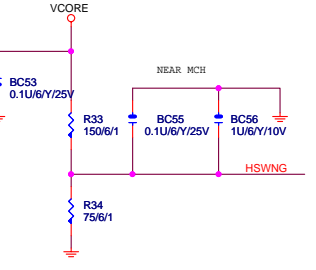
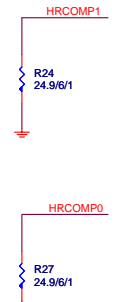
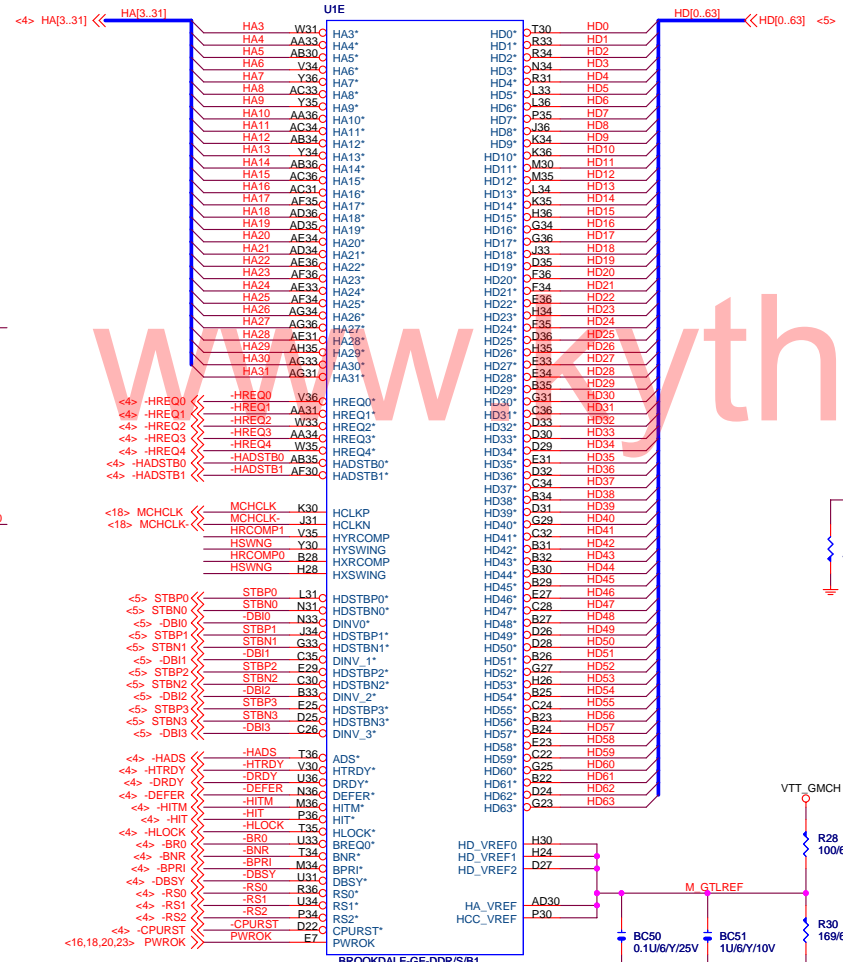
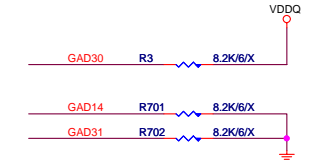


Locate at ICH6 Side

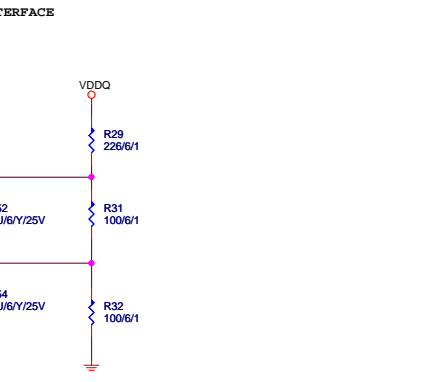
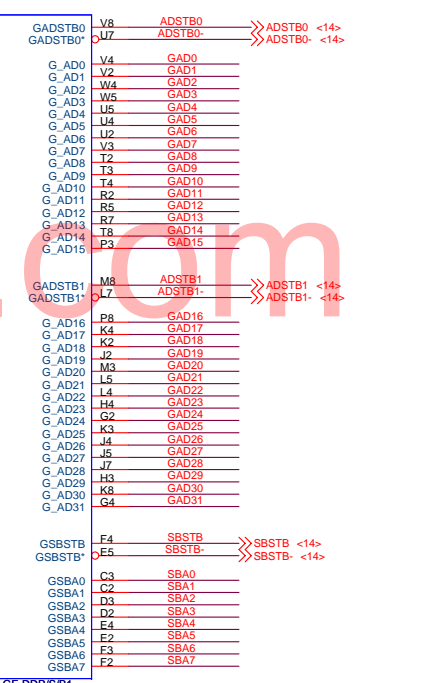
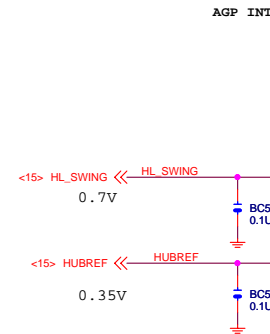
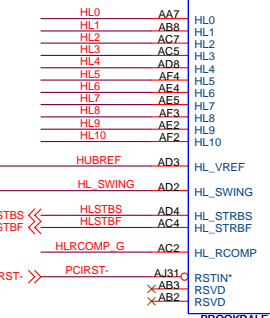
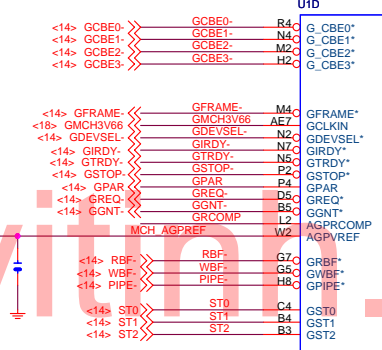




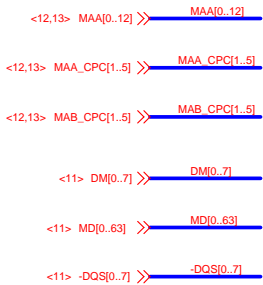
<14> GAD[0..31] <<=====
<14> SBA[0..7] <<=====
<15> HL[0..10] <<=====
VDDQ



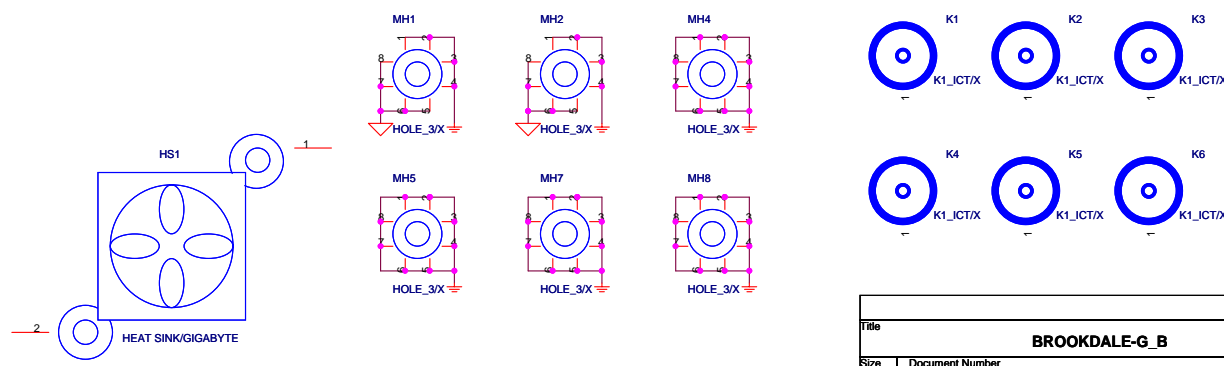
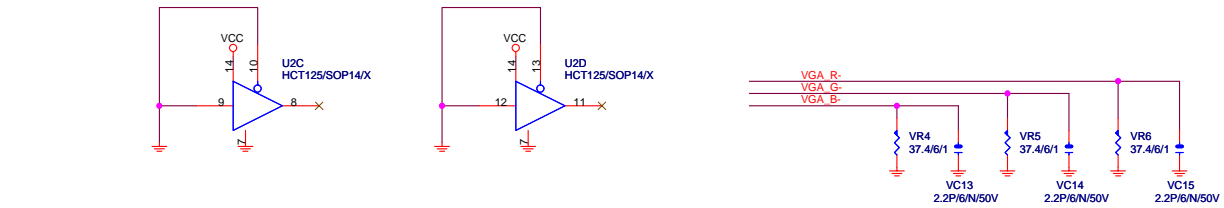
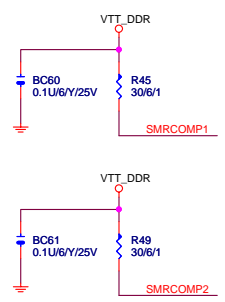
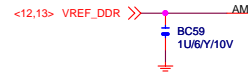
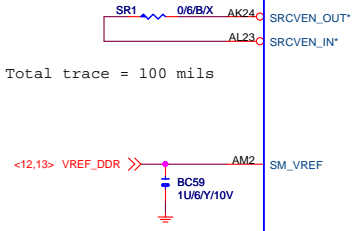
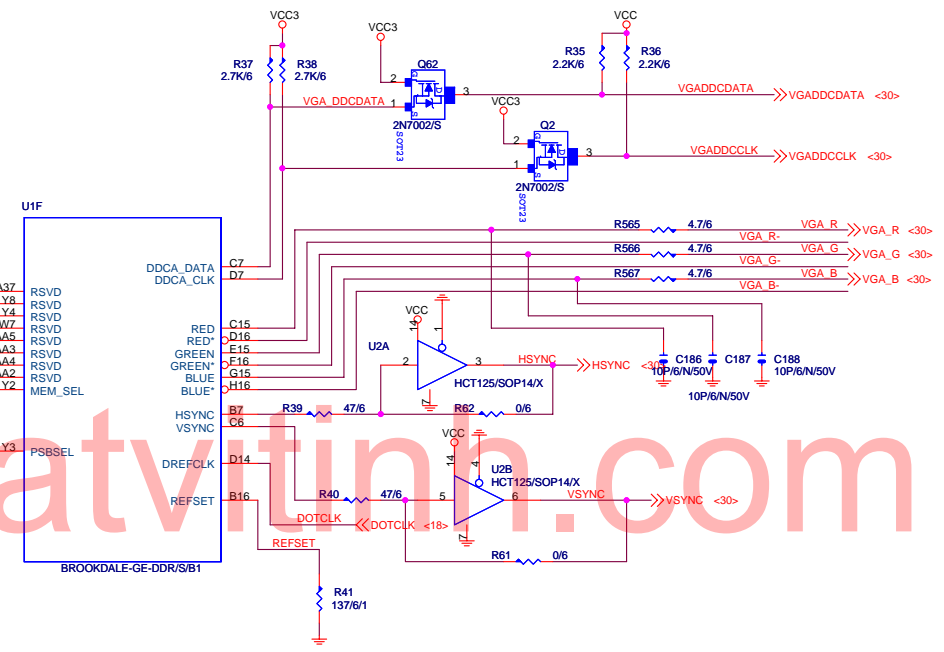
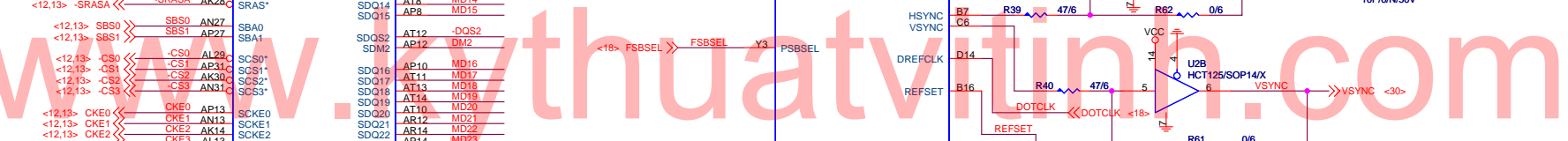
<6,7> GTL_DET <<=====
+12V



Title	BROOKDALE-G_A		
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UHC		UHF	
MAA0	AL25	SMAA0	SDQS0
MAA_CPC1	AN25	SMAA1	SDM0
MAA_CPC2	AP23	SMAA2	SDQ0
MAA3	AK20	SMAA3	SDO1
MAA_CPC4	AL19	SMAA4	SDO2
MAA_CPC5	AL17	SMAA5	SDO3
MAA6	AP19	SMAA6	SDO4
MAA7	AP17	SMAA7	SDO5
MAA8	AN17	SMAA8	SDO6
MAA9	AK26	SMAA9	SDO7
MAA10	AK16	SMAA10	SDQS1
MAA11	AL15	SMAA11	SDM1
MAA12	AN15	SMAA12	SDM1
MAB_CPC1	AP25	SMAB1	SDO8
MAB_CPC2	AN23	SMAB2	SDO9
MAB_CPC4	AN19	SMAB4	SDQ10
MAB_CPC5	AK18	SMAB5	SDQ11
			SDQ12
			SDQ13
			SDQ14
			SDQ15
			SDQS2
			SDM2
			SDQ16
			SDQ17
			SDQ18
			SDQ19
			SDQ20
			SDQ21
			SDQ22
			SDQ23
			SDQS3
			SDM3
			SDQ24
			SDQ25
			SDQ26
			SDQ27
			SDQ28
			SDQ29
			SDQ30
			SDQ31
			SDQS4
			SDM4
			SDQ32
			SDQ33
			SDQ34
			SDQ35
			SDQ36
			SDQ37
			SDQ38
			SDQ39
			SDQS5
			SDM5
			SDQ40
			SDQ41
			SDQ42
			SDQ43
			SDQ44
			SDQ45
			SDQ46
			SDQ47
			SDQS6
			SDM6
			SDQ48
			SDQ49
			SDQ50
			SDQ51
			SDQ52
			SDQ53
			SDQ54
			SDQ55
			SDQS7
			SDM7
			SDQ56
			SDQ57
			SDQ58
			SDQ59
			SDQ60
			SDQ61
			SDQ62
			SDQ63



Title			BROOKDALE-G_B		
Size	Document Number		Rev		
Custom	81845GE775-G		1.0		
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<12,13> MD_0.63] << MD[0.63] <9>

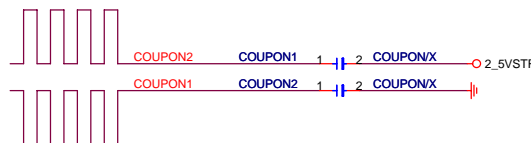
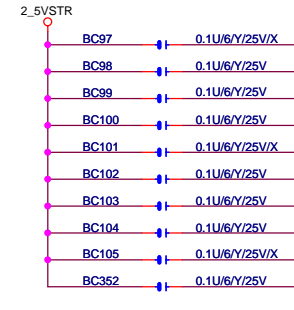
<12,13> DM_0.7] << DM[0.7] <9>

<12,13> -DQS_0.7] << -DQS[0.7] <9>

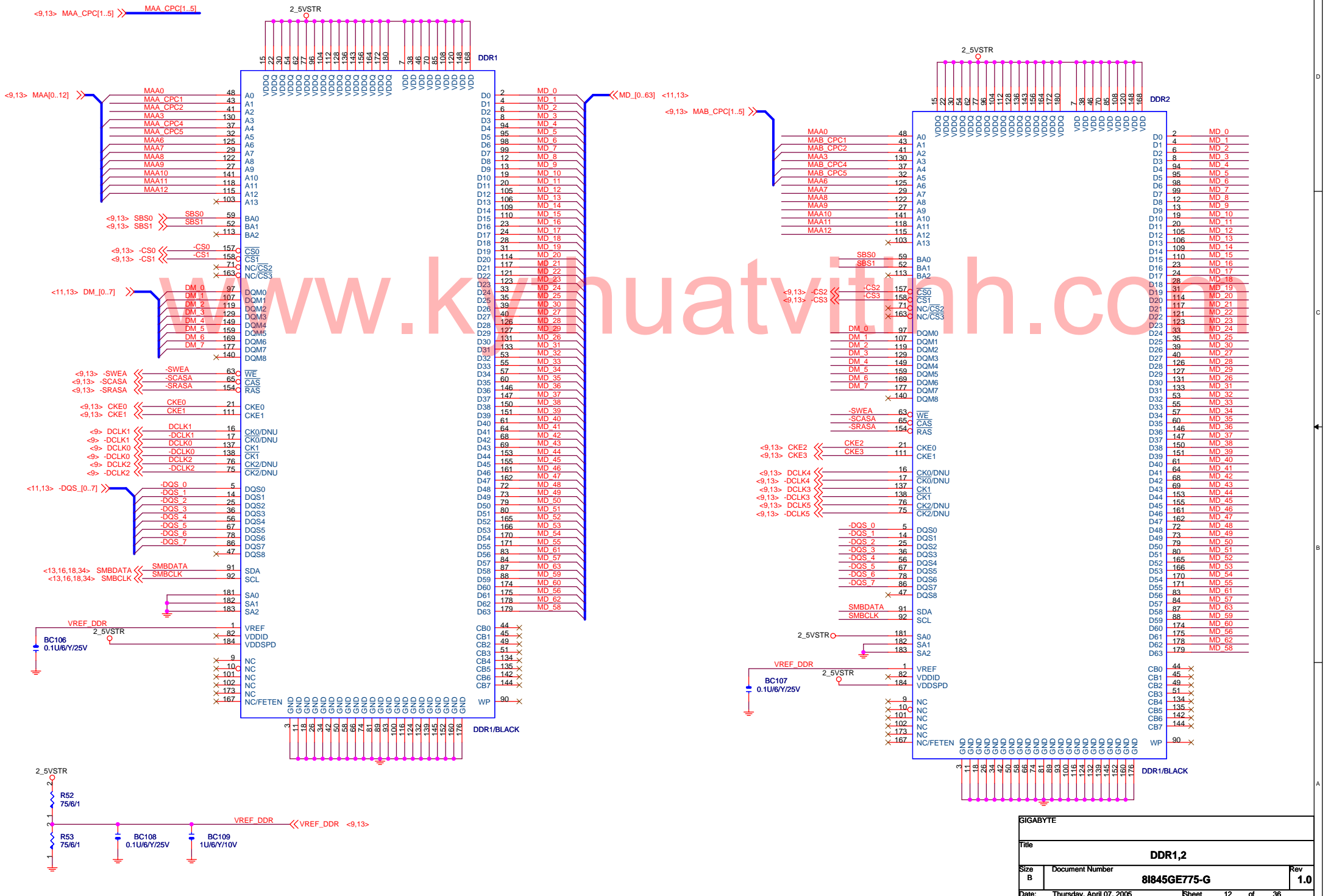
DM 0	RN3	1	2	0/8P4R/X	DM0
-DQS 0		3	4		-DQS0
MD 2		5	6		MD2
MD 6		7	8		MD6
MD 0	RN5	1	2	0/8P4R/X	MD0
MD 4		3	4		MD4
MD 5		5	6		MD5
MD 1		7	8		MD1
MD 12	RN6	1	2	0/8P4R/X	MD12
MD 13		3	4		MD13
-DQS 1		5	6		-DQS1
DM 1		7	8		DM1
MD 14	RN7	1	2	0/8P4R/X	MD14
MD 15		3	4		MD15
MD 10		5	6		MD10
MD 11		7	8		MD11
-DQS 2	RN9	1	2	0/8P4R/X	-DQS2
DM 2		3	4		DM2
MD 18		5	6		MD18
MD 22		7	8		MD22
MD 23	RN11	1	2	0/8P4R/X	MD23
MD 25		3	4		MD25
-DQS 3		5	6		-DQS3
DM 3		7	8		DM3
MD 26	RN12	1	2	0/8P4R/X	MD26
MD 30		3	4		MD30
MD 27		5	6		MD27
MD 31		7	8		MD31
MD 32	RN13	1	2	0/8P4R/X	MD32
MD 36		3	4		MD36
MD 33		5	6		MD33
MD 37		7	8		MD37
MD 39	RN14	1	2	0/8P4R/X	MD39
MD 35		3	4		MD35
MD 40		5	6		MD40
MD 44		7	8		MD44
-DQS 4	RN15	1	2	0/8P4R/X	-DQS4
DM 4		3	4		DM4
MD 34		5	6		MD34
MD 38		7	8		MD38
MD 45	RN16	1	2	0/8P4R/X	MD45
MD 41		3	4		MD41
DM 5		5	6		DM5
-DQS 5		7	8		-DQS5
MD 48	RN17	1	2	0/8P4R/X	MD48
MD 49		3	4		MD49
MD 52		5	6		MD52
MD 53		7	8		MD53
MD 50	RN18	1	2	0/8P4R/X	MD50
MD 51		3	4		MD51
MD 60		5	6		MD60
MD 61		7	8		MD61
DM 6	RN19	1	2	0/8P4R/X	DM6
-DQS 6		3	4		-DQS6
MD 54		5	6		MD54
MD 55		7	8		MD55
MD 56	RN20	1	2	0/8P4R/X	MD56
MD 57		3	4		MD57
DM 7		5	6		DM7
-DQS 7		7	8		-DQS7
MD 59	RN21	1	2	0/8P4R/X	MD59
MD 63		3	4		MD63
MD 58		5	6		MD58
MD 62		7	8		MD62

MD 20	RN2	1	2	0/8P4R/X	MD20
MD 16		3	4		MD16
MD 17		5	6		MD17
MD 21		7	8		MD21
MD 42	RN4	1	2	0/8P4R/X	MD42
MD 46		3	4		MD46
MD 43		5	6		MD43
MD 47		7	8		MD47

MD 7	RN8	1	2	0/8P4R/X	MD7
MD 3		3	4		MD3
MD 8		5	6		MD8
MD 9		7	8		MD9
MD 19	RN10	1	2	0/8P4R/X	MD19
MD 23		3	4		MD23
MD 24		5	6		MD24
MD 28		7	8		MD28



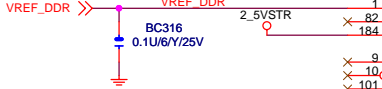
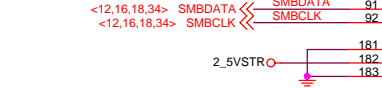
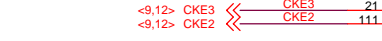
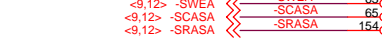
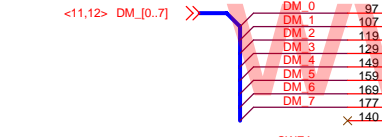
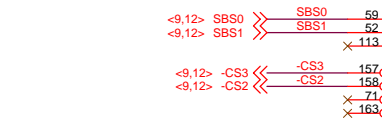
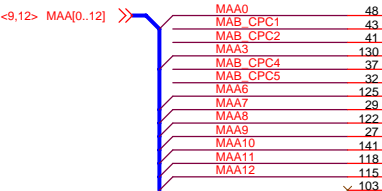
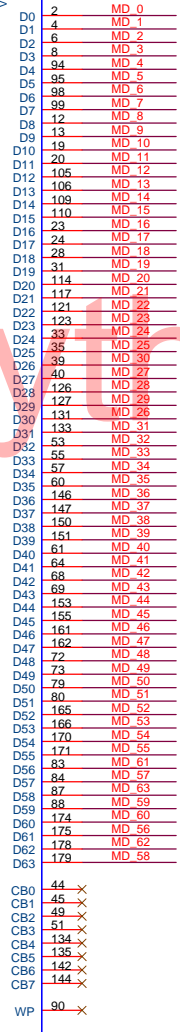
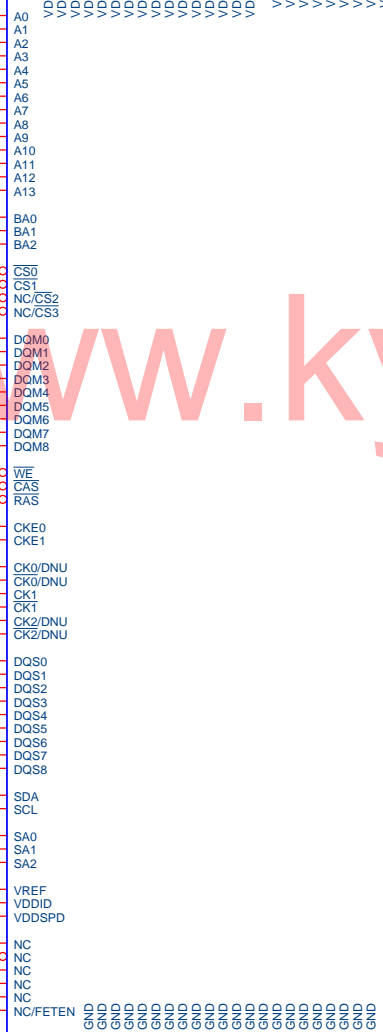
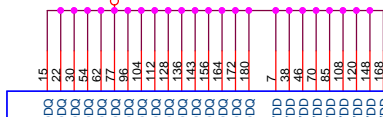
GIGABYTE			
Title			
DDR SERIAL TERM.			
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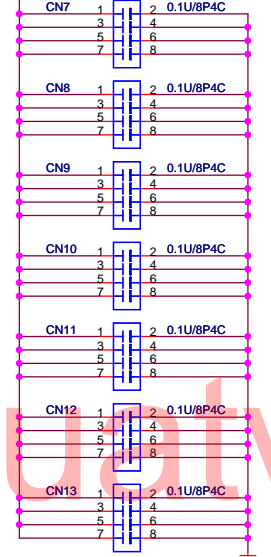
GIGABYTE			
Title	DDR1,2		
Size B	Document Number	81845GE775-G	Rev 1.0
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<9,12> MAB_CPC[1..5] <> MAB_CPC[1..5]

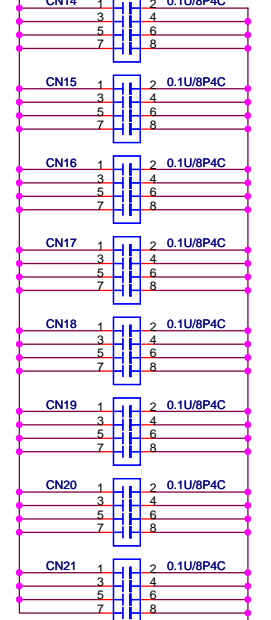
2_5VSTR



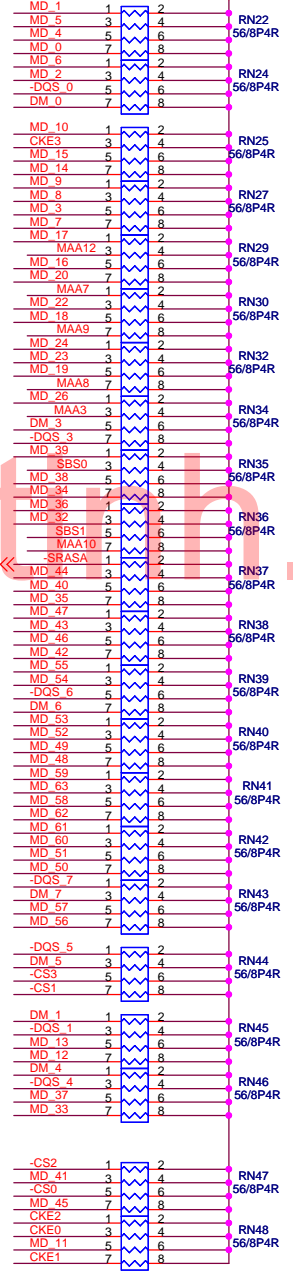
VTT_DDR



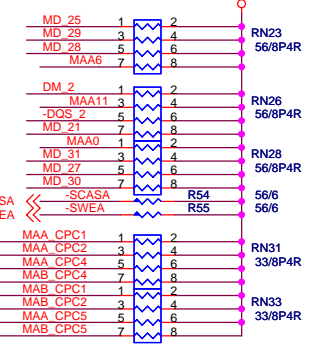
VTT_DDR



VTT_DDR



VTT_DDR



<9,12> -SCASA <> -SCASA

<9,12> -SWEA <> -SWEA

<9,12> MAA_CPC[1..5] <> MAA_CPC[1..5]

<9,12> MAB_CPC[1..5] <> MAB_CPC[1..5]

<9,12> -DQS[0..7] <> -DQS[0..7]

<9,12> MAA[0..12] <> MAA[0..12]

<9,12> -CS[0..3] <> -CS[0..3]

<9,12> CKE[0..3] <> CKE[0..3]

<9,12> SSB[0..1] <> SSB[0..1]

<9,12> MAA_CPC[1..5] <> MAA_CPC[1..5]

<9,12> MAB_CPC[1..5] <> MAB_CPC[1..5]

<9,12> -DQS[0..7] <> -DQS[0..7]

<9,12> MAA[0..12] <> MAA[0..12]

<9,12> -CS[0..3] <> -CS[0..3]

<9,12> CKE[0..3] <> CKE[0..3]

<9,12> SSB[0..1] <> SSB[0..1]

<9,12> MAA_CPC[1..5] <> MAA_CPC[1..5]

<9,12> MAB_CPC[1..5] <> MAB_CPC[1..5]

<9,12> -DQS[0..7] <> -DQS[0..7]

<9,12> MAA[0..12] <> MAA[0..12]

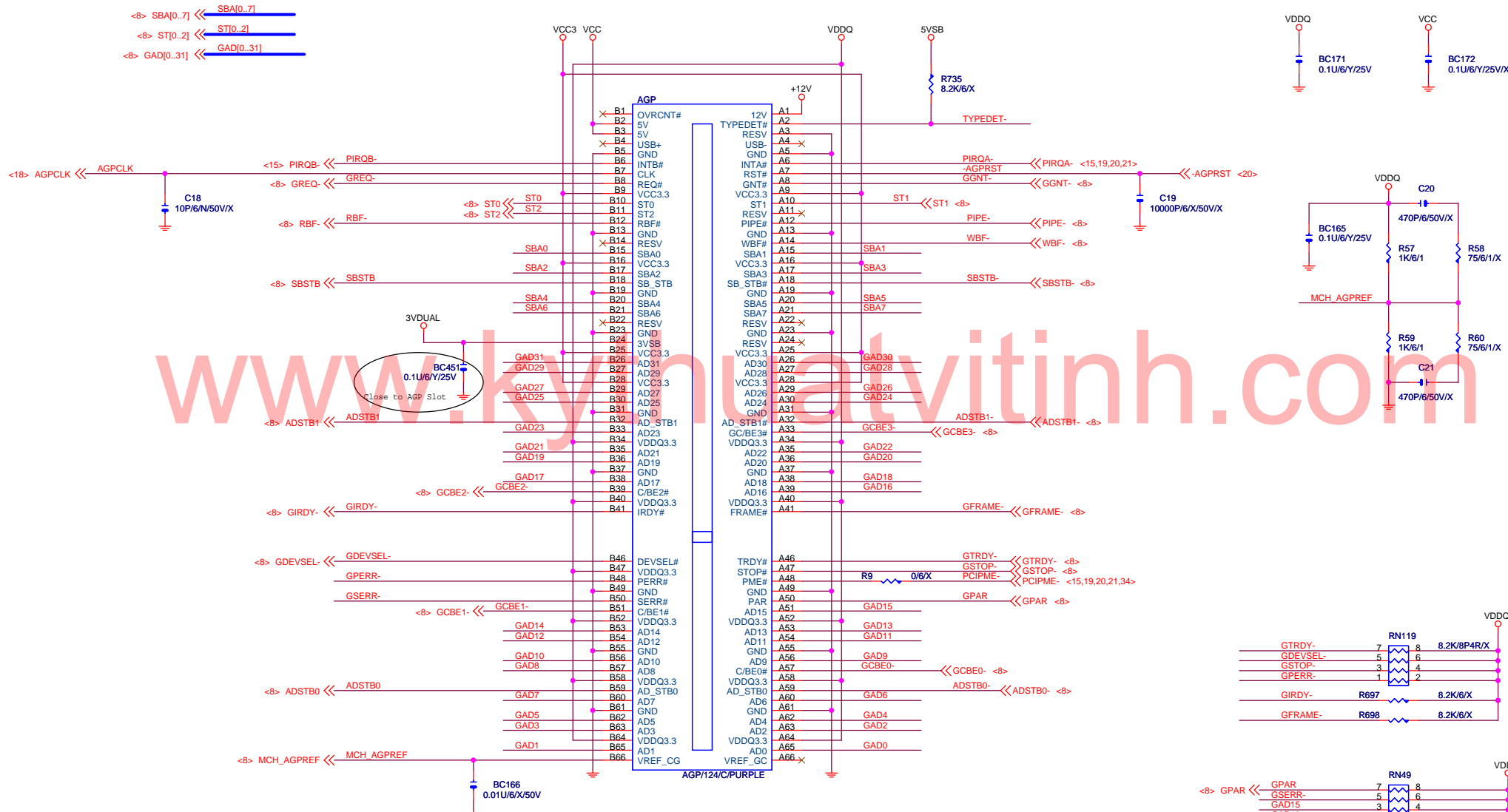
<9,12> -CS[0..3] <> -CS[0..3]

<9,12> CKE[0..3] <> CKE[0..3]

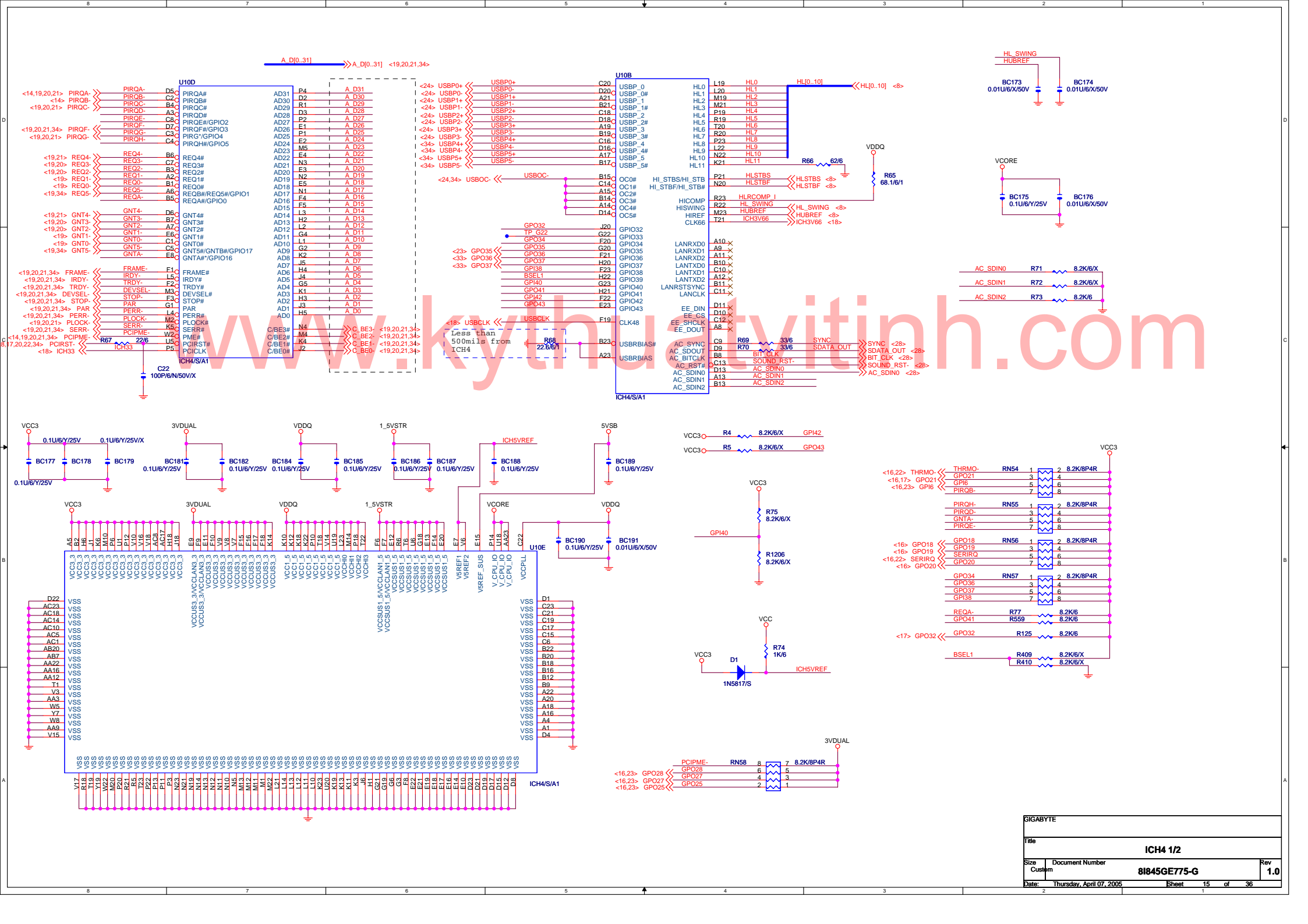
<9,12> SSB[0..1] <> SSB[0..1]

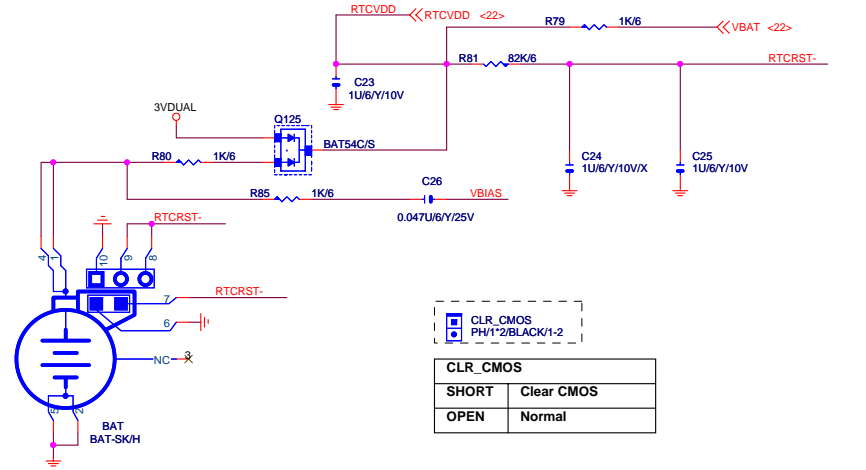
<9,12> MAA_CPC[1..5] <> MAA_CPC[1..5]

<9,12> MAB_CPC[1..5] <> MAB_CPC[1..5]

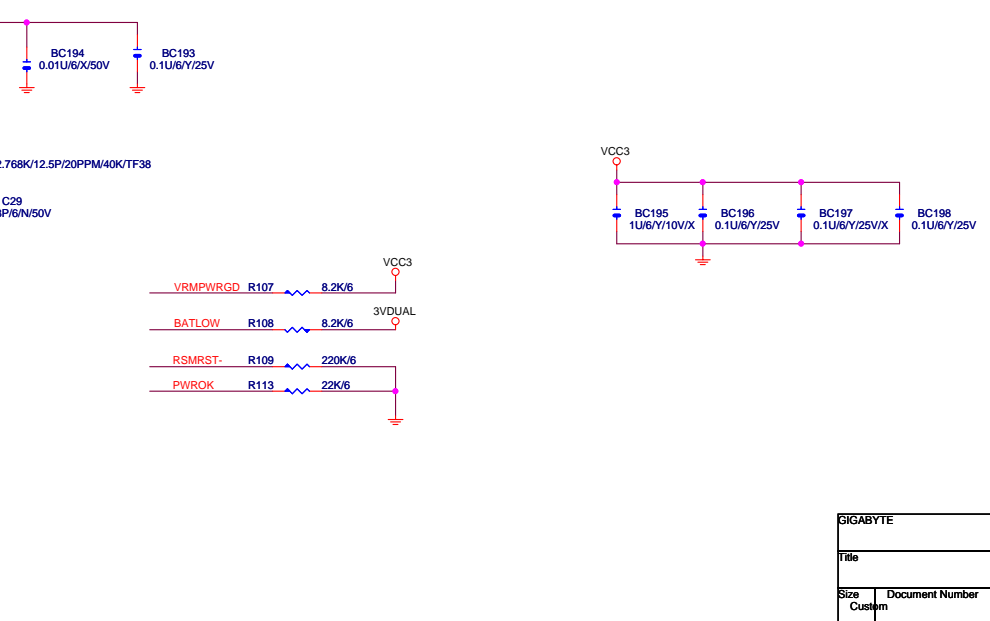
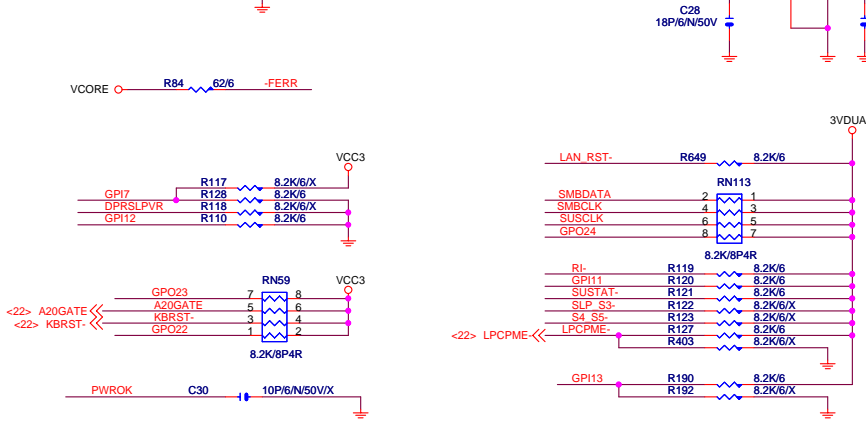
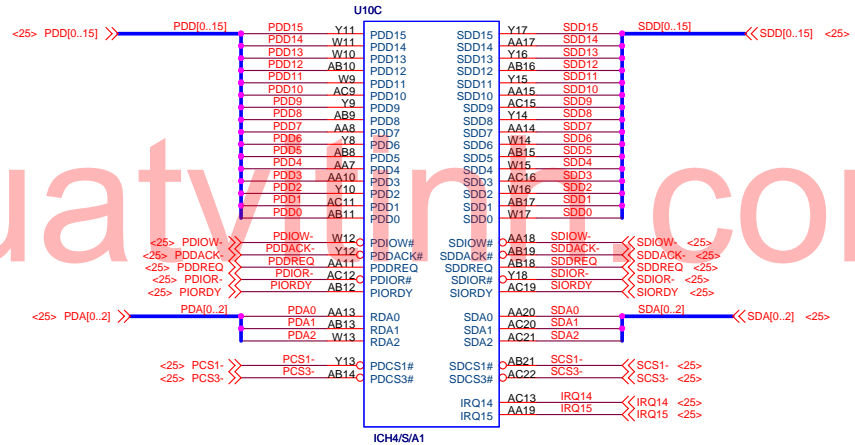
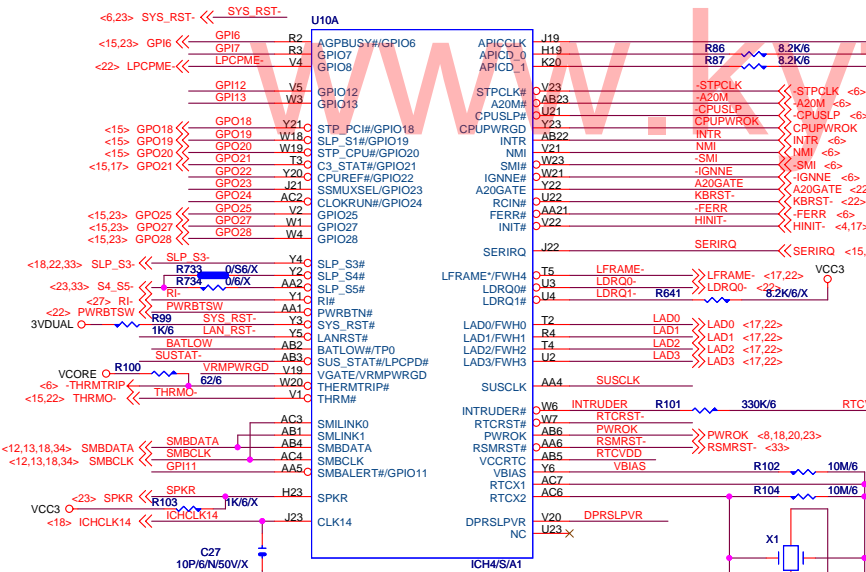


GIGABYTE			
Title			
AGP SLOT			
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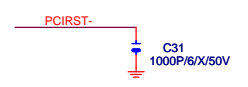
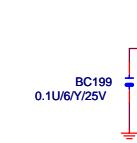
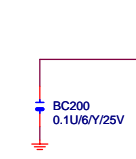
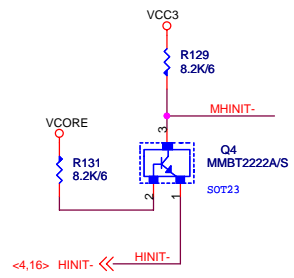




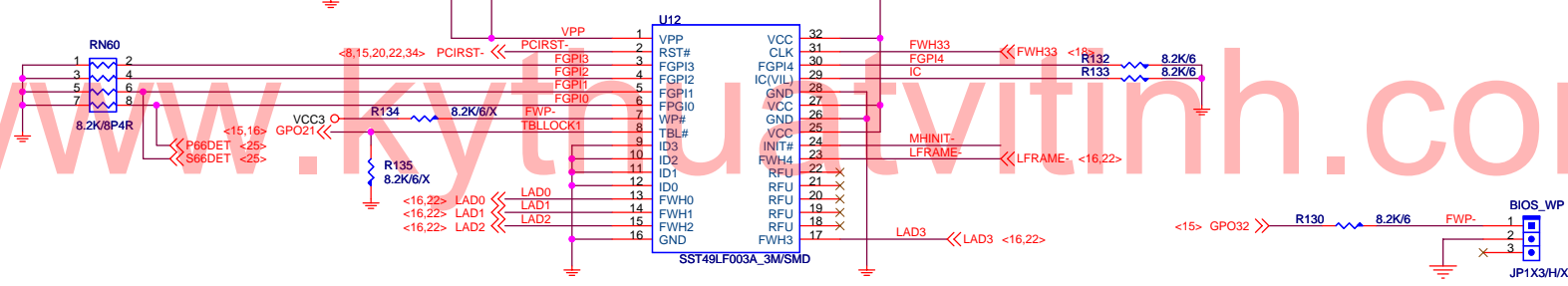
CLR_CMOS	
SHORT	Clear CMOS
OPEN	Normal



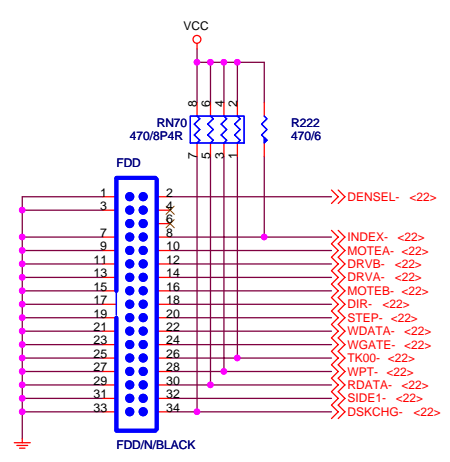
GIGABYTE			
Title			
IC4 2/2			
Size	Document Number	Rev	
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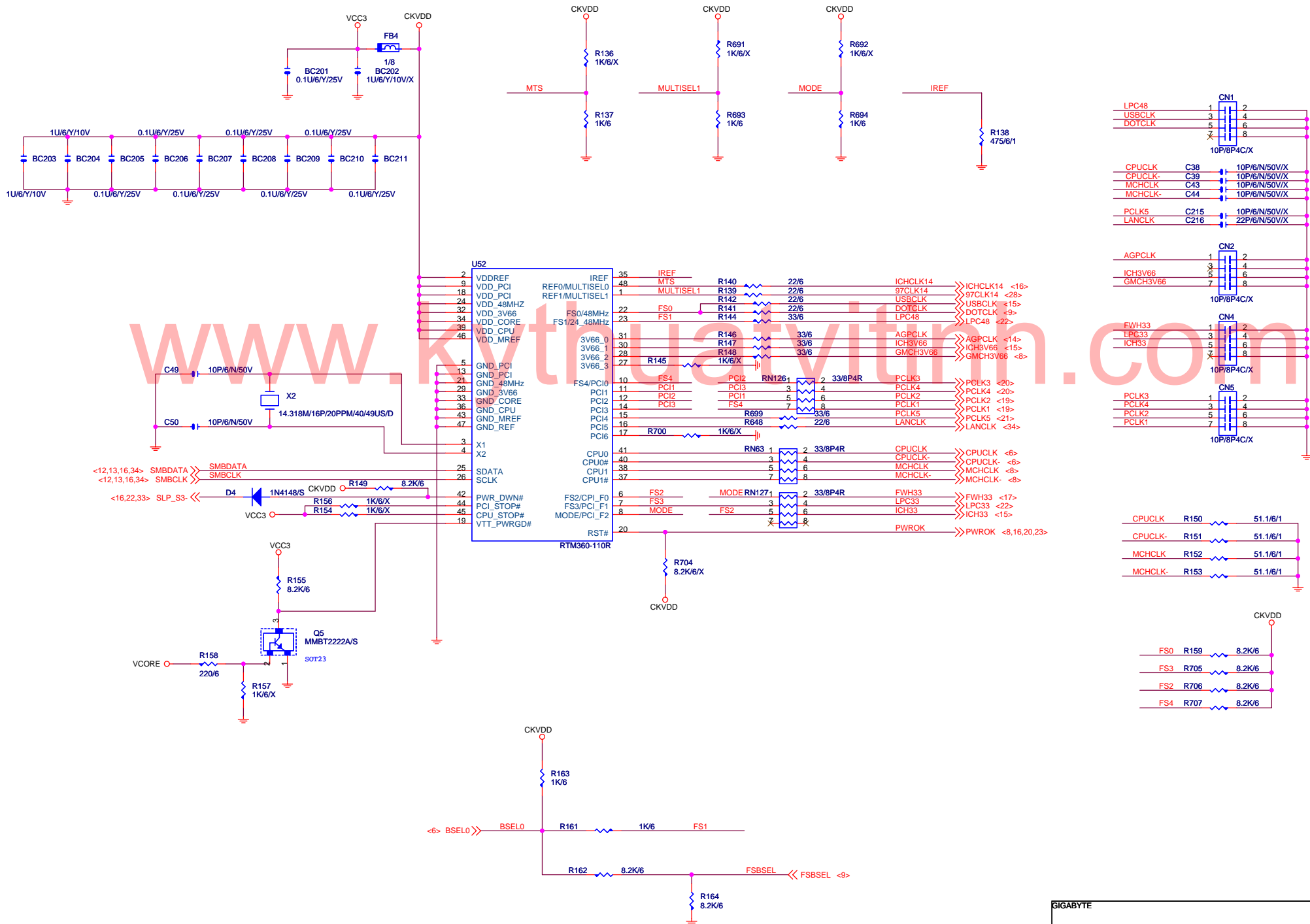
www.kytrivinh.com



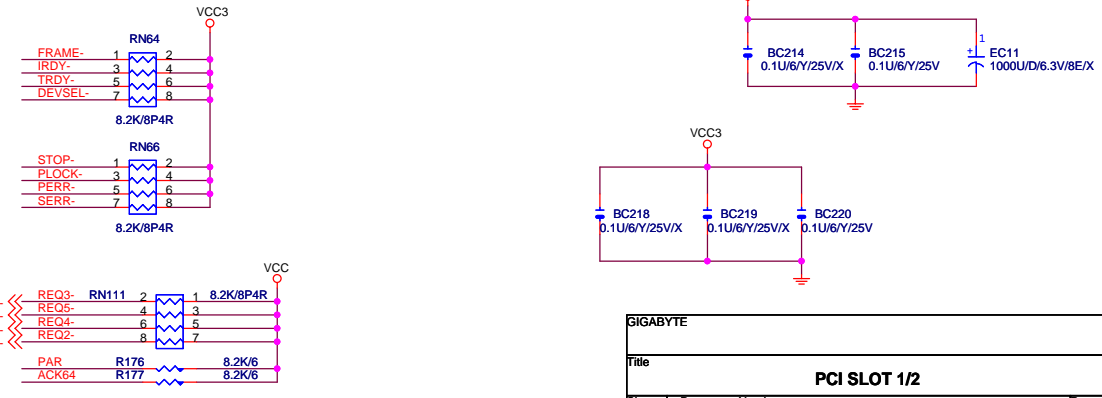
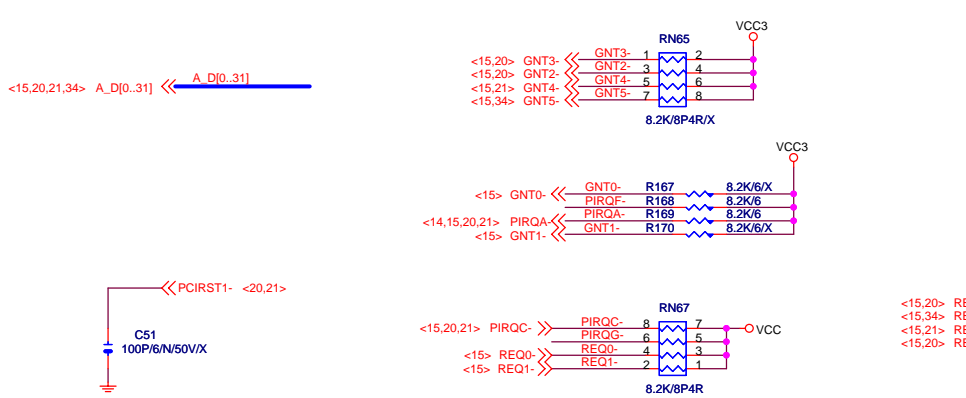
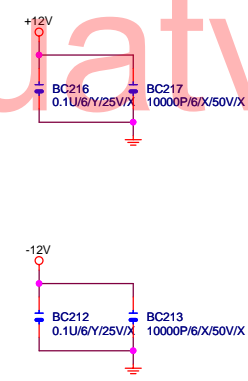
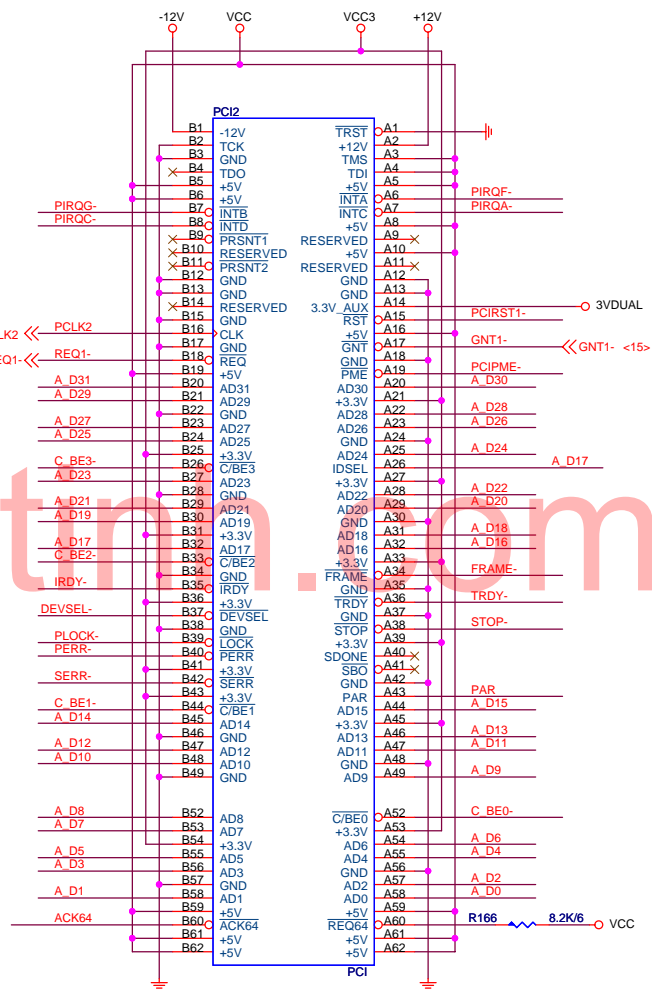
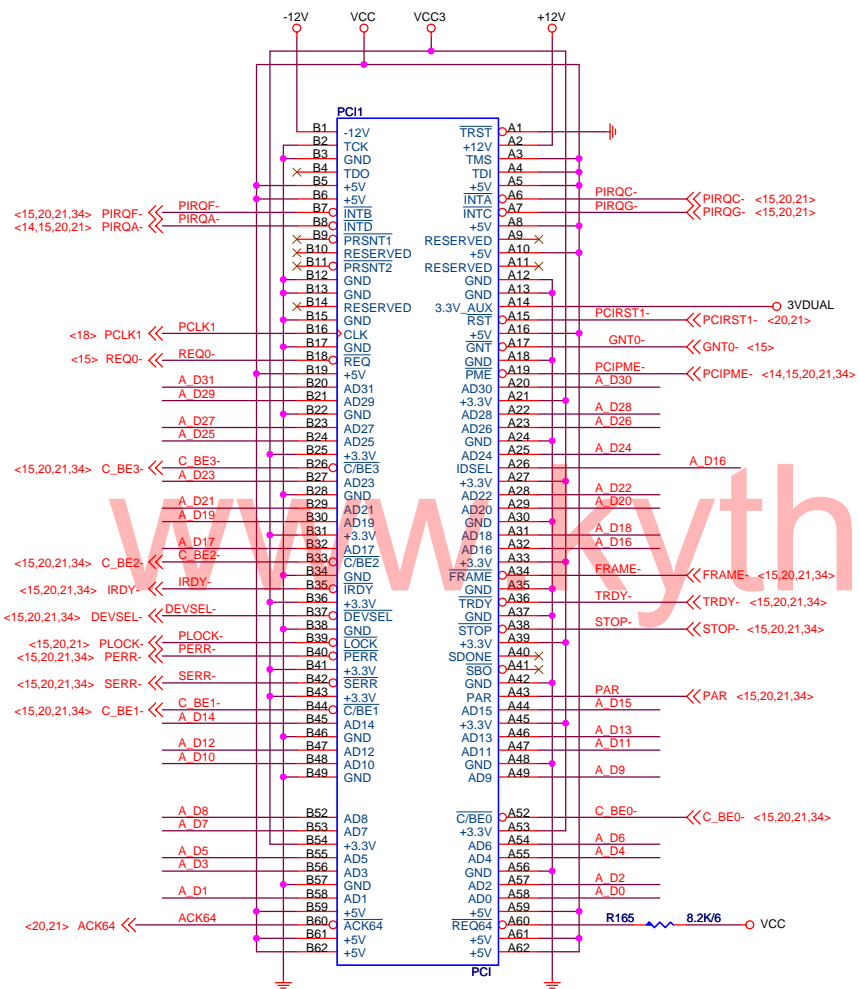
1-2 : WRITE PROTECT
2-3 : DISABLE



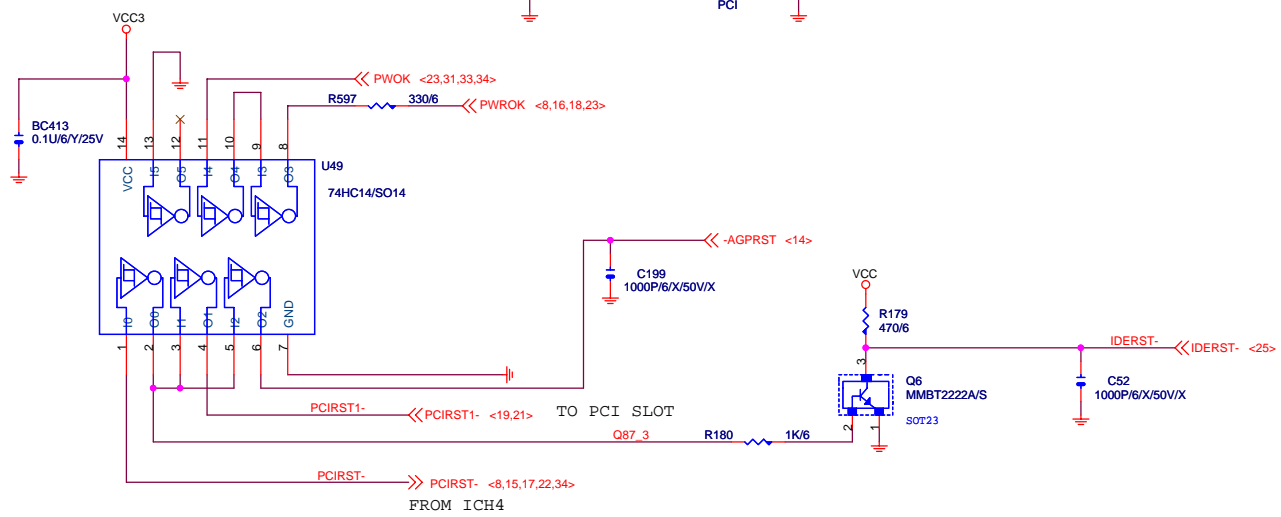
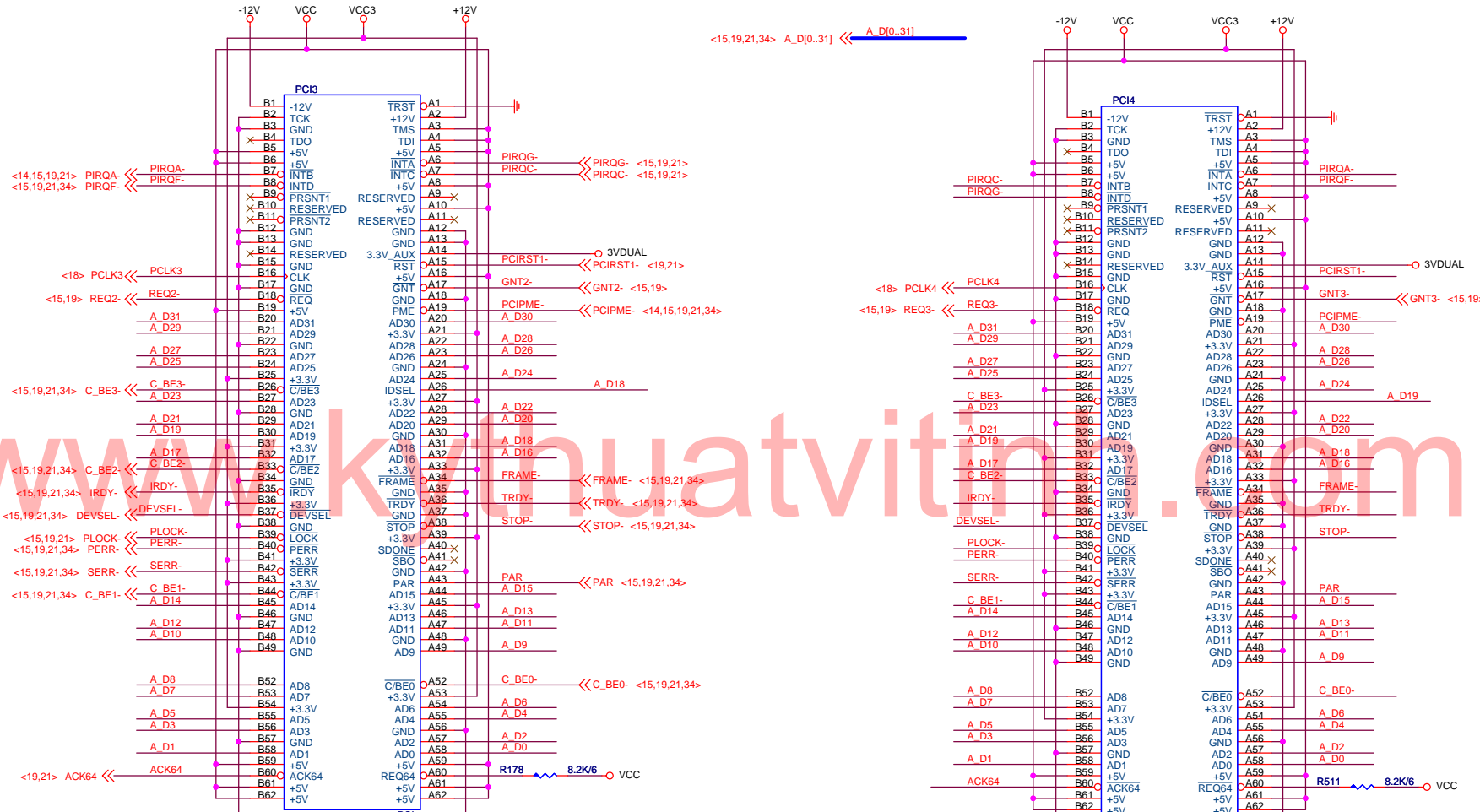
GIGABYTE		
Title		
FWH		
Size B	Document Number	Rev
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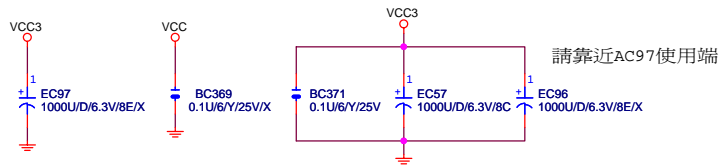
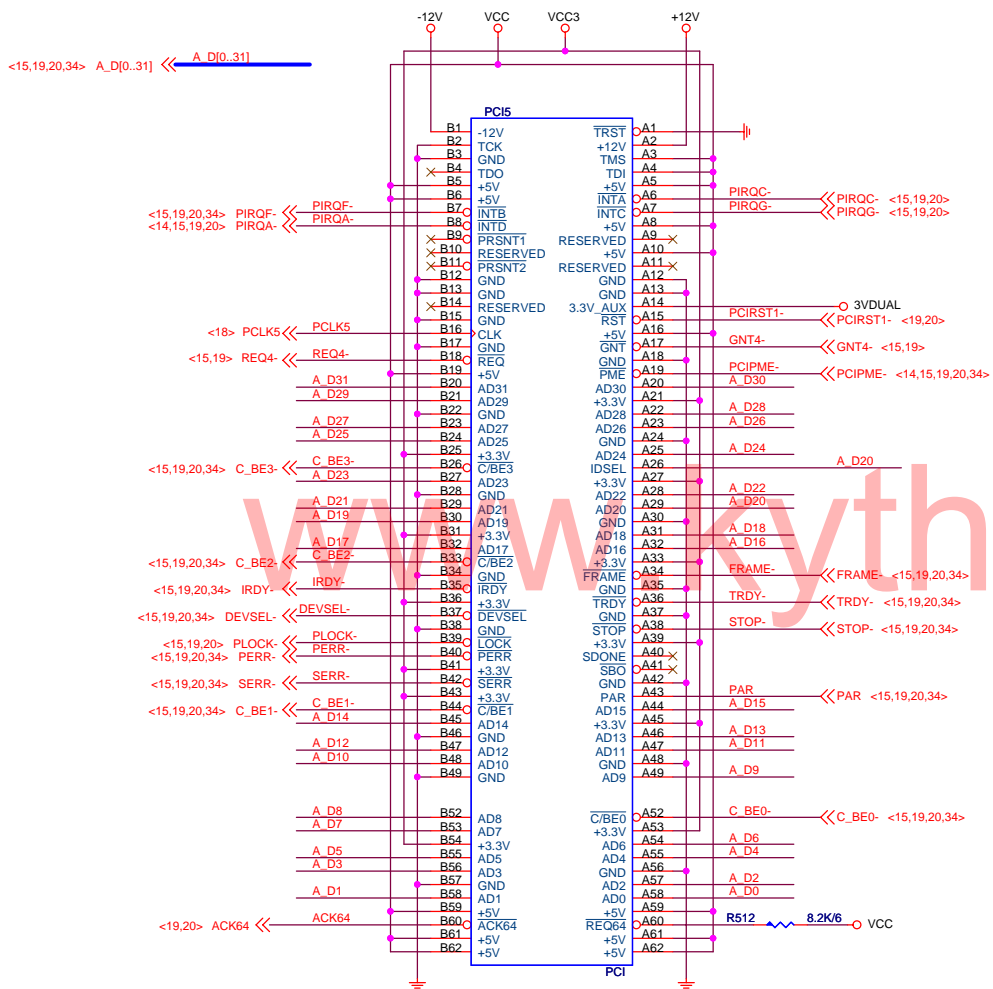
GIGABYTE			
Title			
CLOCK GENERATOR			
Size	Document Number	Rev	
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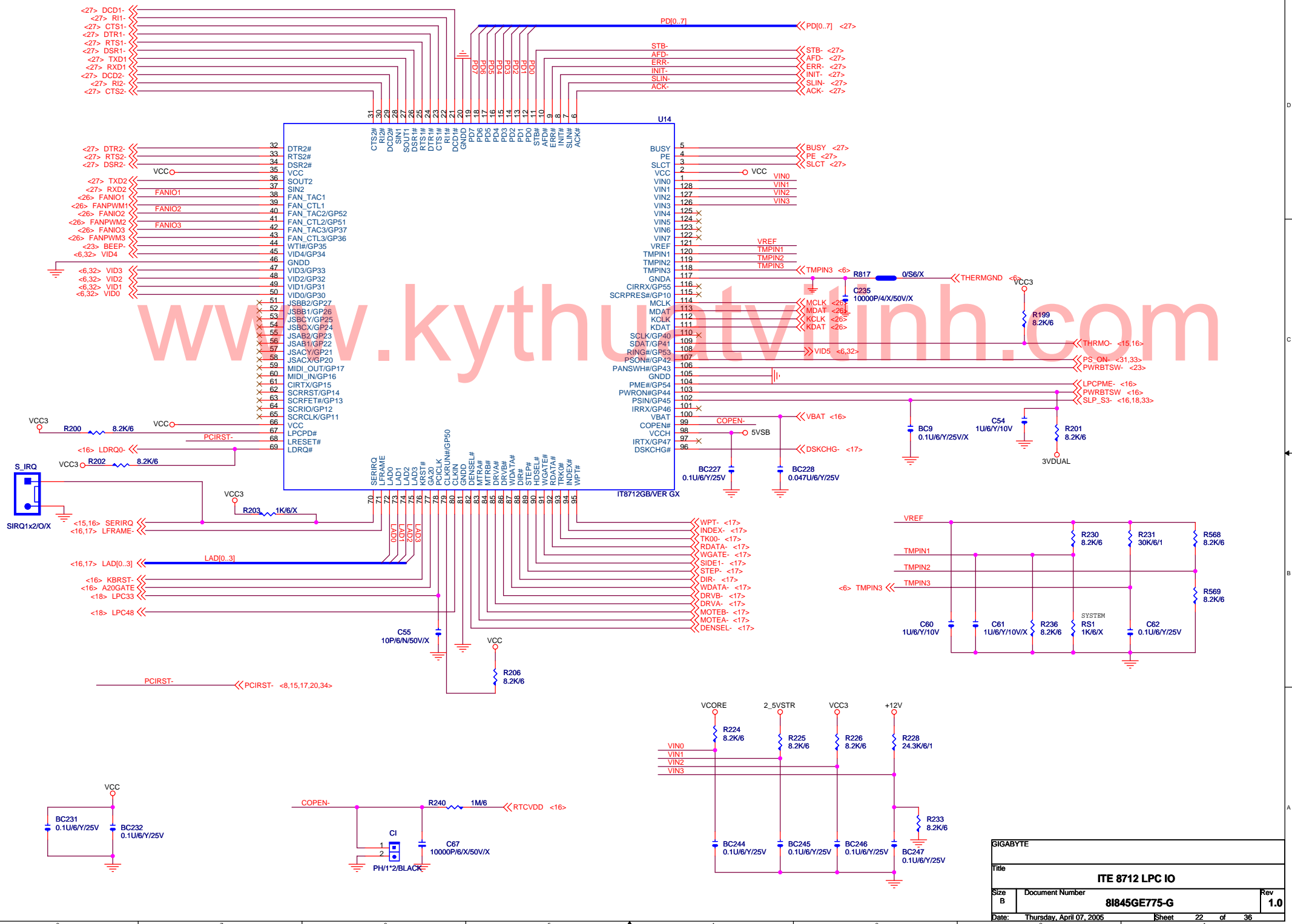
GIGABYTE		
Title		
PCI SLOT 1/2		
Size B	Document Number	Rev
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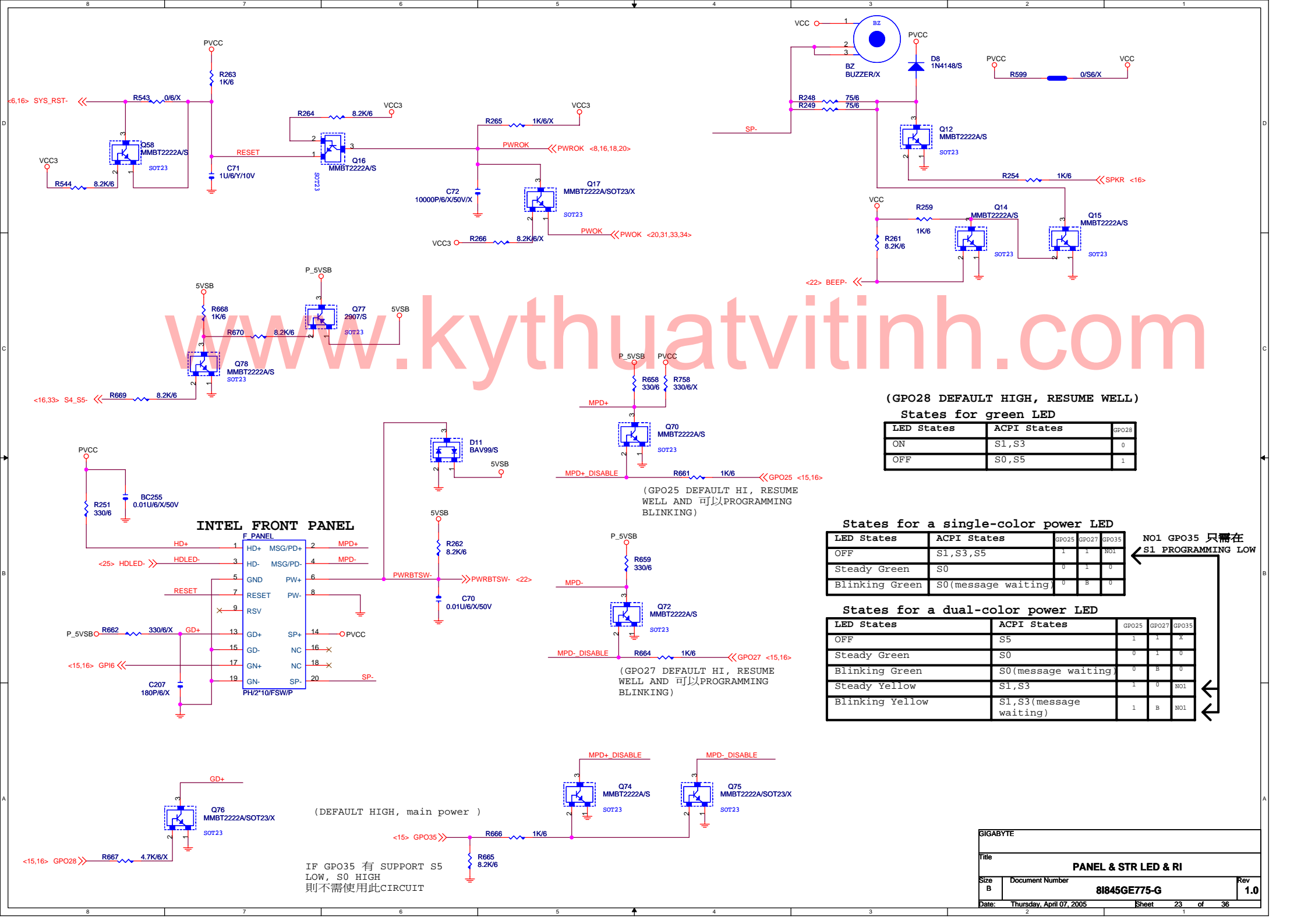
GIGABYTE		
Title		PCI SLOT 3/4
Size B	Document Number	81845GE775-G
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		Rev 1.0



GIGABYTE		
Title		
PCI SLOT 5		
Size	Document Number	Rev
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GIGABYTE		
Title		
ITE 8712 LPC IO		
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(GPO28 DEFAULT HIGH, RESUME WELL)
States for green LED

LED States	ACPI States	GPO28
ON	S1, S3	0
OFF	S0, S5	1

States for a single-color power LED

LED States	ACPI States	GPO25	GPO27	GPO35
OFF	S1, S3, S5	1	1	NO1
Steady Green	S0	0	1	0
Blinking Green	S0 (message waiting)	0	B	0

NO1 GPO35 只需在 S1 PROGRAMMING LOW

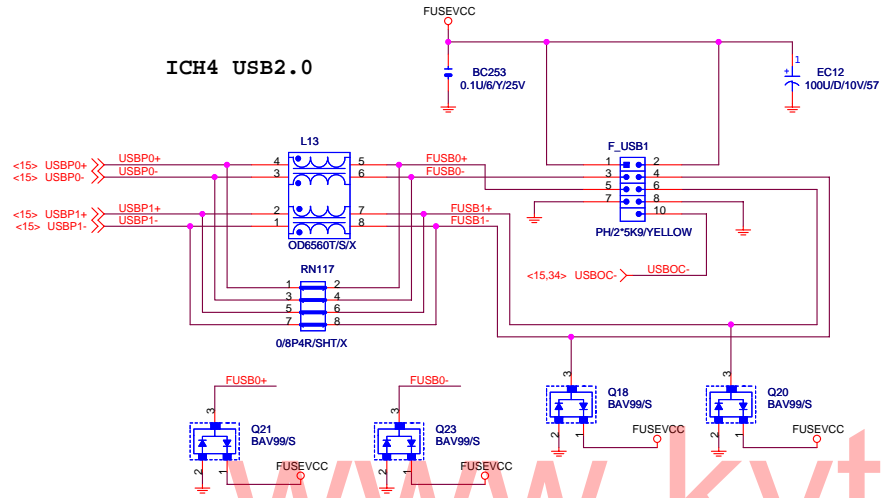
States for a dual-color power LED

LED States	ACPI States	GPO25	GPO27	GPO35
OFF	S5	1	1	X
Steady Green	S0	0	1	0
Blinking Green	S0 (message waiting)	0	B	0
Steady Yellow	S1, S3	1	0	NO1
Blinking Yellow	S1, S3 (message waiting)	1	B	NO1

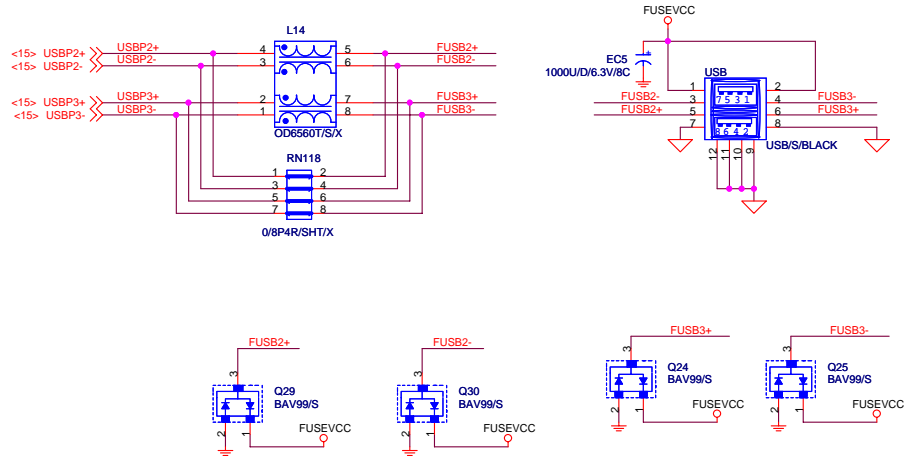
(DEFAULT HIGH, main power)

IF GPO35 有 SUPPORT S5 LOW, S0 HIGH 則不需使用此CIRCUIT

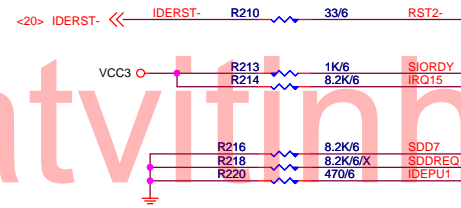
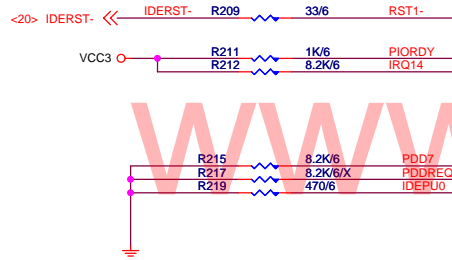
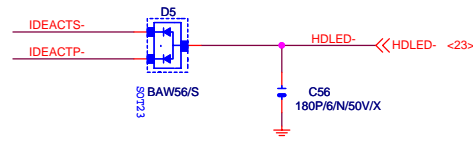
ICH4 USB2.0



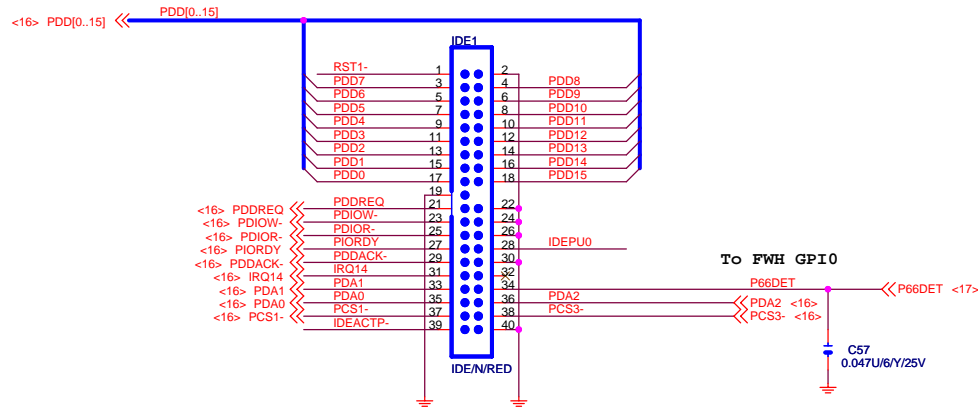
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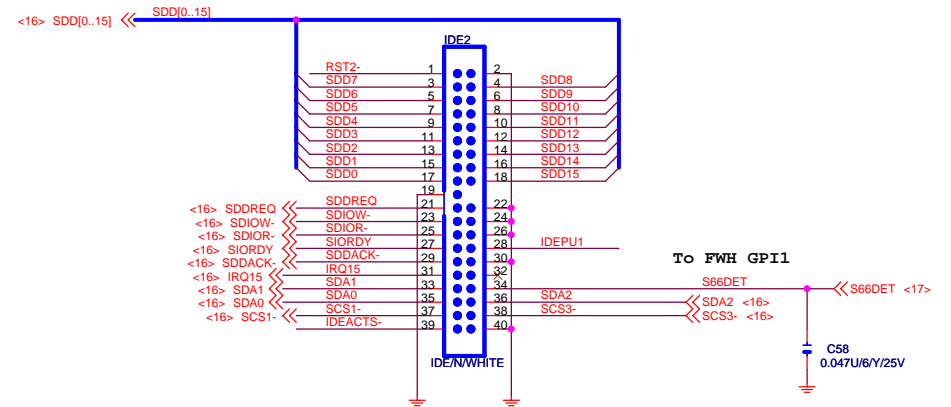
GIGABYTE			
Title			
ICH USB+LAN PORT			
Size	Document Number	Rev	
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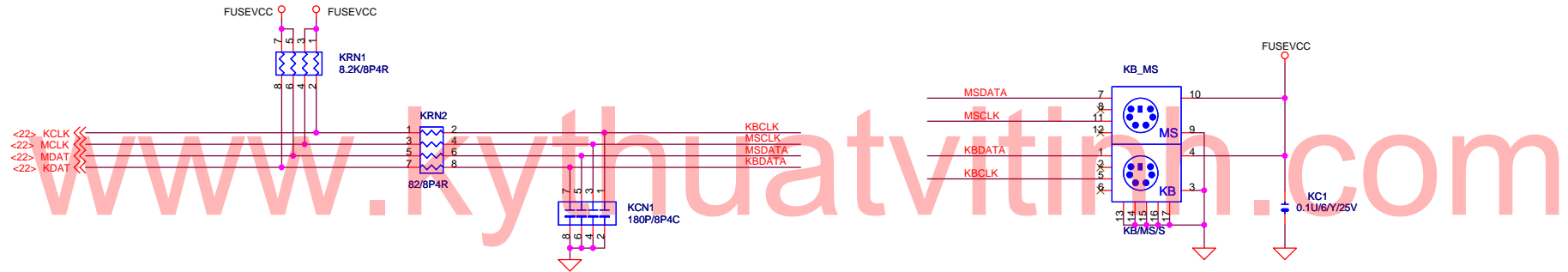
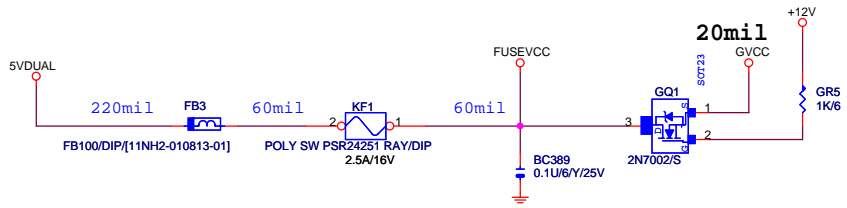
PRIMARY IDE CONNECTOR



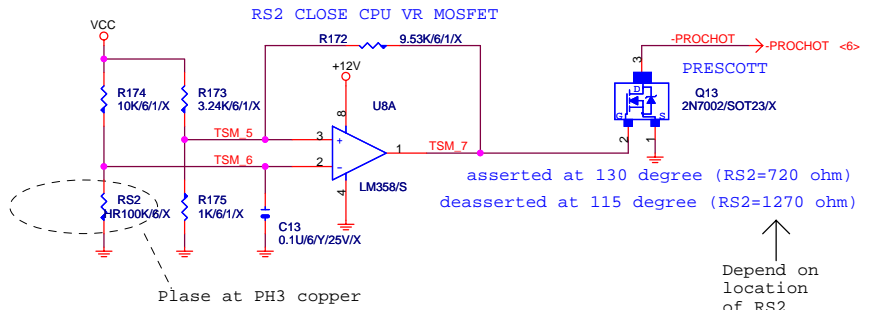
SECONDARY IDE CONNECTOR



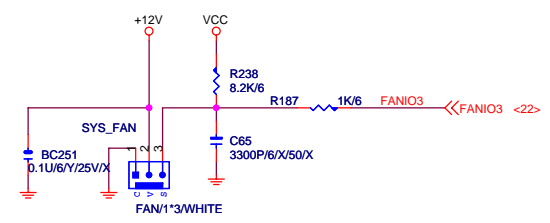
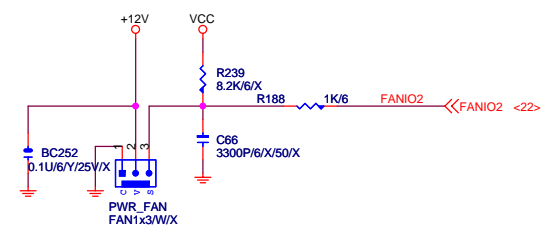
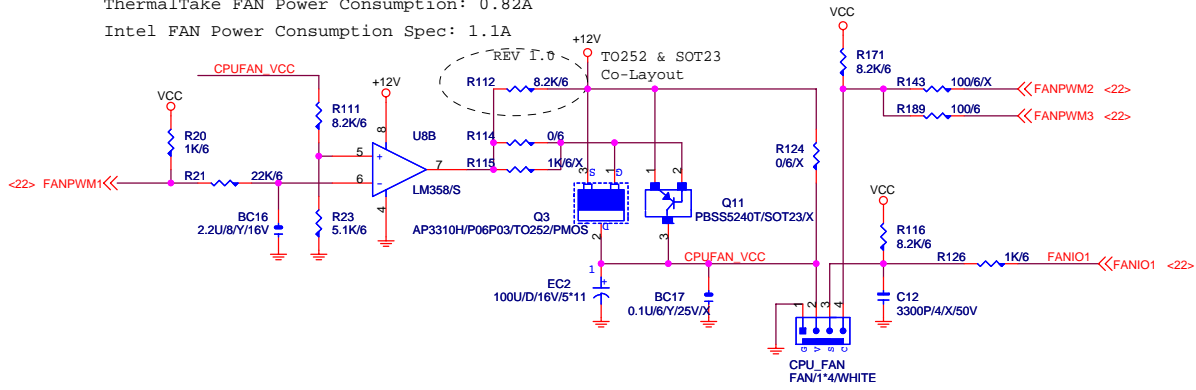
GIGABYTE		
Title		
IDE CONNECTOR		
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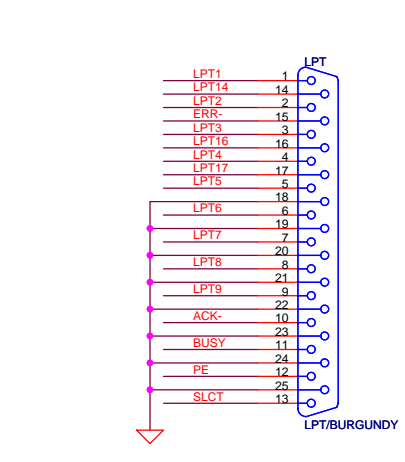
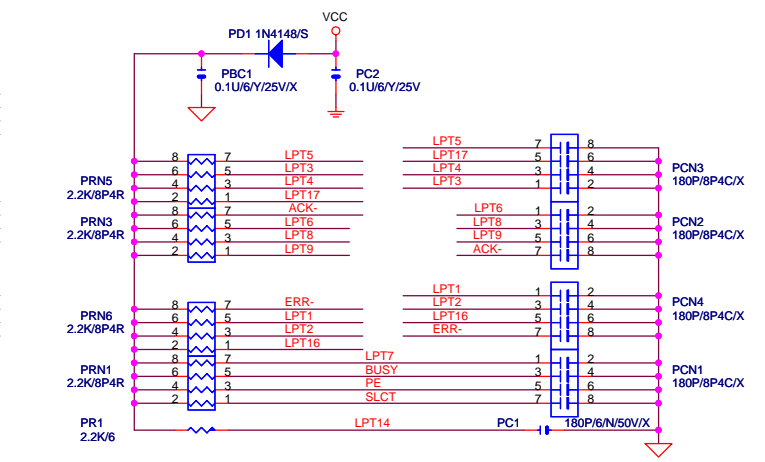
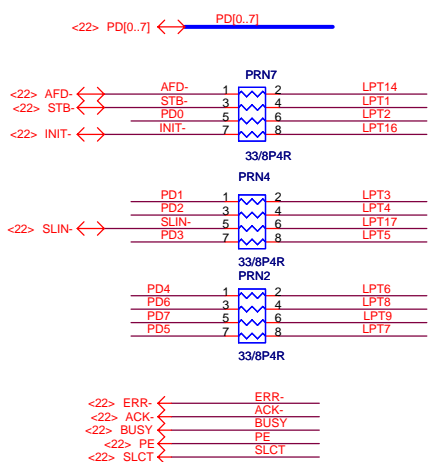
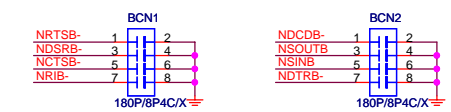
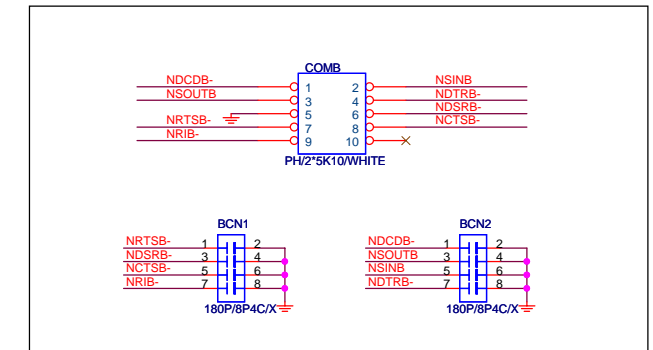
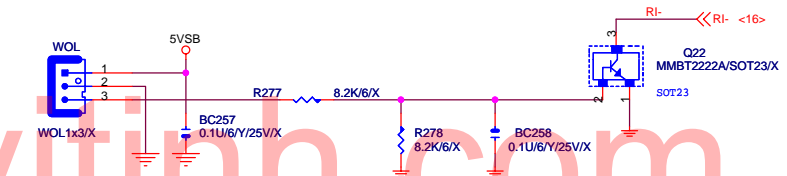
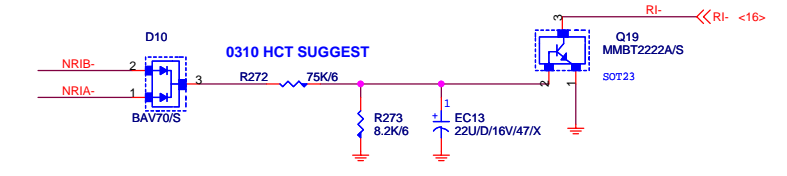
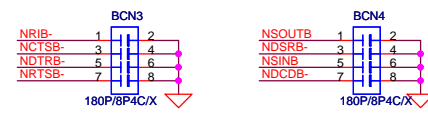
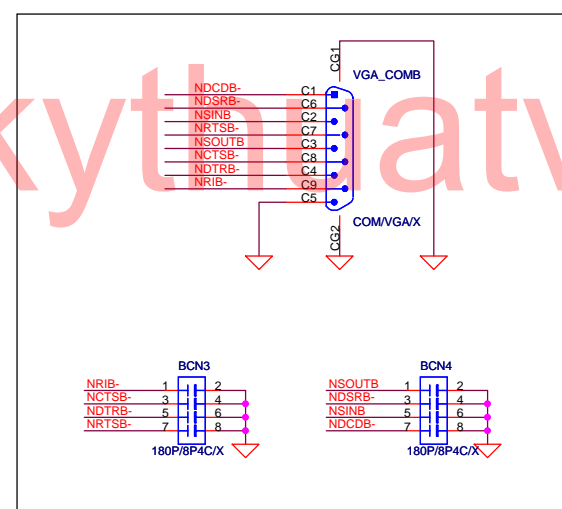
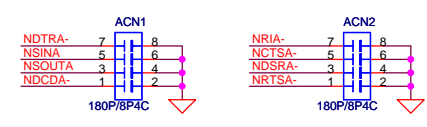
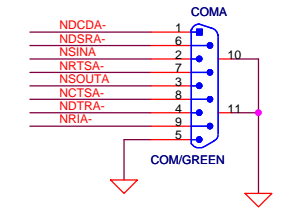
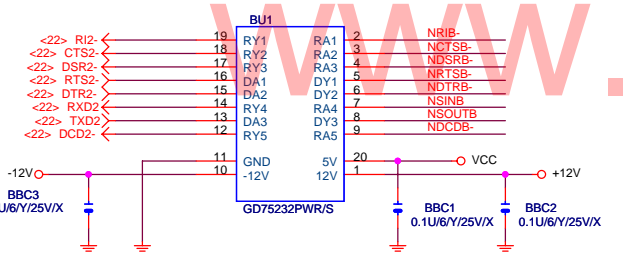
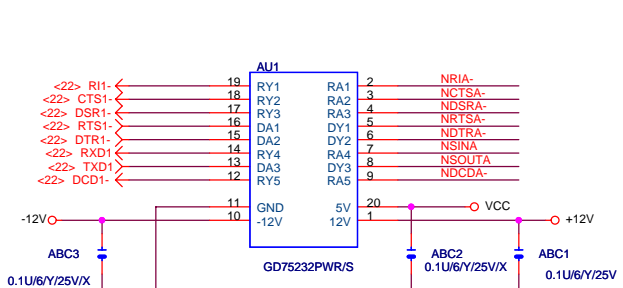
PROCESSOR HOT U-ATX PROCESSOR HOT NO POP



ThermalTake FAN Power Consumption: 0.82A
Intel FAN Power Consumption Spec: 1.1A



GIGABYTE		
Title		
KB & PS2 MOUSE & IR		
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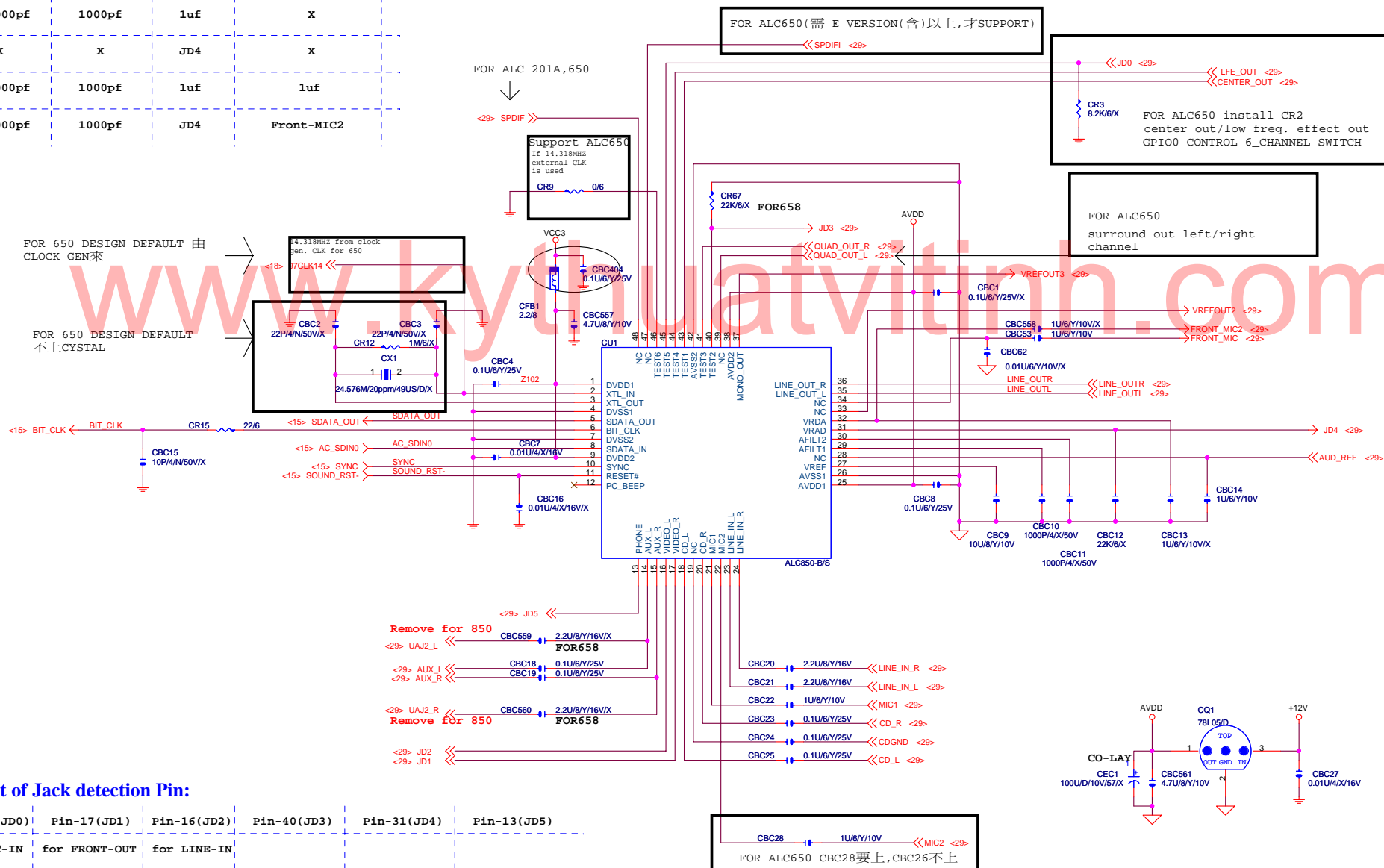


模組化線路

GIGABYTE		
Title COM & IR & LPT PORT & FLOOPY		
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Filter Cap design:

	Pin-29	Pin-30	Pin-31	Pin-32
ALC655 Rev D	1000pf	1000pf	1uf	Front-MIC2
ALC655 Rev C	1000pf	1000pf	1uf	X
ALC658	X	X	JD4	X
ALC650	1000pf	1000pf	1uf	1uf
ALC850	1000pf	1000pf	JD4	Front-MIC2

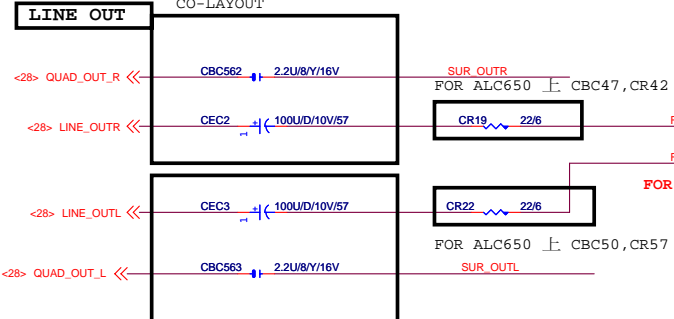


Arrangement of Jack detection Pin:

	Pin-45(JD0)	Pin-17(JD1)	Pin-16(JD2)	Pin-40(JD3)	Pin-31(JD4)	Pin-13(JD5)
ALC655	for MIC-IN	for FRONT-OUT	for LINE-IN			
ALC658	for MIC-IN	for UAJ1	for UAJ2	for FRONT-OUT External pull high is needed	for LINE-IN External pull high is needed	
ALC850	for MIC-IN	for Front Pannel OUT	for Front Pannel IN	for FRONT-OUT	for LINE-IN	for SurrBack Out

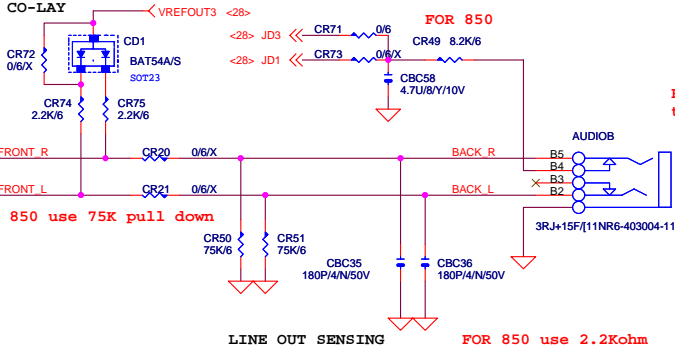
JDO,JD2,GPIO0 為偵測DEVICE INPUT 時由LOW TO HIGH Edge trigger(pop manual)

注意CBC47,CBC48不可 CO-LAYOUT



注意CBC49,CBC50不可 CO-LAYOUT

1/2(3.14)RC=1/2(3.14)8.2K*4.7U=4.3HZ以上AC 信號全部衰減 TO 0V 不會造成JDO 誤動作(無device 時play wav)



LINE OUT SENSING
R>4K OHM=>POWER SPEAKER
4K OHM>R>400 OHM=>MICROPHONE
R<400 OHM=>HEADPHONE

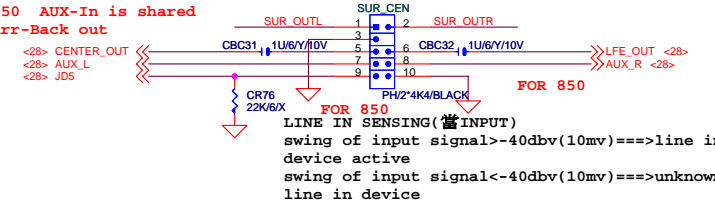
2x5 header for 850

For 850 if JD5 = low AUX-In is configured as input
For 850 if JD5 = high AUX-In is configured as output, Surr-Back out

FOR SUPPORT 6 CHANNEL, SURROUND OUT

CENTER OUT, LOW FREQUENCY EFFECT OUT

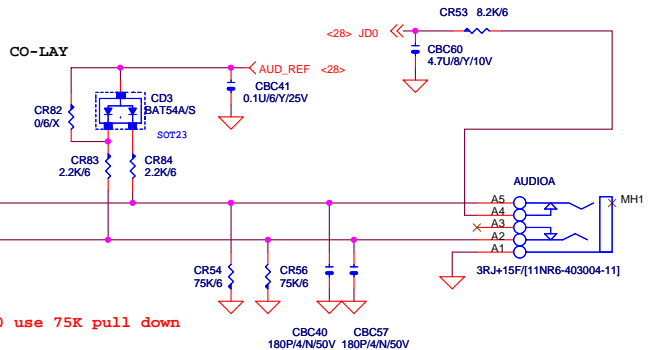
For 850 AUX-In is shared to Surr-Back out



FOR 850 LINE IN SENSING (當INPUT) swing of input signal<-40dbv(10mv)===>line in device active swing of input signal<-40dbv(10mv)===>unknown line in device

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MIC



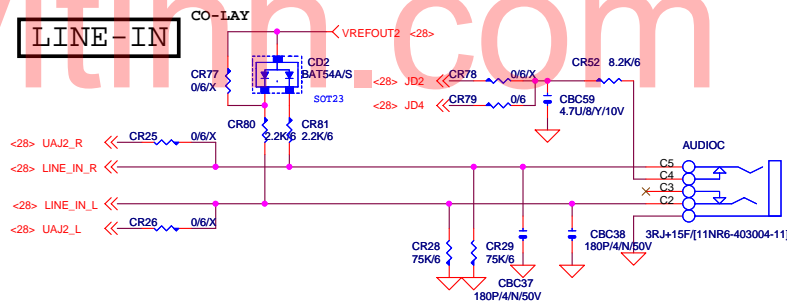
FOR 850 use 75K pull down

MICROPHONE IN SENSING (當INPUT) (利用vref 偏壓與CR43,CR32 並聯求出阻抗)
7.1k ohm>R>2.3k ohm===>microphone in
R<2.3k ohm or R>7.1k ohm===>unknown device

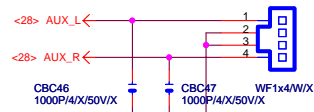
MICROPHONE IN SENSING (當OUTPUT)
R>4K OHM=>POWER SPEAKER
4K OHM>R>400 OHM=>MICROPHONE
R<400 OHM=>HEADPHONE

LINE-IN

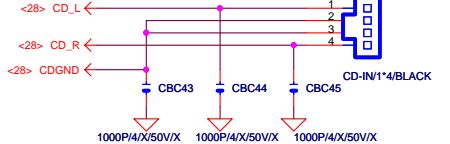
LINE IN SENSING (當OUTPUT)
R>4K OHM=>POWER SPEAKER
4K OHM>R>400 OHM=>MICROPHONE
R<400 OHM=>HEADPHONE



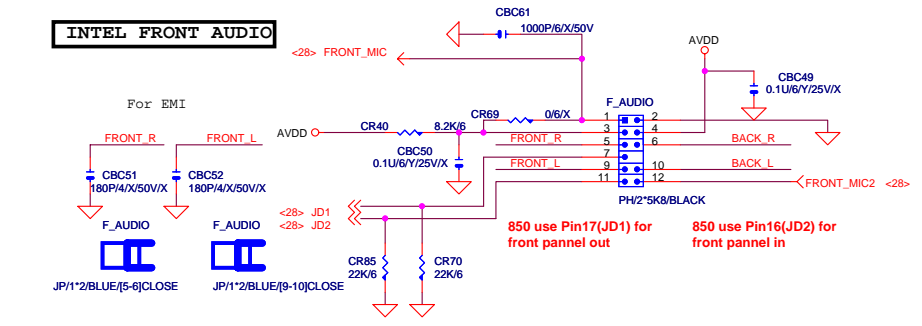
AUX IN DEFAULT NO POP



CD IN



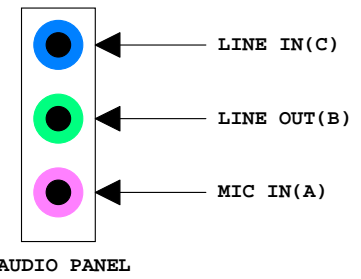
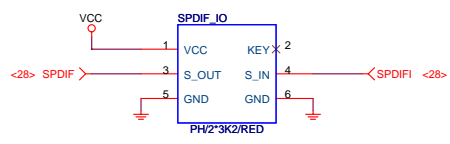
INTEL FRONT AUDIO



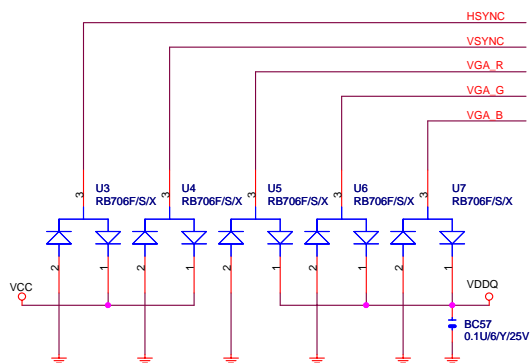
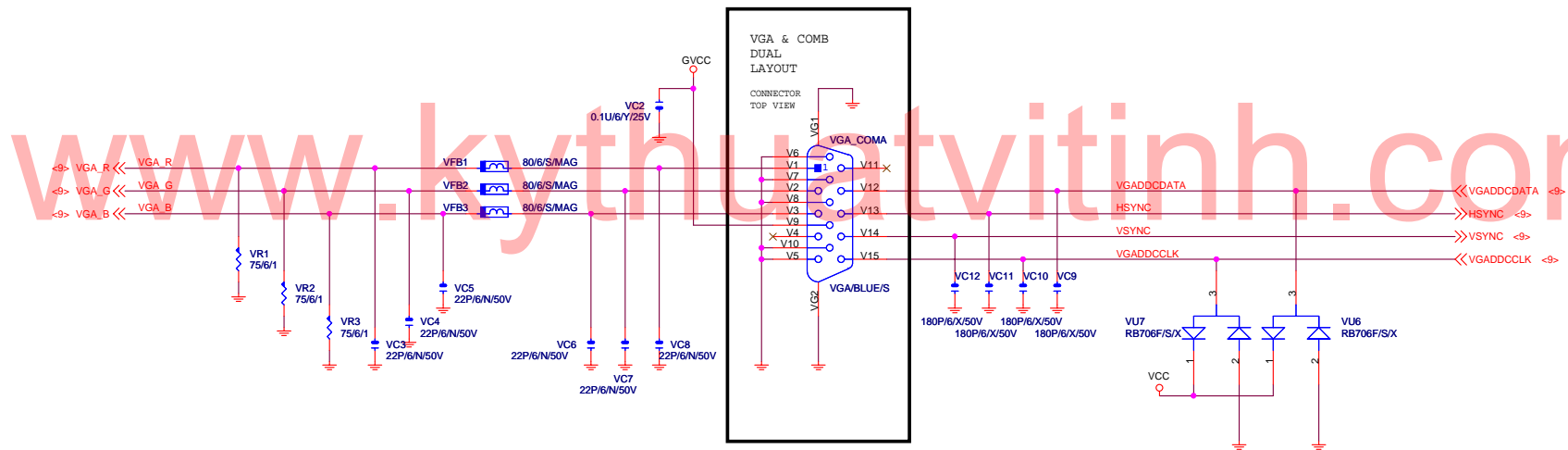
850 use Pin17(JD1) for front pannel out

850 use Pin16(JD2) for front pannel in

SPDIF



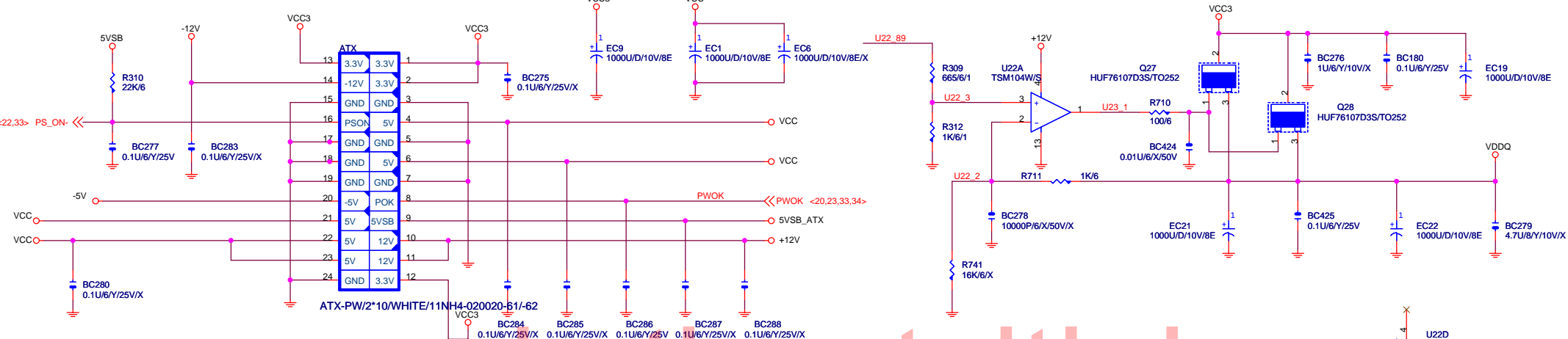
GIGABYTE		
AC97 OUTPUT, GAME PORT		
Title	Document Number	Rev
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模組化線路

Title			VGA CONNECTOR		
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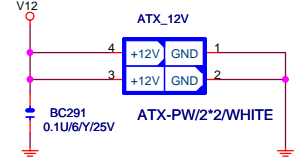
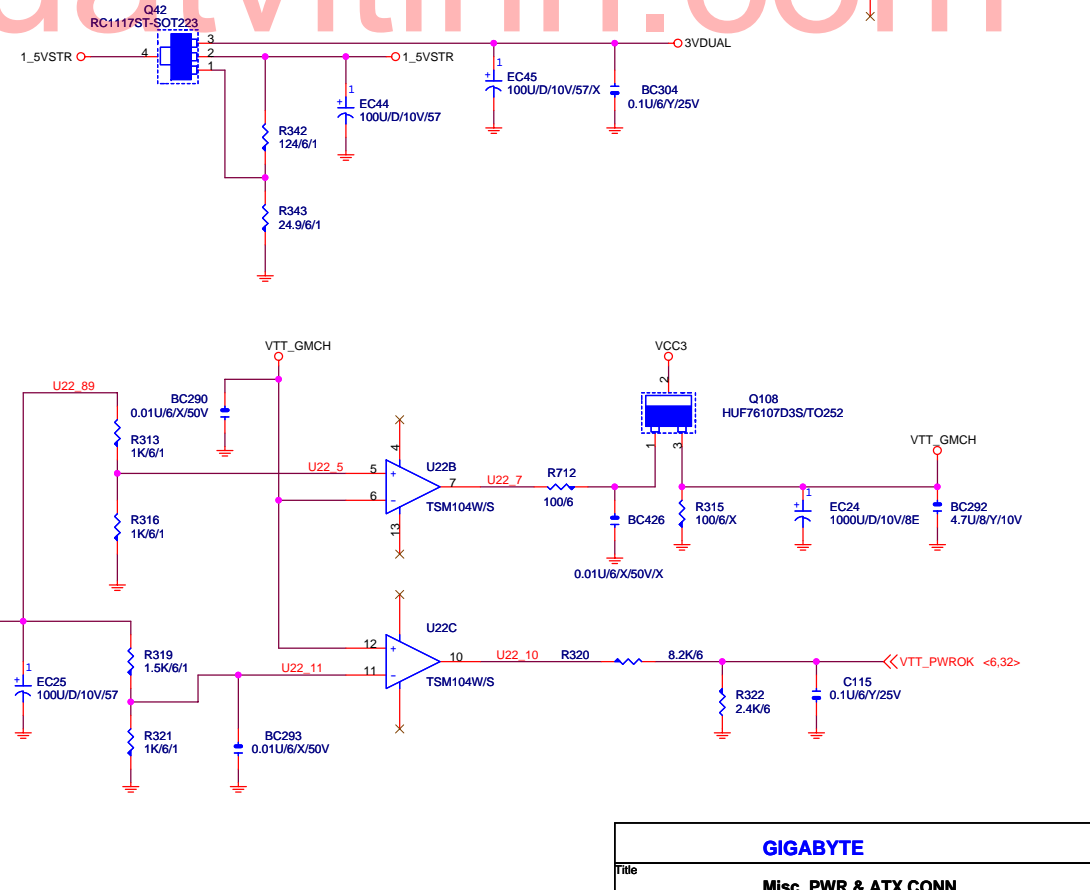
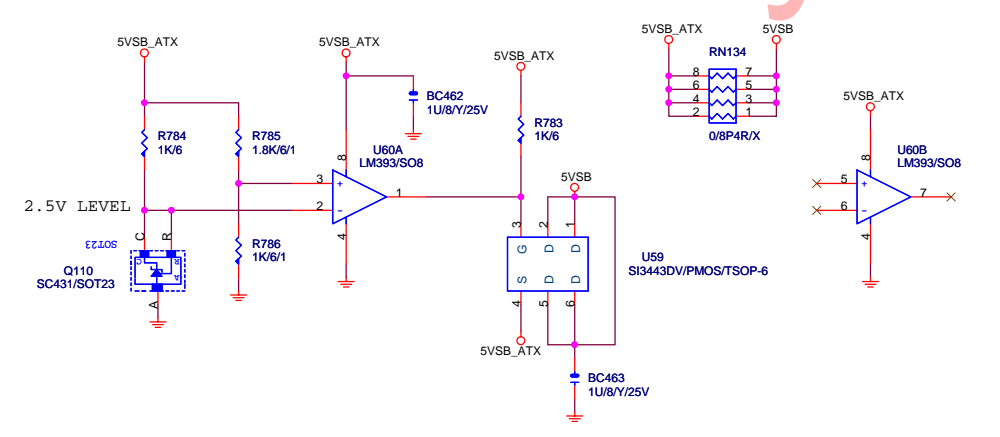
ATX POWER CONNECTOR



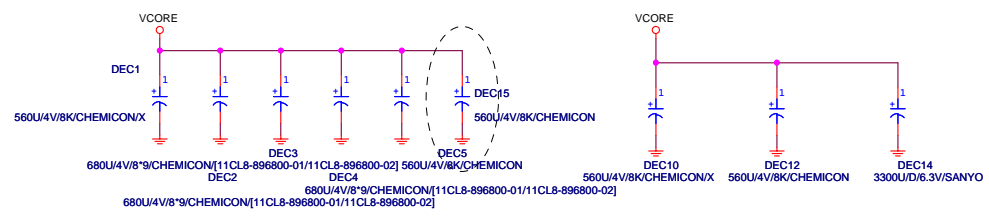
11NH4-020020-61/62

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S.P.R Circuit

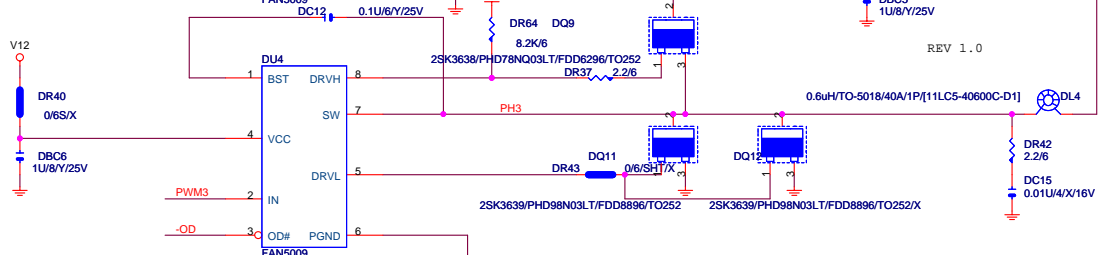
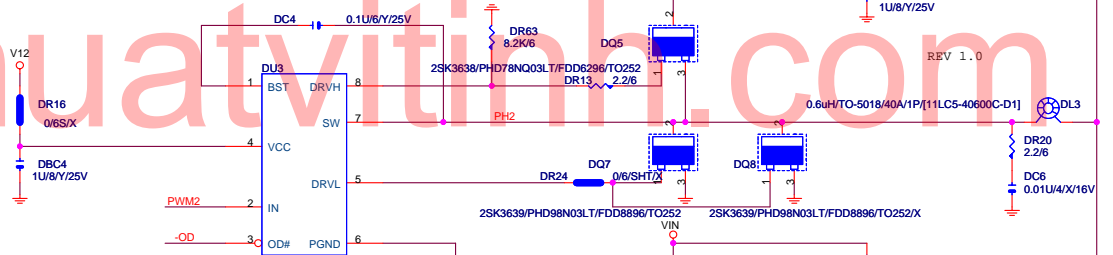
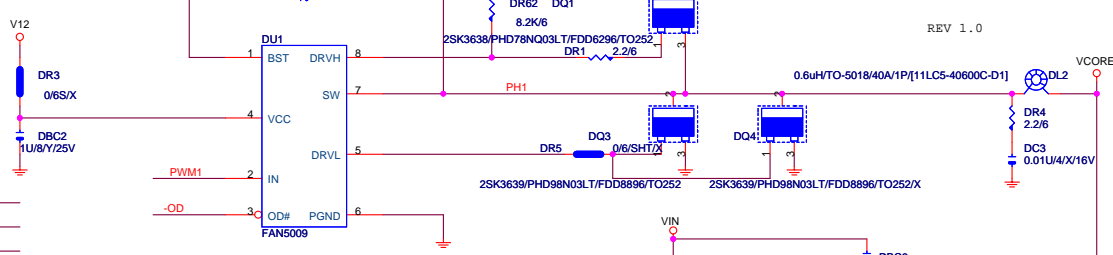
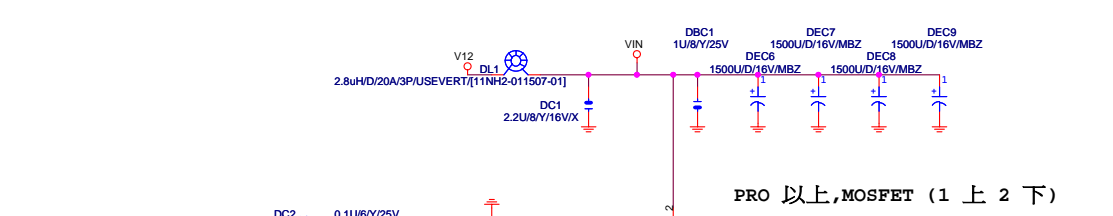
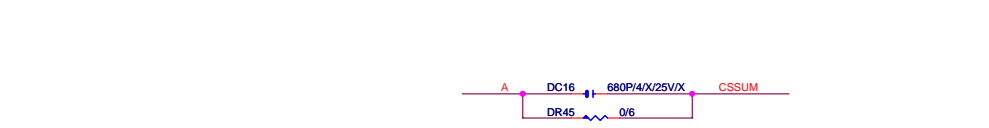
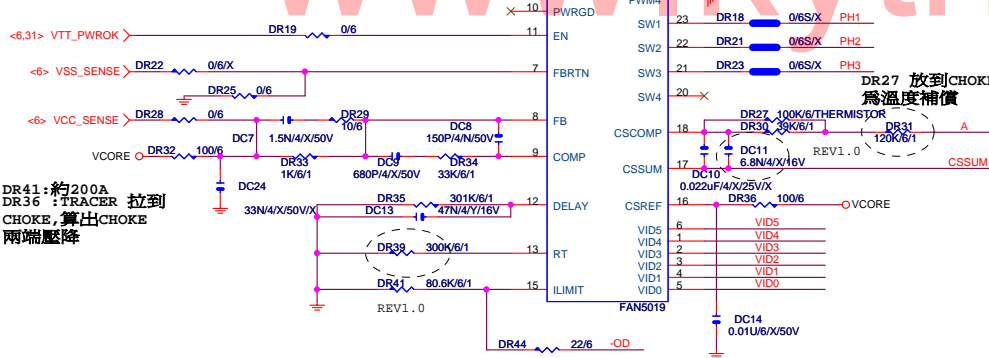


GIGABYTE		
Title		
Misc. PWR & ATX CONN.		
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DD1 : 在HIGH SIDE 發生S-D SHORT 時(OVP), LOW SIDE MOSFET TURN ON ,讓Vcore 降壓,同時V12 也會下降6~7V以下時,造成PWM 無法提供POWER TO LOW SIDE TURN ON,所以加DD1 延長PWM POWER 提供給LOW SIDE MOSFET TURN ON 保護CPU VOLTAGE 過高.

DR9,DC5 FOR V12 OVER 19V 以上,SPEC (18V)
 DR12 為一個 CYCLE的DUTY(50%)
 DELAY : 讓限流時間DELAY (DR35,DC13) 才啓動.
 DR39:240K
 工作頻率=720KHZ / 4PHASE=180K(1 PHASE)



PRO 以上,MOSFET (1 上 2 下)

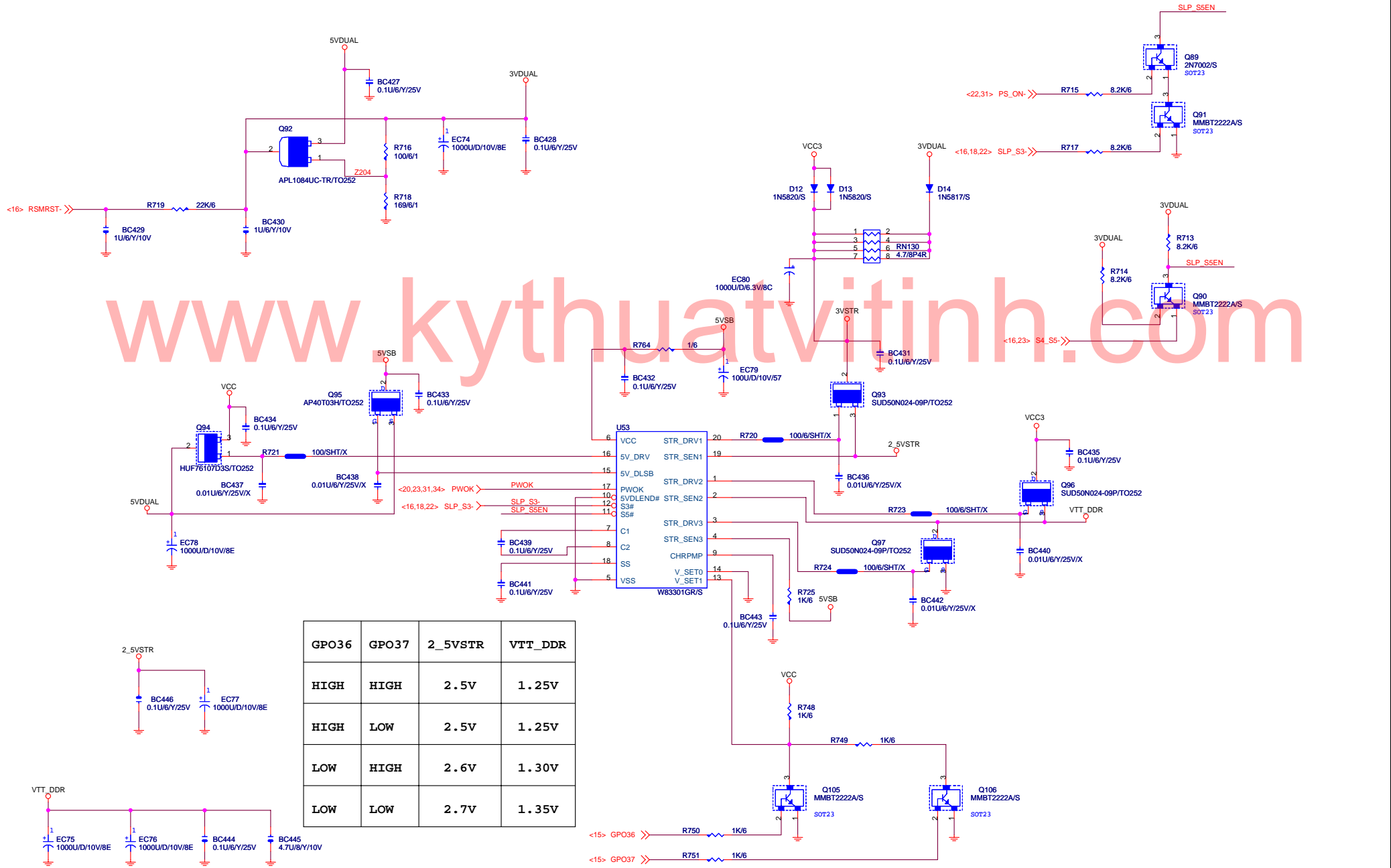
REV 1.0

REV 1.0

REV 1.0

CHOKE06U-30A_3PM-2

GIGABYTE		
VRD 10.1		
Size	Document Number	Rev
Custom	81845GE775-G	1.0
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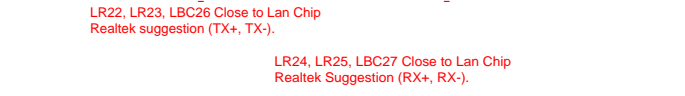
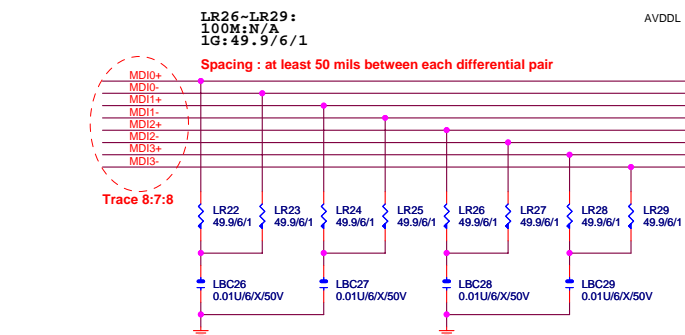
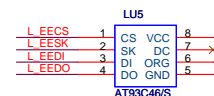
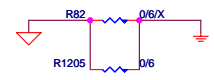


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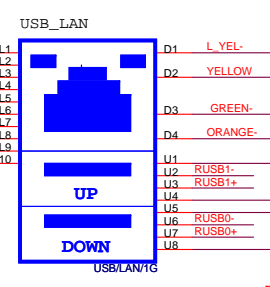
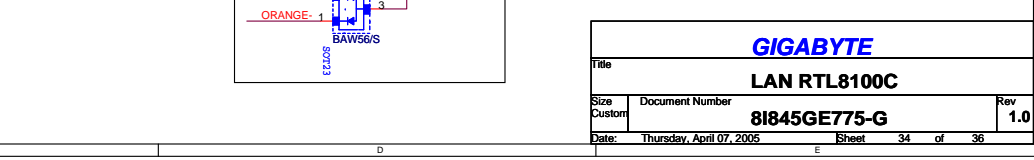
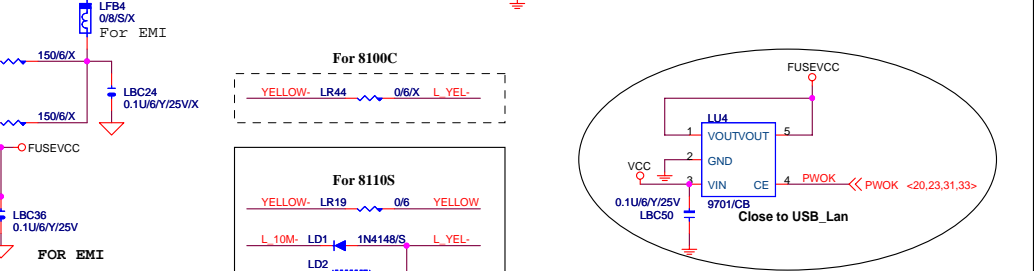
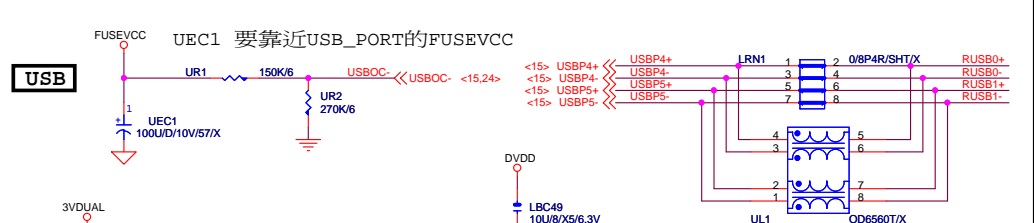
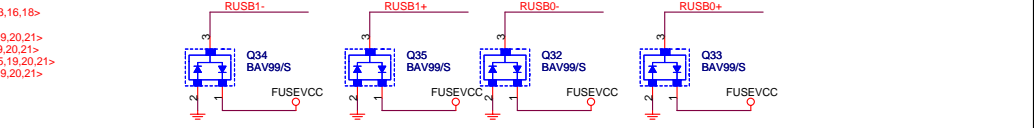
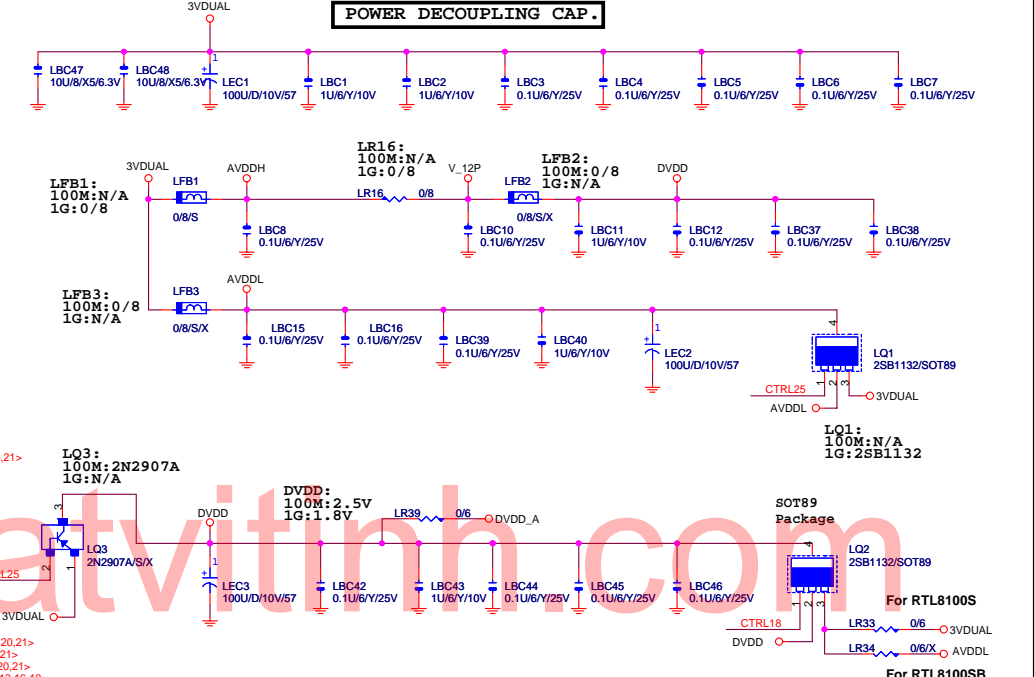
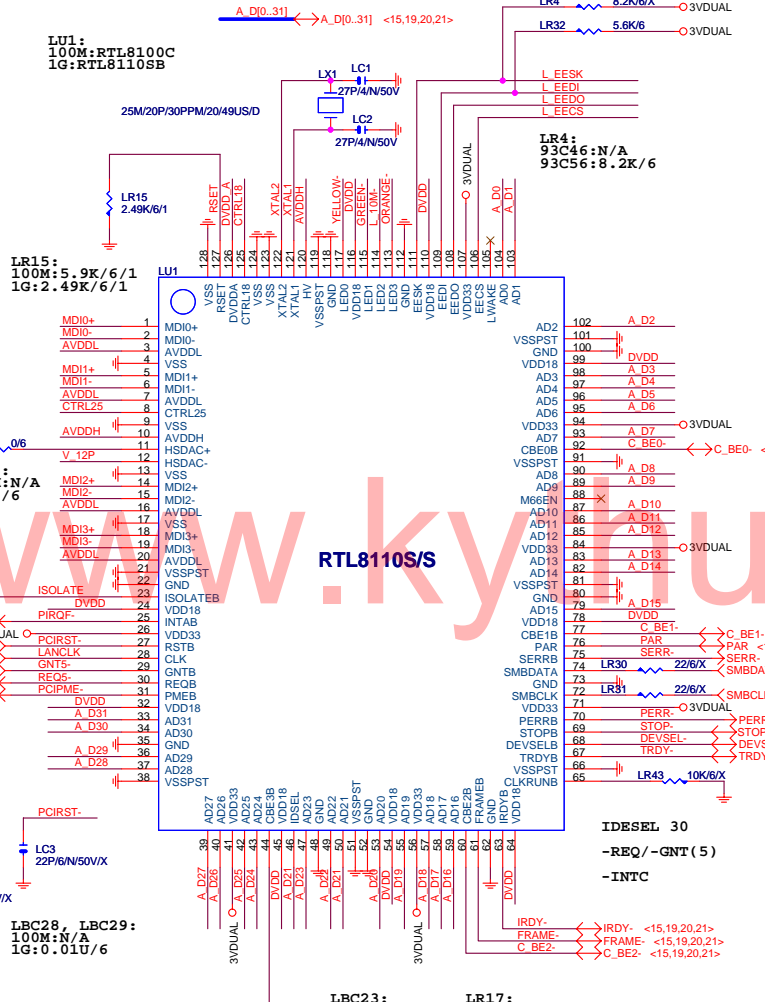
	GPO36	GPO37	2_5VSTR	VTT_DDR
HIGH	HIGH	HIGH	2.5V	1.25V
HIGH	LOW	LOW	2.5V	1.25V
LOW	HIGH	HIGH	2.6V	1.30V
LOW	LOW	LOW	2.7V	1.35V

	10/100	Giga	Giga
	8100C	8110S	8110SB
AVDDH	N/A	3.3V	3.3V
V_12P	2.5V	N/A	3.3V
AVDDL	3.3V	2.5V	2.5V
V_DAC	N/A	2.5V	2.5V
DVDD	2.5V	1.8V	1.3V
DVDD_A	2.5V	1.8V	1.3V

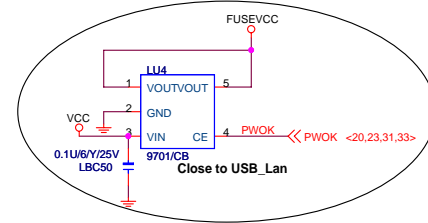
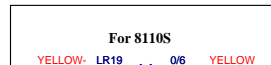
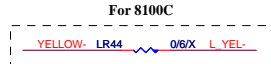
	Yellow	Dual Color LED	
		Green	Orange
10Mb	---	OFF	OFF
100Mb	---	ON	OFF
1Gb	---	OFF	ON
Link	ON	---	---
Active	Blink	---	---



LR22, LR23, LBC26 Close to Lan Chip
Realtek suggestion (TX+, TX-).
LR24, LR25, LBC27 Close to Lan Chip
Realtek Suggestion (RX+, RX-).



FOR 1G:P35-152-19W9
FOR 100M:P35-152-11Z9



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GIGABYTE GA-8I845GE775-G PCI ROUNTING LIST

PCI DEVICE	IDSEL	INT	CLOCK	REQ	GNT	
PCI SLOT1	16	C,F,G,A	PCLK1	REQ0-	GNT0-	
PCI SLOT2	17	F,G,A,C	PCLK2	REQ1-	GNT1-	
PCI SLOT3	18	G,A,C,F	PCLK3	REQ2-	GNT2-	
PCI SLOT4	19	A,C,F,G	PCLK4	REQ3-	GNT3-	
PCI SLOT5	20	C,F,G,A	PCLK5	REQ4-	GNT4-	
LAN	21	F	LANCLK	REQ5-	GNT5-	

GIGABYTE			
Title			
PCI ROUNT LIST			
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GIGABYTE GA-8I845GE775-G GPIO LIST

SHEET

TITLE

GPI		
GPI0/REQA-		PULL DOWN 15K, detect IDE1 connector type.
GPI1/REQ5-		PULL DOWN 15K, detect IDE2 connector type.
GPI2/PIRQE-		PULL 8.2K TO VCC3
GPI3/PIRQF-		PULL 8.2K TO VCC3
GPI4/PIRQG-		PULL 8.2K TO VCC3
GPI5/PIRQH-		PULL 8.2K TO VCC3
GPI6		PULL 8.2K TO VCC3 (GREEN_BUTTON)
GPI7		NOT IMPLEMENTED
GPI8		PULL 8.2K TO 3VDUAL, LPC PME.
GPI9	NA	NOT IMPLEMENTED
GPI10	NA	NOT IMPLEMENTED
GPI11		PULL 4.7K TO 3VDUAL (SMBALERT)
GPI12		PULL DOWN 10K.
GPI13		PULL DOWN 10K, CNR_PRIMARY
GPI14	NA	NOT IMPLEMENTED
GPI15	NA	NOT IMPLEMENTED

SHEET

TITLE

GPO		
GPO16		PULL 8.2K TO VCC3
GPO17		PULL 8.2K TO VCC3 (GNT5-)
GPO18		PULL 8.2K TO VCC3
GPO19		PULL 8.2K TO VCC3
GPO20		PULL 8.2K TO VCC3
GPO21		PULL 8.2K TO VCC3
GPO22		PULL 8.2K TO VCC3
GPO23		PULL 8.2K TO VCC3
GPO24		PULL 1K TO 3VDUAL (TOP BLOCK)
GPO25		PULL 4.7K TO 3VDUAL, POWER LED CONTROL.
GPO26		NOT IMPLEMENTED
GPO27		PULL 8.2K TO 3VDUAL, POWER LED CONTROL.
GPO28		PULL 8.2K TO 3VDUAL, GREEN LED.
GPO32		PULL 8.2K TO 3VDUAL, BIOS WRITE PROTECT.
GPO35		PULL DOWN 10K, POWER LED CONTROL.

GIGABYTE

Title

GPIO LIST

Size
Custom

Document Number

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Rev

1.0

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