GIGABYTE GA-8I848E Schematics

Revision 1.01

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29	KB_PS2
30	FPANEL
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32	DDR POWER
33	VCORE POWER
34	ATX, OTHERS POWER
35	KINNERETH-R LNA(CSA-1)
36	KINNERETH-R LNA(CSA-2)
37	KINNERETH-R LNA(CSA-3)

Digitally signed by dd
DN: cn=dd, o=dd, ou=dd, email=dddd@yahoo.com, c=US
Date: 2009.11.05 19:37:16 +07'00'

COMPONENT SIDE (1 oz. Copper)
VCC SIDE (1 oz. Copper)
GND SIDE (1 oz. Copper)
SOLDER SIDE (1 oz. Copper)

GIGABYTE CORP.

Title: COVER SHEET

Size: Custom | Document Number: GA-8I848E | Rev: 1.01

Date: | Sheet 1 of 38

Model Name: GA-8I848E

Component history

Date	Change Item	Reason
	EVT BOM READY	
8I848E-00-10A	PCB 1.0 尺寸304.8X204.97	
8I848E-00-10C	ADD R1247 62/6	
	PCB REV1.0 CHANGE TO REV1.01	
8I848E-00-10D	ADD C431 1000P/6/X/50V	FOR 未鎖頻 W.M. CPU 不開機
9MK8I848E-NB-10D	FOR 大中國 CKD(-NB) BOM	
8I848E-00-10E	R1247 62/6 --> 1K/6	FOR CHECKSUM ERROR ISSUE

Circuit or PCB layout history

Date	Change Item	Reason
8IPE1000S-00-10A		

GIGABYTE CORP.

Title: BOM & PCB MODIFY HISTORY

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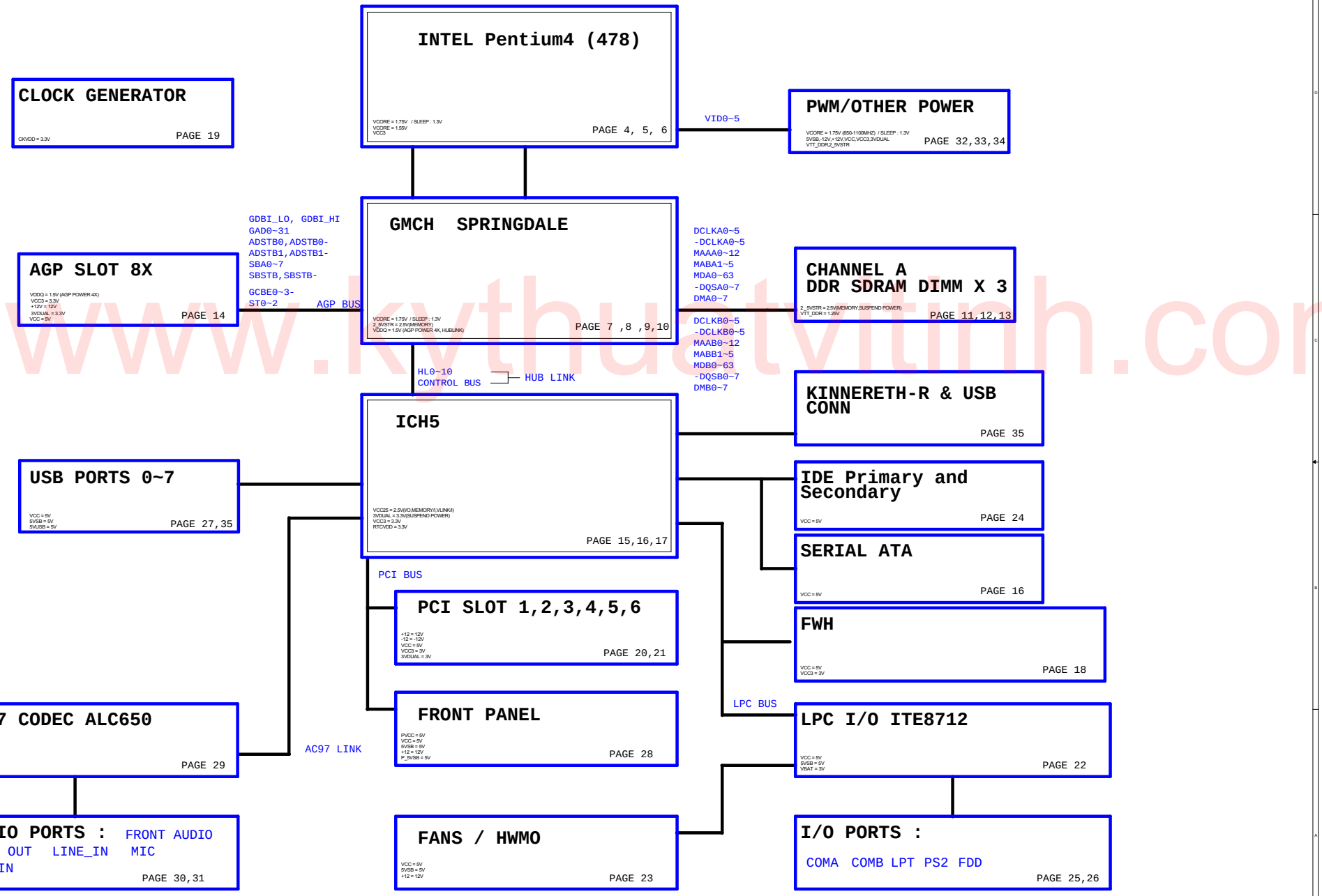
BLOCK DIAGRAM



GIGABYTE CORP.

Title: BOM & PCB MODIFY HISTORY

Size: Custom | Document Number: GA-8I848E | Rev: 1.01

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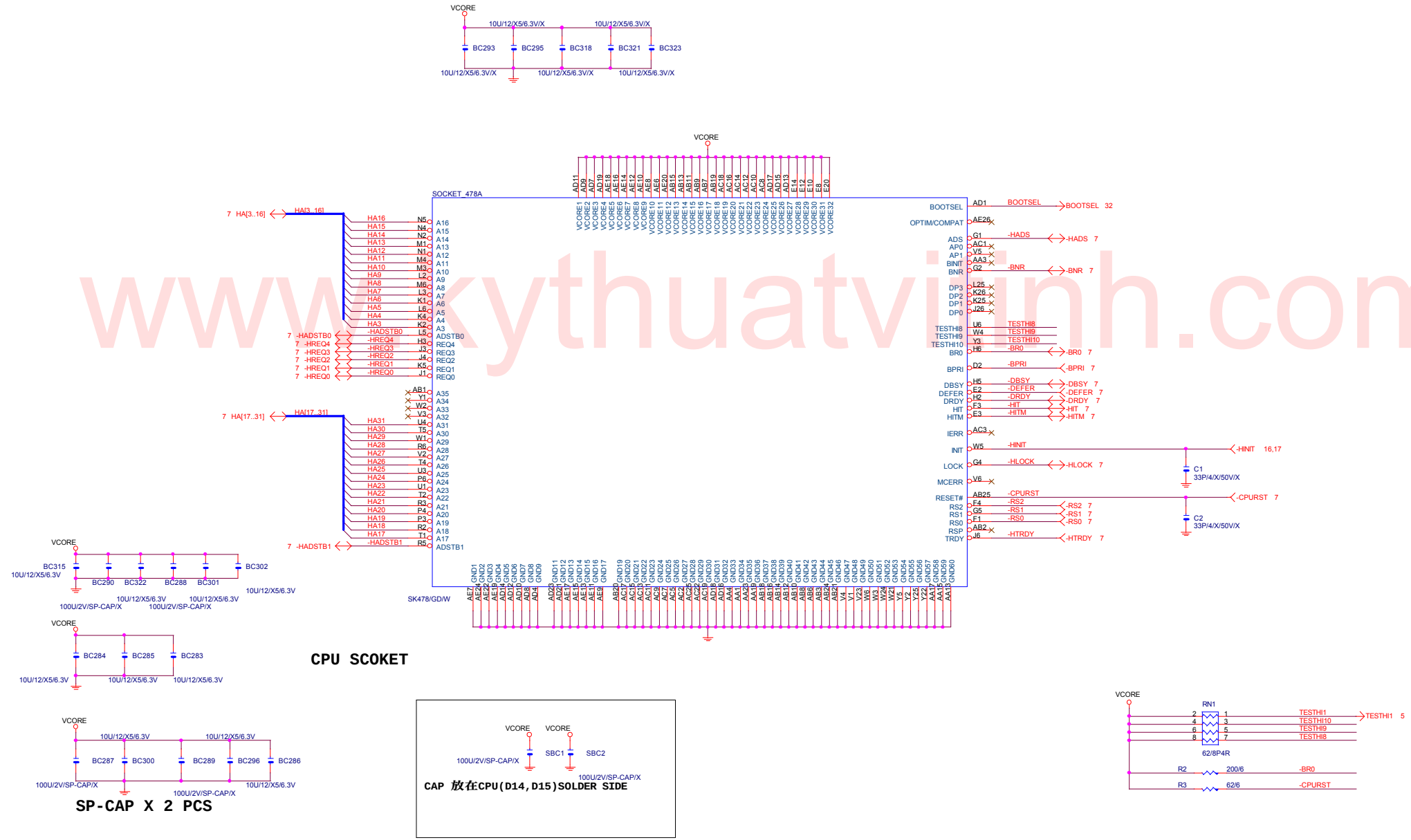

Title	P4 478A
Size	Custom
Document Number	GA-8I848E
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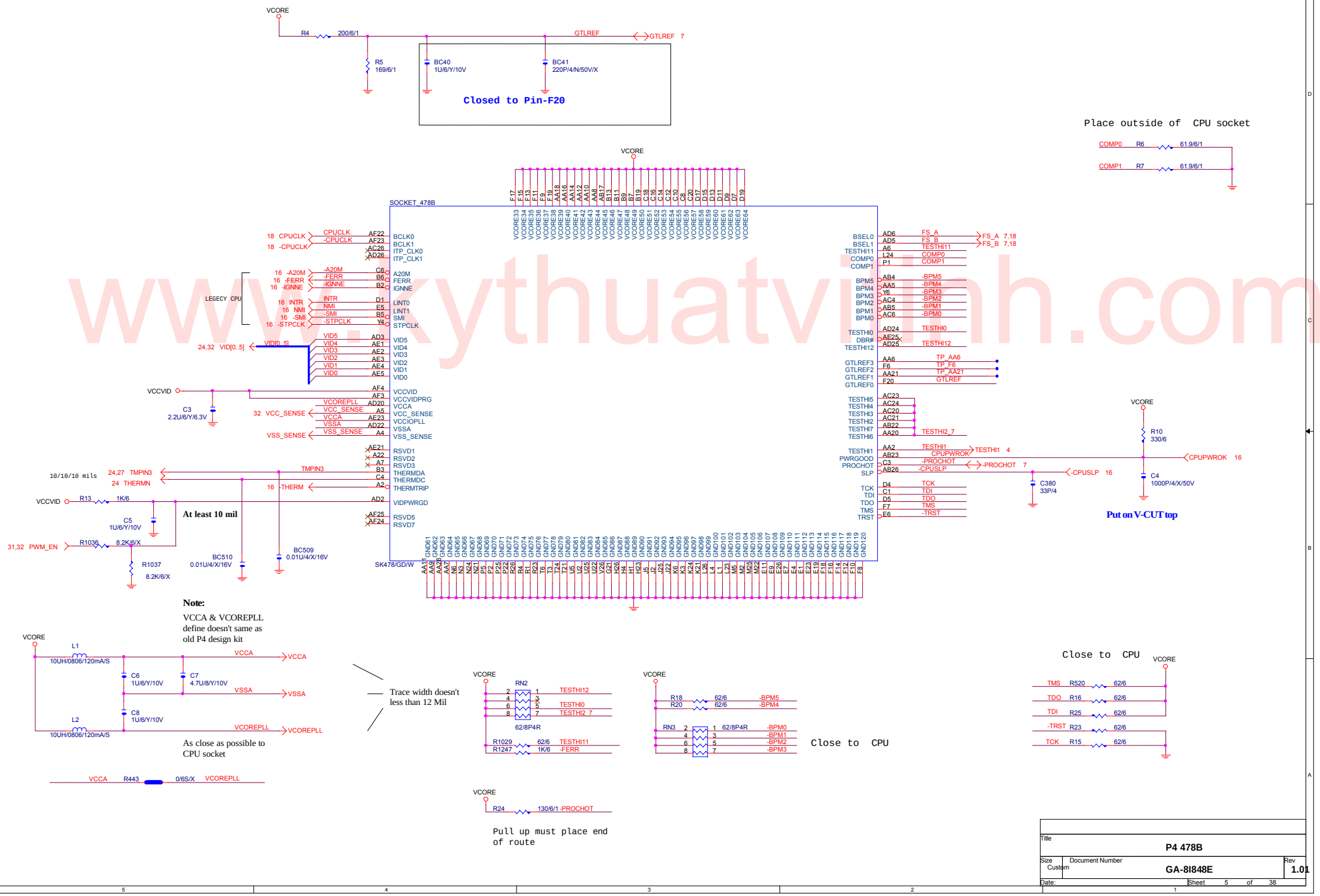
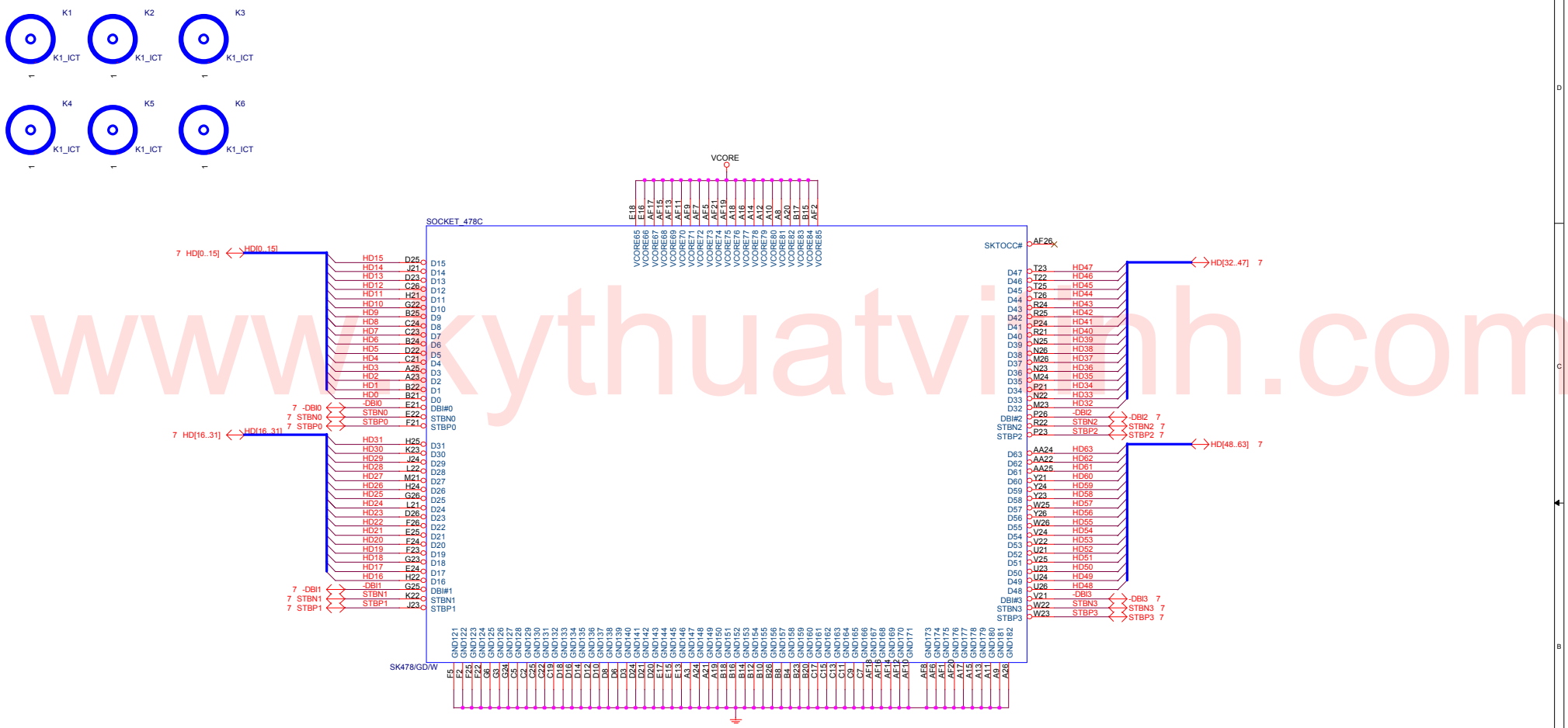
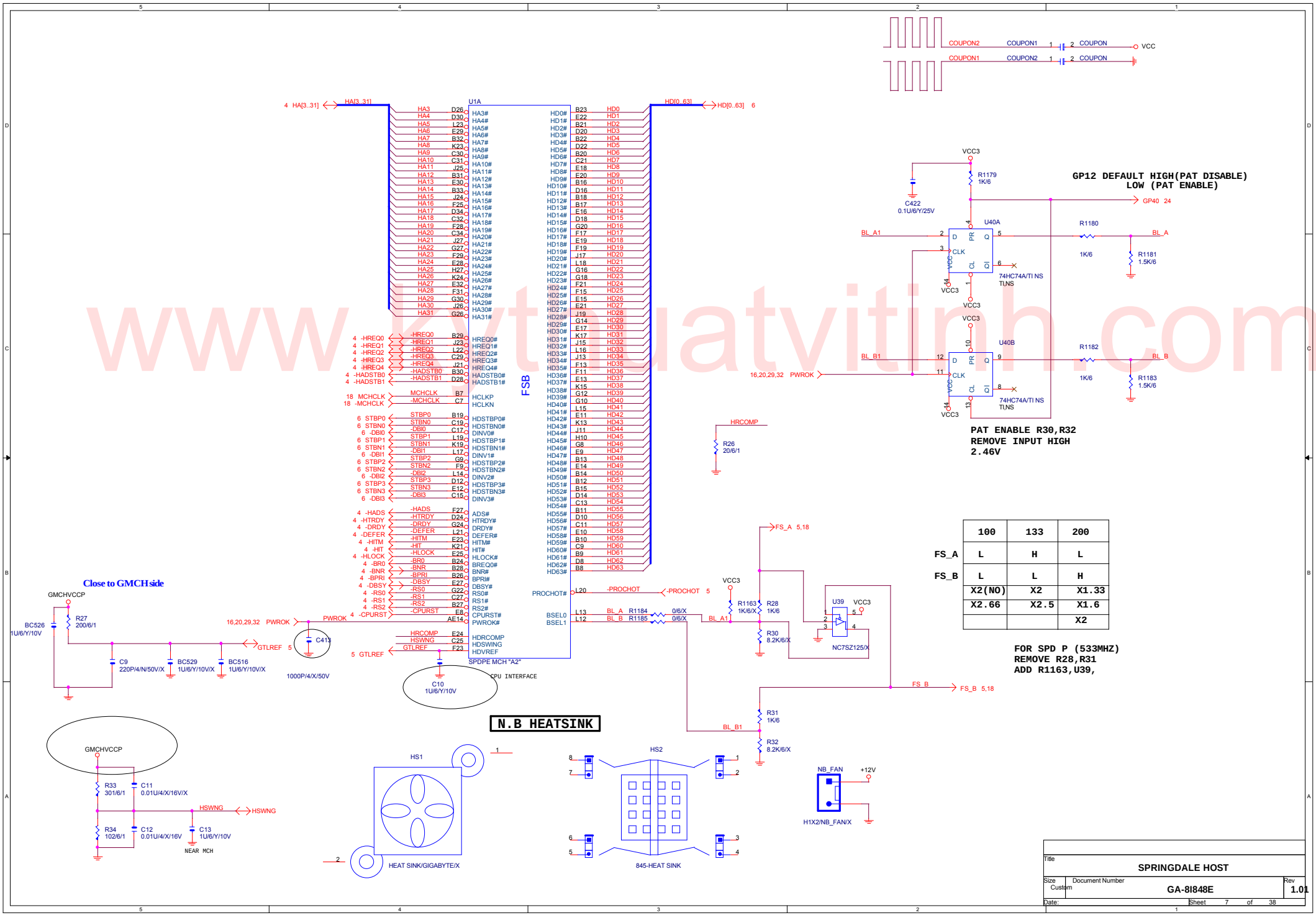




Title	P4 478C	
Size: Custom	Document Number: GA-8I848E	Rev: 1.01
Date:	Sheet 6 of 38	



	100	133	200
FS_A	L	H	L
FS_B	L	L	H
	X2(NO)	X2	X1.33
	X2.66	X2.5	X1.6
			X2

Title	SPRINGDALE HOST
Size / Document Number	Custom / GA-8I848E
Rev	1.01
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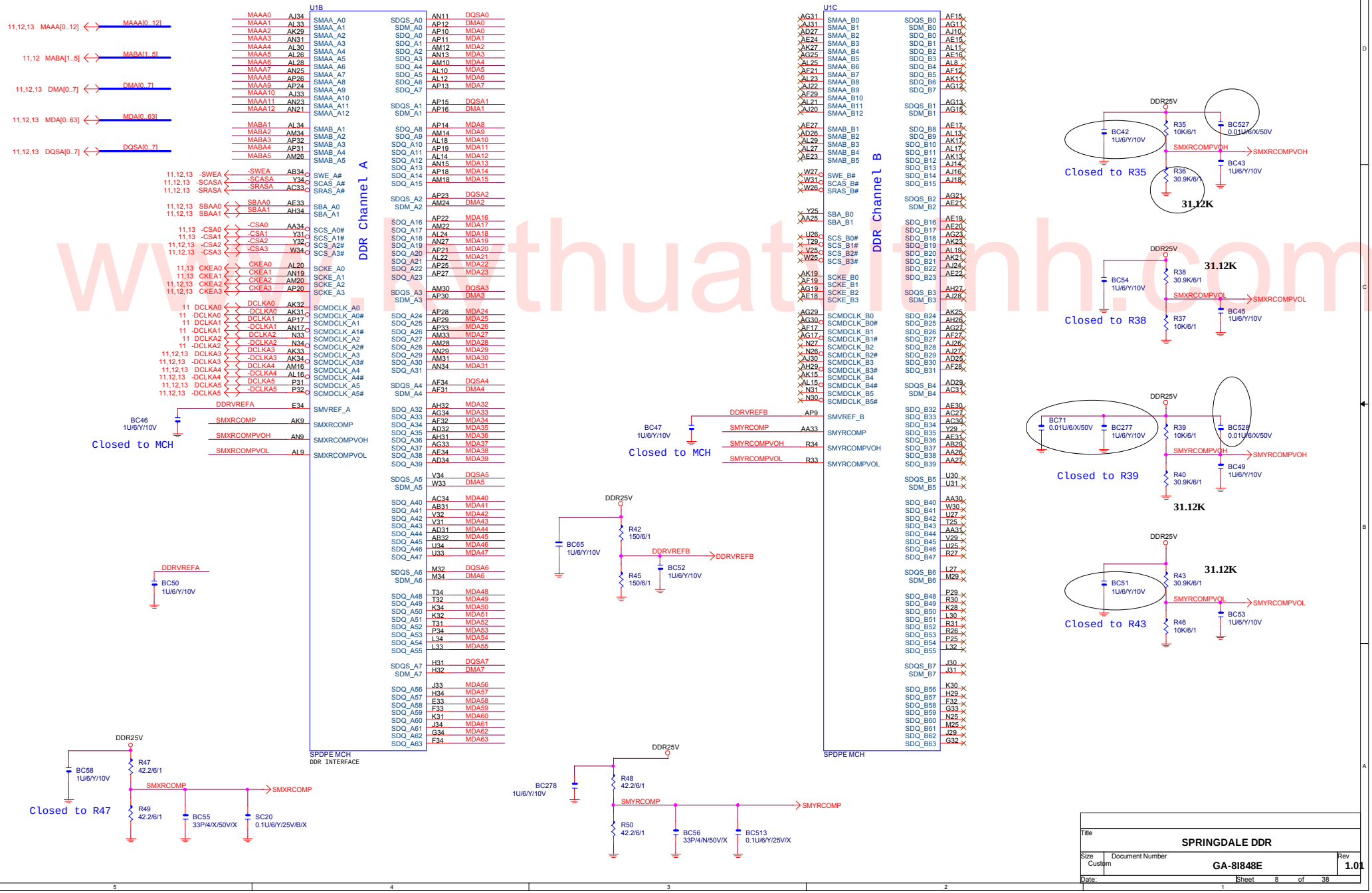
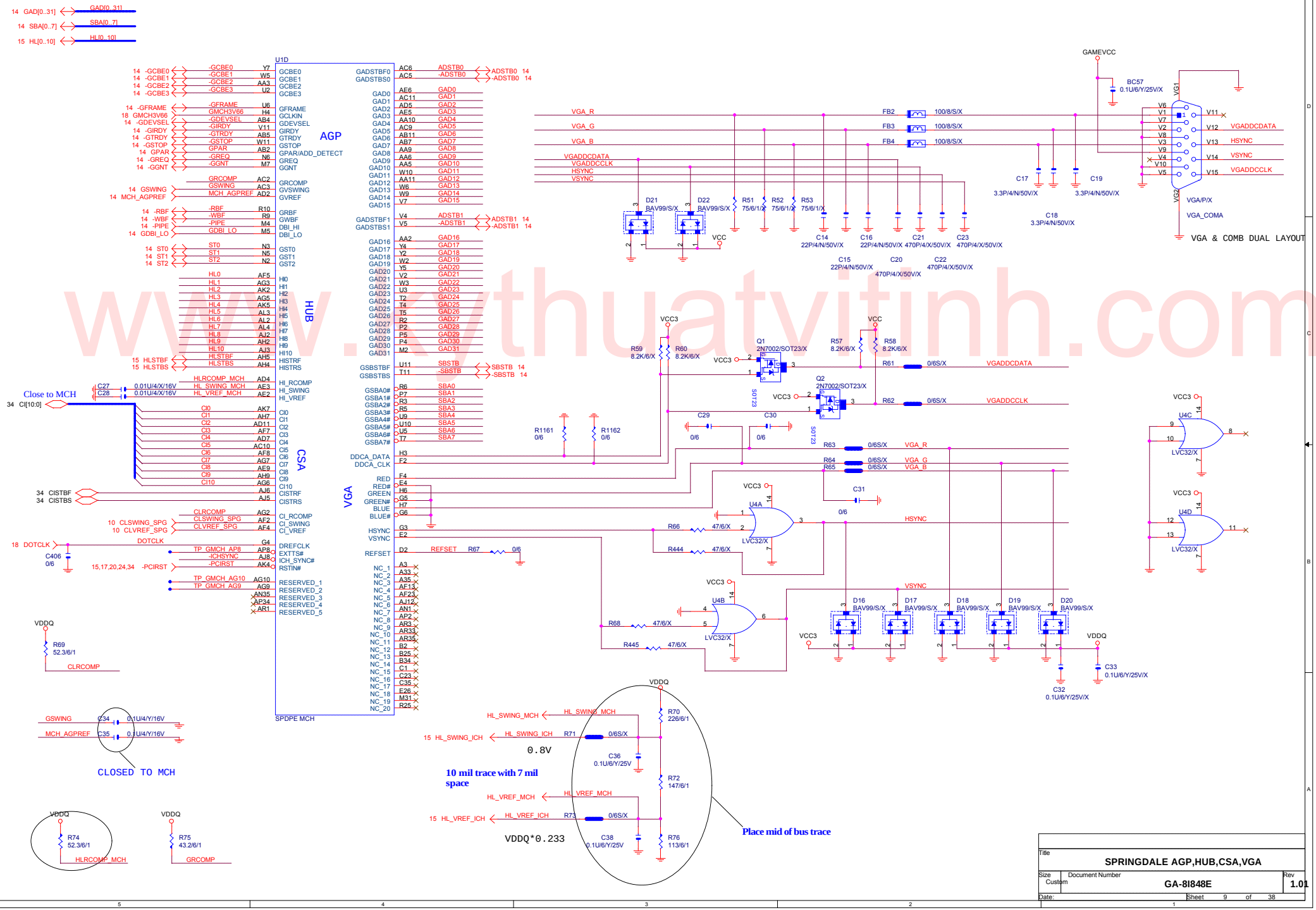
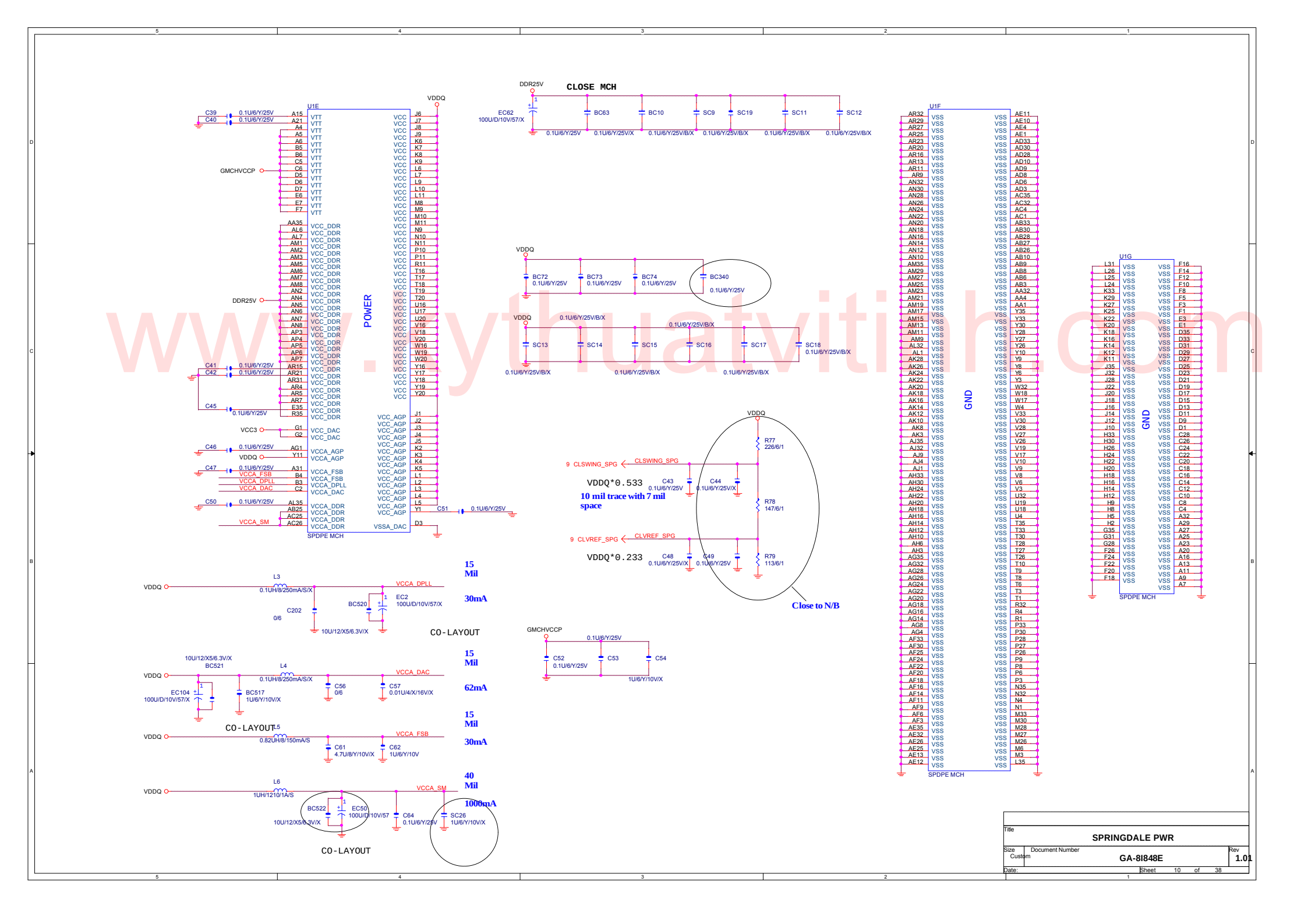
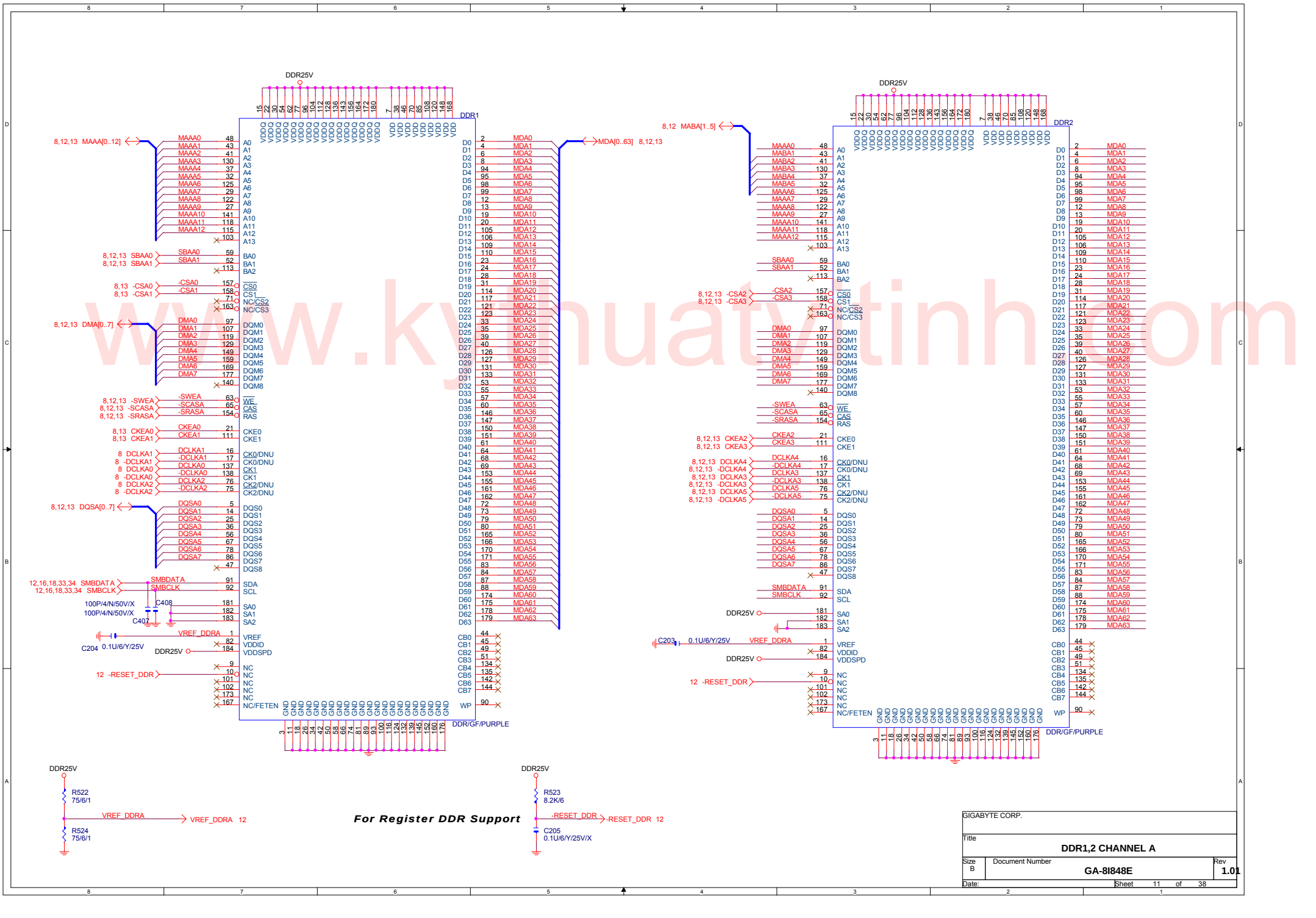
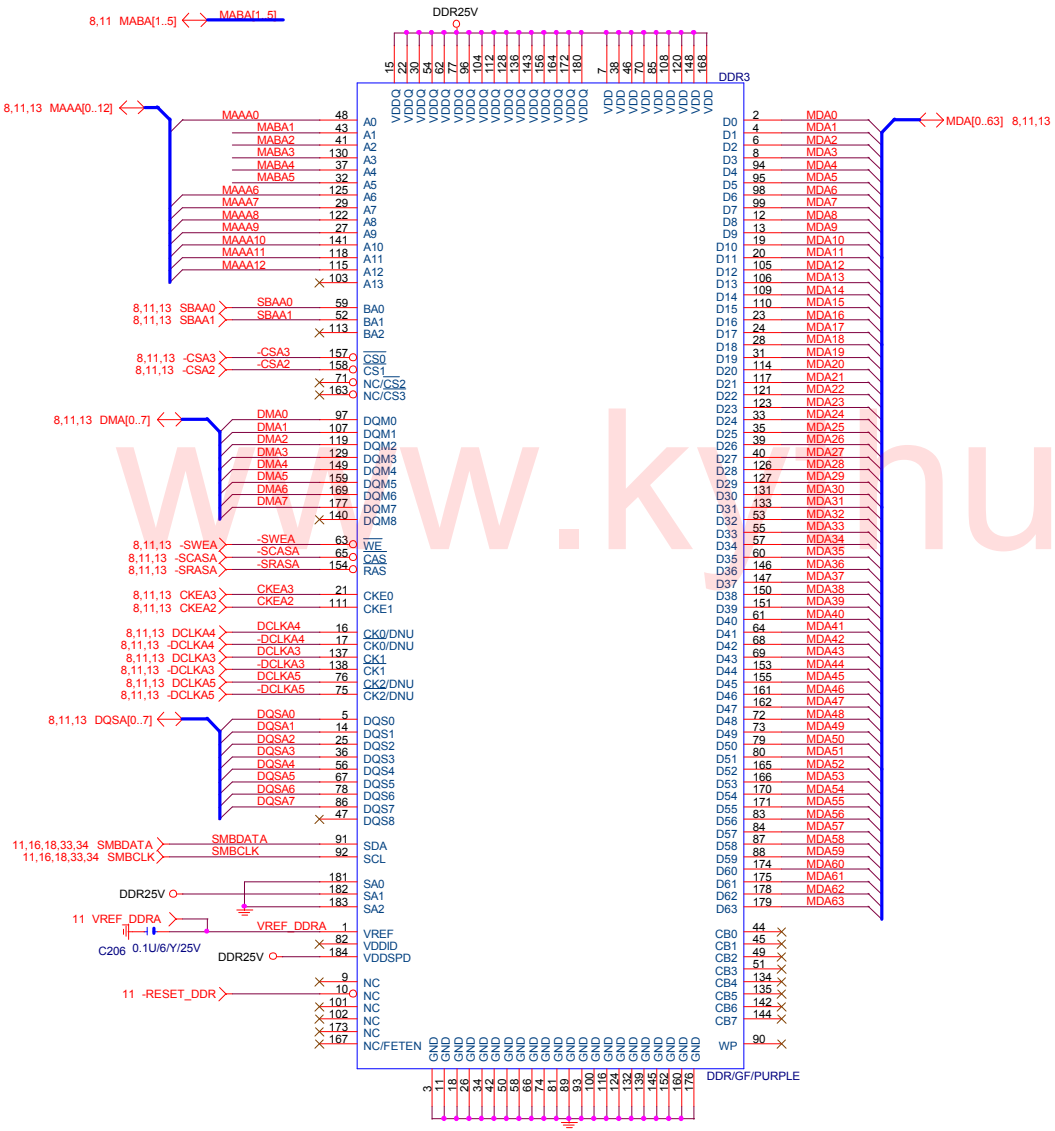


Title	SPRINGDALE DDR	
Size: Custom	Document Number: GA-8I848E	Rev: 1.01
Date:	Sheet 8 of 38	


U1D
AGP
HUB
CSA
VGA
SPDPE MCH
VGA & COMB DUAL LAYOUT
SPRINGDALE AGP,HUB,CSA,VGA
GA-8I848E
Rev 1.01
Close to MCH
CLOSED TO MCH
10 mil trace with 7 mil space
Place mid of bus trace
0.8V
VDDQ*0.233
www.kythuatvitinh.com







GIGABYTE CORP.		
Title: DDR3,4 CHANNEL B		
Size: B	Document Number: GA-8I848E	Rev: 1.01
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DDR TERMINATION
CHANNEL A

DDRVTT Decouple

DDRVTT

CN6 0.1U/8P4C
CN1 0.1U/8P4C
CN21 0.1U/8P4C
CN9 0.1U/8P4C
CN13 0.1U/8P4C
CN25 0.1U/8P4C
CN18 0.1U/8P4C

DDRVTT

CN5 0.1U/8P4C
CN23 0.1U/8P4C
CN17 0.1U/8P4C
CN27 0.1U/8P4C
CN29 0.1U/8P4C

DDRVTT

CN2 0.1U/8P4C
CN10 0.1U/8P4C
CN14 0.1U/8P4C

DEFAULT MOUNT

DDR25V Decouple

DDR25V

Channel -A CLOSE

BC64 0.1U/6/Y/25V
BC78 0.1U/6/Y/25V
BC81 0.1U/6/Y/25V
BC82 0.1U/6/Y/25V
BC88 0.1U/6/Y/25V
BC83 0.1U/6/Y/25V/X
BC91 0.1U/6/Y/25V
BC69 0.1U/6/Y/25V/X

DDRVTT

MDA6 MDA2 DMA0 DQSA0 MDA1 MDA5 MDA4 MDA0 MDA9 MDA8 MDA3 MDA7 DMA1 MDA13 DQSA1 MDA12 MDA22 MDA18 DMA2 — RN7 56/8P4R
DQSA2 MDA21 MDA17 MDA16 DMA3 DQSA3 MDA29 MDA25 MDA31 MDA27 MDA26 MDA30 MDA38 DMA4 MDA34 DQSA4 MDA28 MDA24 MDA23 MDA19 MDA40 MDA44 MDA35 MDA39 MDA11 MDA10 MDA15 MDA14 DQSA7 DMA7 MDA57 MDA56 — RN11 RN15 RN19 RN23 RN27 RN31 RN33 RN37 RN42 RN46 RN51 RN55 56/8P4R

MDA20 R94 56/6
DMA5 R497 56/6
DQSA5 R499 56/6
MDA41 R501 56/6
MDA45 R503 56/6

DDRVTT

MDA37 MDA33 MDA36 MDA32 MDA47 MDA46 MDA43 MDA42 MDA55 MDA54 DQSA6 DMA6 MDA53 MDA52 MDA49 MDA48 MDA58 MDA59 MDA63 MDA62 MDA61 MDA60 MDA51 MDA50 — RN5 RN13 RN17 RN21 RN25 RN29 56/8P4R

MABA3 MAAA3 MABA4 MAAA4 MABA5 MAAA8 MAAA7 — RN35 RN43 47/8P4R

SBAA1 MAAA10 MAAA0 MAAA1 CKEA0 CKEA2 CKEA3 CKEA1 -CSA3 -CSA2 -SCASA -CSA0 — RN49 RN53 RN57 47/8P4R

8,11,12 -SCASA
8,11,12 -CSA0

MAAA5 R494 47/6
MAAA6 R495 47/6
MAAA9 R496 47/6
MAAA11 R498 47/6
MAAA12 R500 47/6
SBAA0 R502 47/6
-CSA1 R1135 47/6
MAAA2 R1118 47/6
R92 47/6
R96 47/6

8,11,12 -SWEA
8,11,12 -SRASA

47 Ohms

SBAA[0:1] <> SBAA[0:1] 8,11,12
-CSA[0:3] <> -CSA[0:3] 8,11,12
CKEA[0:3] <> CKEA[0:3] 8,11,12
MABA[1..5] <> MABA[1..5] 8,11,12
MAAA[0..12] <> MAAA[0..12] 8,11,12

56 Ohms

DQSA[0..7] <> DQSA[0..7] 8,11,12
DMA[0..7] <> DMA[0..7] 8,11,12
MDA[0..63] <> MDA[0..63] 8,11,12

DDRVTT Decouple

DDRVTT

8,11,12 MABA1
8,11,12 MABA2
MABA1 R1164 47/6
MABA2 R1165 47/6

DDR25V

BC532 0.1U/6/Y/25V
BC533 0.1U/6/Y/25V/X
BC534 0.1U/6/Y/25V/X
BC536 0.1U/6/Y/25V
BC540 0.1U/6/Y/25V

CHANNEL B

DDRVTT

BC558 BC559 BC560 BC561 BC562 BC563 BC564 BC565 BC566 BC567 BC568 BC569 BC570 BC571 BC572 BC573 BC574 — 0.1U/6/Y/25V/X

VCC3

EC9 1000U/D/6.3V/8E

GIGABYTE CORP.		
Title: DDR TERMINATION		
Size: B	Document Number: GA-8I848E	Rev: 1.01
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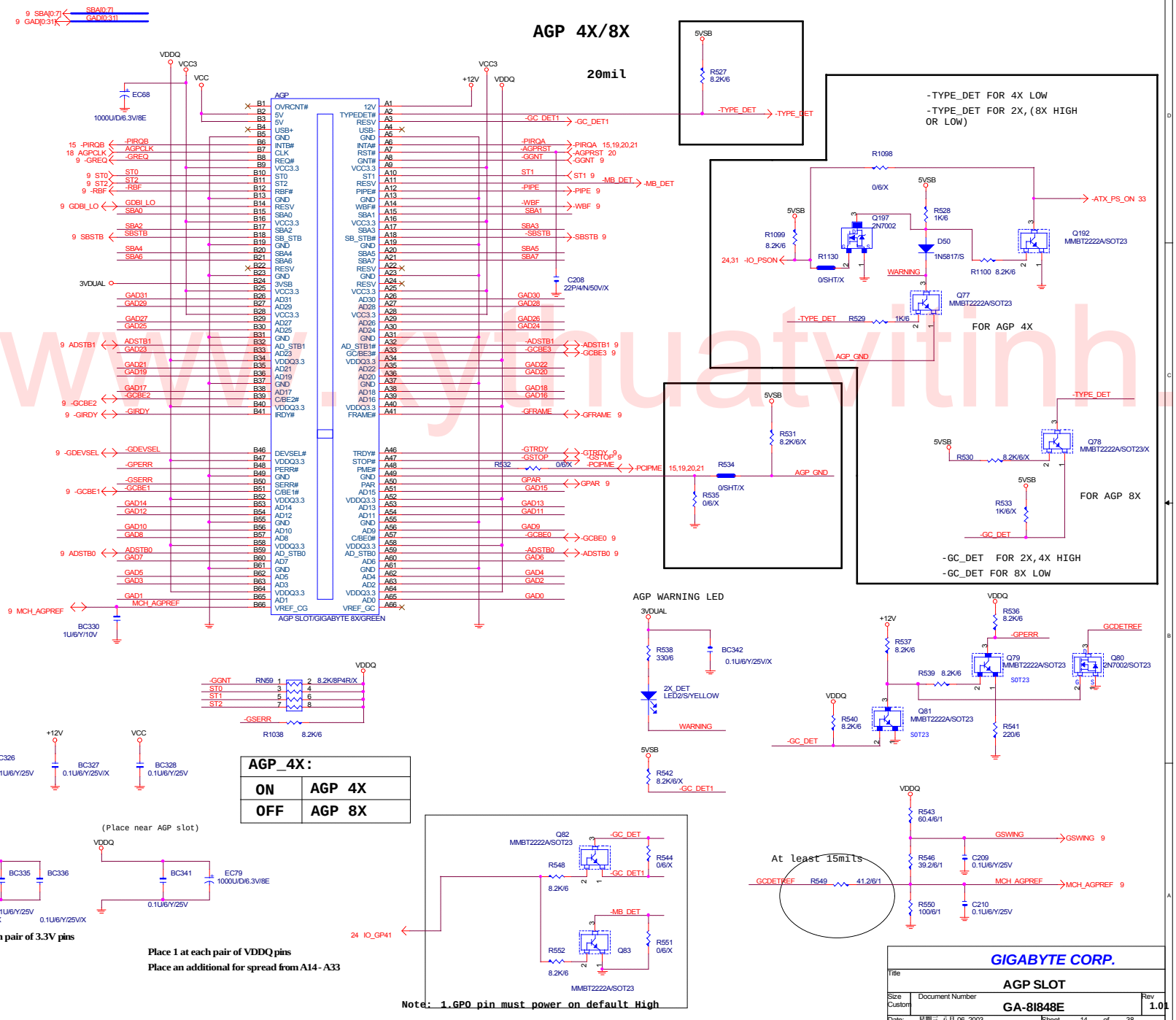

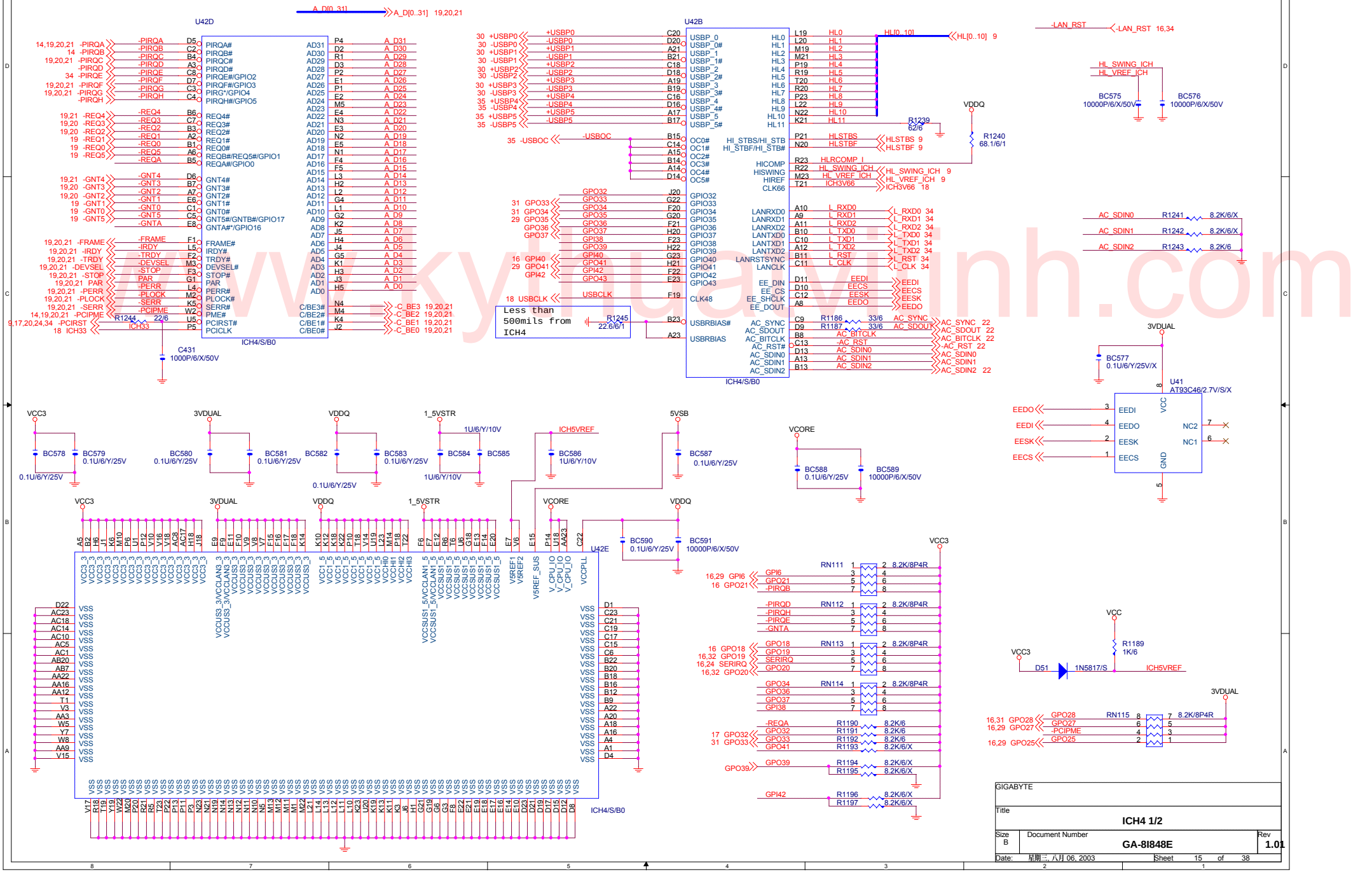

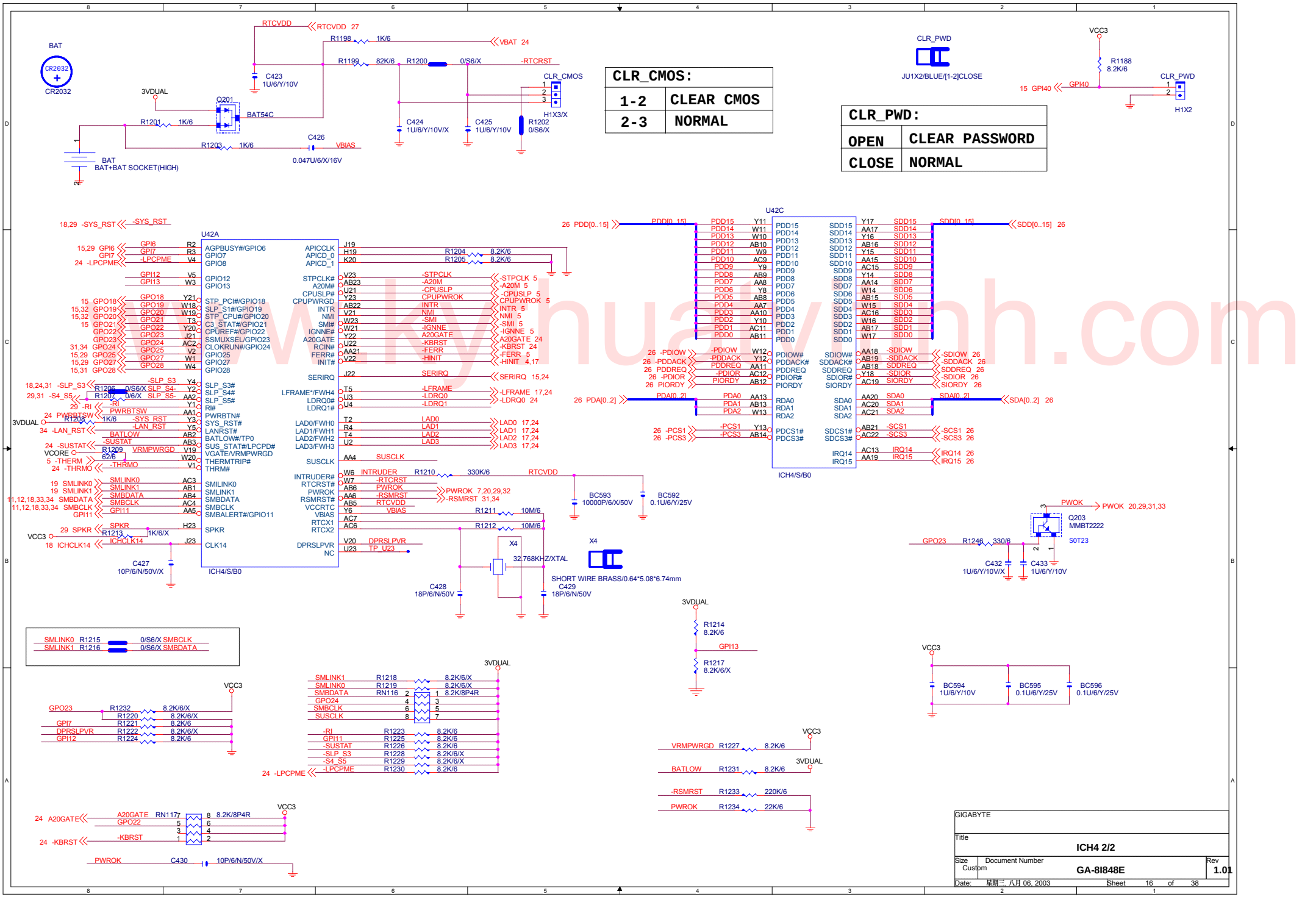
CLR_CMOS:
1-2 CLEAR CMOS
2-3 NORMAL
CLR_PWD:
OPEN CLEAR PASSWORD
CLOSE NORMAL
JU1X2/BLUE/[1-2]CLOSE
U42A
U42C
ICH4/S/B0
BAT
CR2032
BAT54C
BAT+BAT SOCKET(HIGH)
SHORT WIRE BRASS/0.64*5.08*6.74mm
32.768KHZ/XTAL
GIGABYTE
Title
ICH4 2/2
Size Custom
Document Number
GA-8I848E
Rev 1.01
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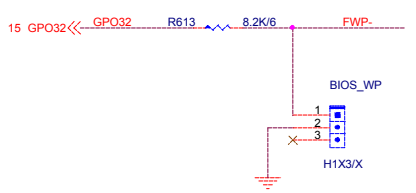
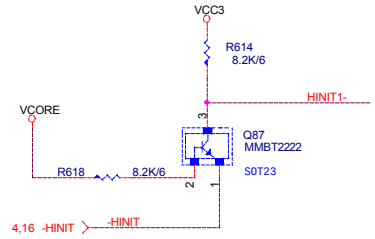
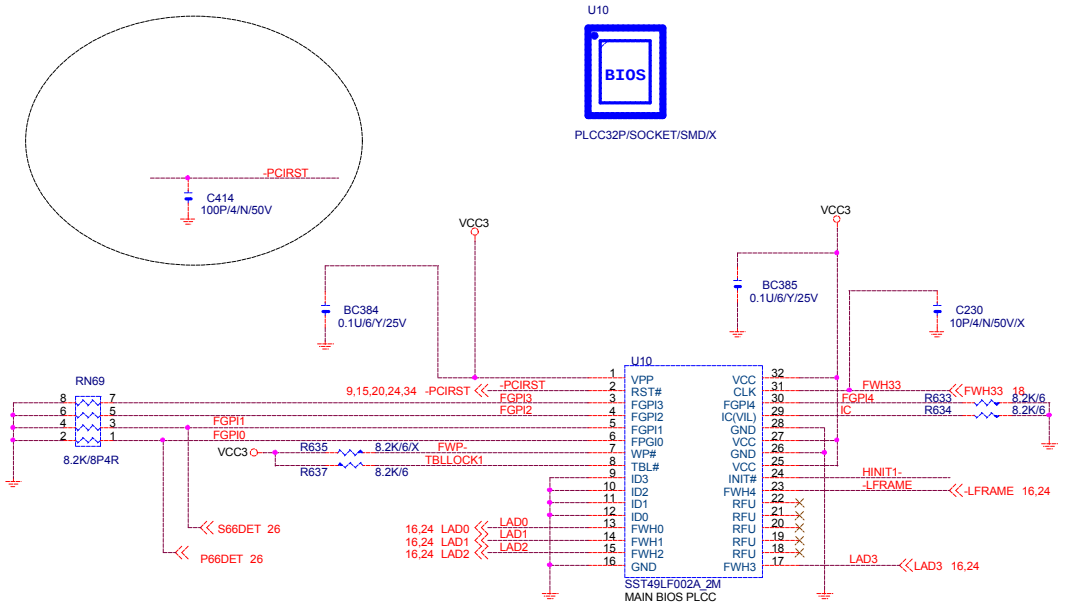




BIOS_WP:	
1-2	WRITE PROTECT
2-3	DISABLE



ADD WINBOUD FWH SEC. SOURCE

GIGABYTE CORP.

Title: FWH

Size: B | Document Number: GA-8I848E | Rev: 1.01

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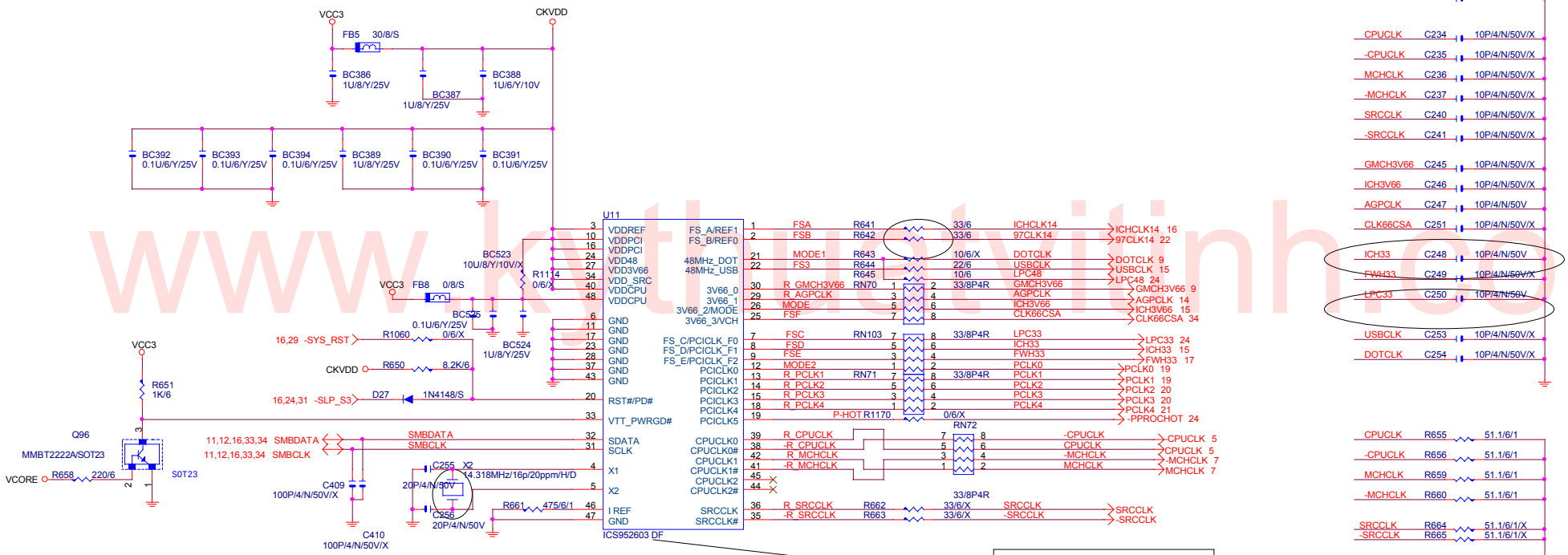

CYPRESS CY28405

FS_E	FS_D	FS_C	FS_A	FS_B	Clock
1	1	0	0	0	100MHz
1	1	0	1	0	133MHz
1	1	0	1	1	166MHz
1	1	0	0	1	200MHz

ICS952603

FS_D	FS_3	FS_C	FS_A	FS_B	Clock
1	0	0	0	0	100MHz
1	0	0	1	0	133MHz
1	0	0	1	1	166MHz
1	0	0	0	1	200MHz

CY28405 上 R1031,R1034,R671
不上R1030,R1035
ICS952616上R1030,R1035
不上 R1031,R1034,R671

GIGABYTE CORP.

Title: CLOCK GENERATOR

Size: Custom | Document Number: GA-8I848E | Rev: 1.01

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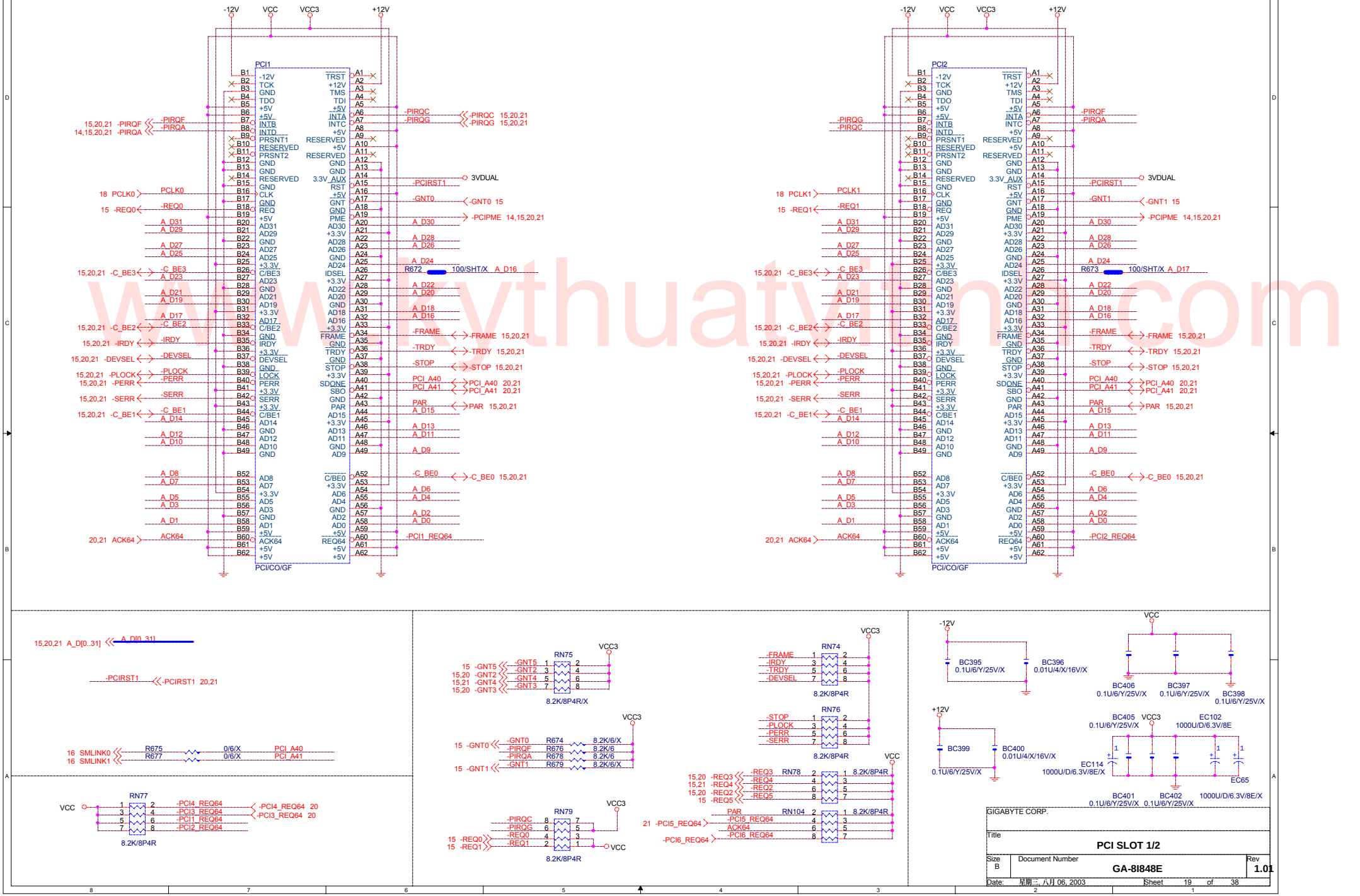
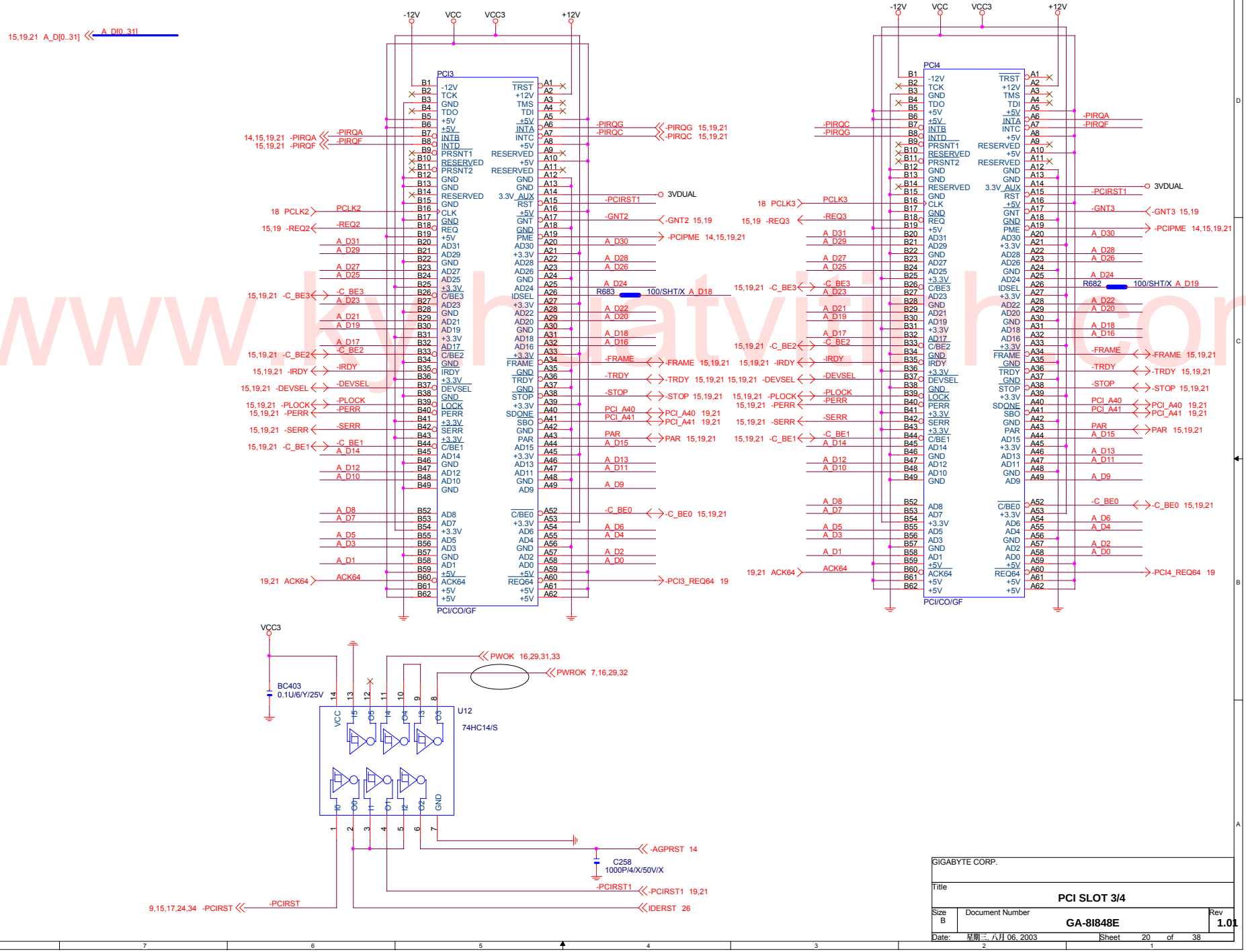
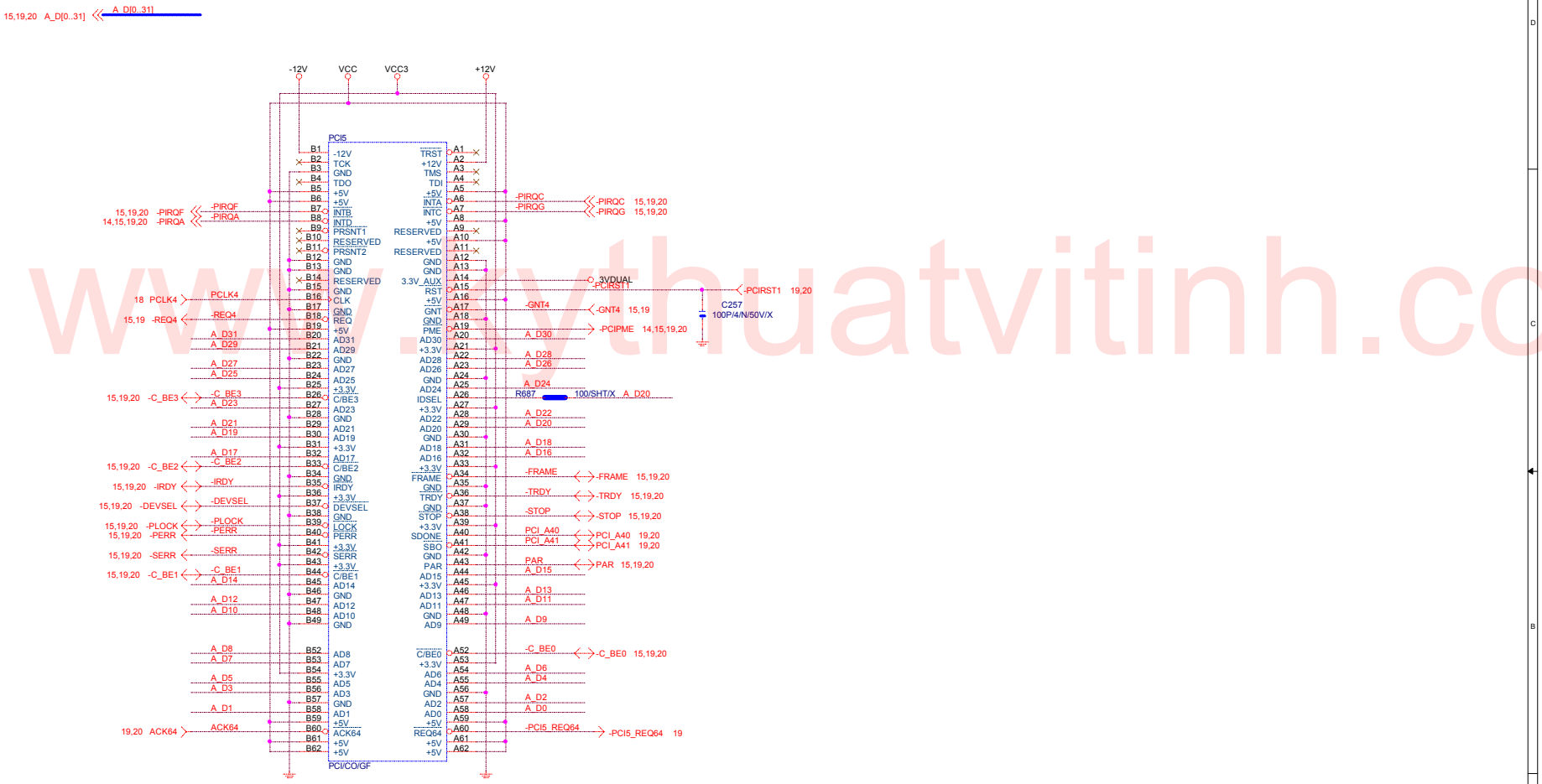




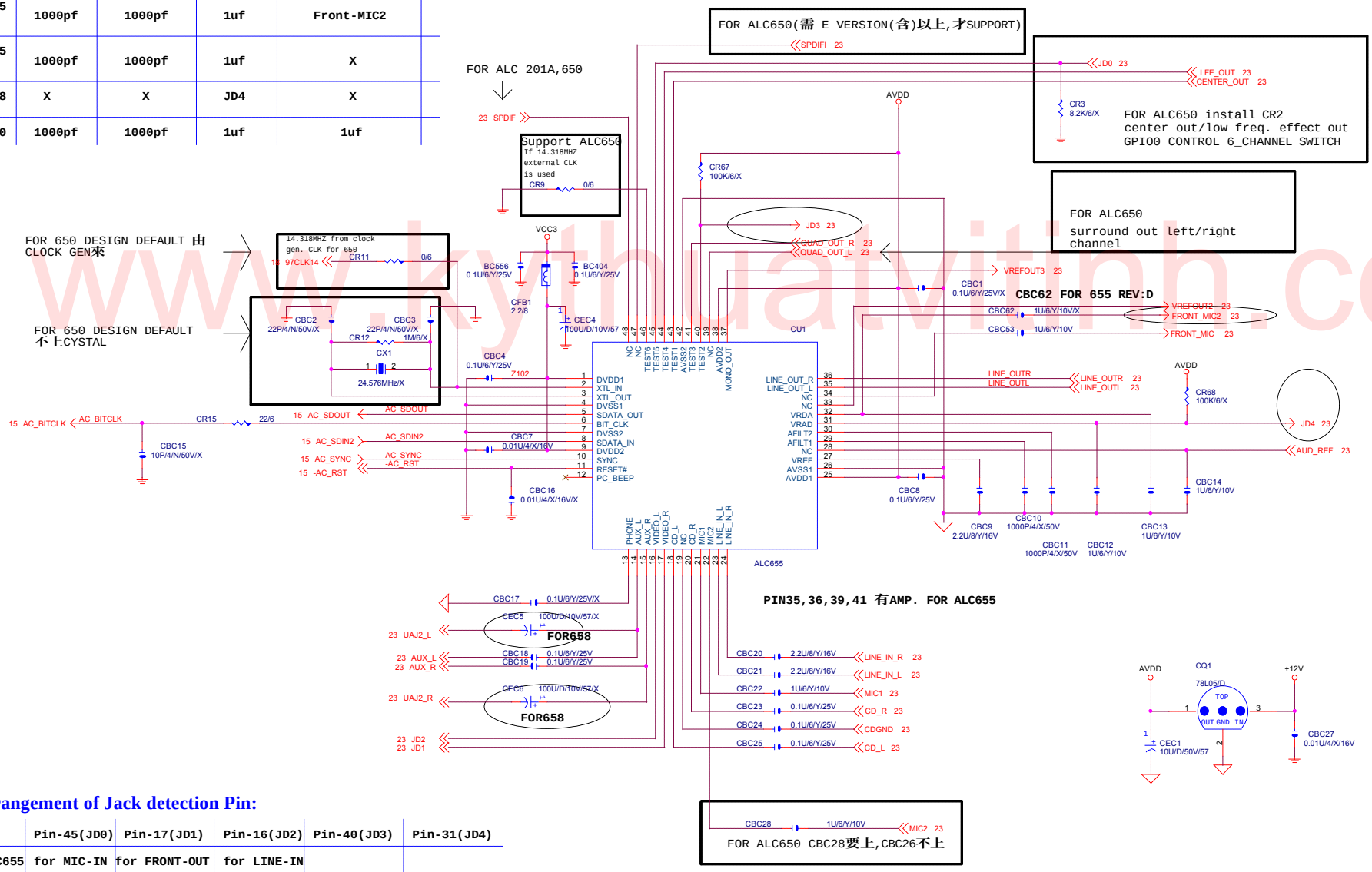
GIGABYTE CORP.		
Title	PCI SLOT 3/4	
Size B	Document Number GA-8I848E	Rev 1.01
Date: 星期三, 八月 06, 2003	Sheet 20 of 38	



GIGABYTE CORP.		
Title: PCI SLOT 5/6		
Size: Custom	Document Number: GA-8I848E	Rev: 1.01
Date: 星期三, 八月 06, 2003	Sheet 21 of 38	

Filter Cap design:

	Pin-29	Pin-30	Pin-31	Pin-32
ALC655 Rev D	1000pf	1000pf	1uf	Front-MIC2
ALC655 Rev C	1000pf	1000pf	1uf	X
ALC658	X	X	JD4	X
ALC650	1000pf	1000pf	1uf	1uf



Arrangement of Jack detection Pin:

	Pin-45(JD0)	Pin-17(JD1)	Pin-16(JD2)	Pin-40(JD3)	Pin-31(JD4)
ALC655	for MIC-IN	for FRONT-OUT	for LINE-IN		
ALC658	for MIC-IN	for UAJ1	for UAJ2	for FRONT-OUT Exernal pull high is needed	for LINE-IN Exernal pull high is needed

GIGABYTE CORP.		
Title	ALC655	
Size: Custom	Document Number: GA-8I848E	Rev: 1.01
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JDO,JD2,GPIO0 為偵測DEVICE INPUT 時由LOW TO HIGH Edge trigger(pop manual)

1/2(3.14)RC=1/2(3.14)8.2K*4.7U=4.3HZ以上AC 信號全部衰減 TO 0V 不會造成JDO 誤動作(無device 時play wav)

LINE OUT

注意CBC47,CBC48不可 CO LAYOUT

22 QUAD_OUT_R — CBC29 100U/D/10V/57 — SUR_OUTR

22 LINE_OUTR — CBC30 100U/D/10V/57 — CR19 22/6 — FOR ALC650 上 CBC47,CR42

FOR ALC650 CR47=22 OHM

22 LINE_OUTL — CBC33 100U/D/10V/57 — CR22 22/6

FOR ALC650 CR52=22 OHM

22 QUAD_OUT_L — CBC34 100U/D/10V/57 — SUR_OUTL — FOR ALC650 上 CBC50,CR57

注意CBC49,CBC50不可 CO-LAYOUT

FOR655 / FOR658

22 JD1 — CR62 0/6

22 JD3 — CR63 0/6/X — CBC58 4.7U/8/Y/10V — CR49 8.2K/6

FRONT_R — CR20 0/6/X — BACK_R

FRONT_L — CR21 0/6/X — BACK_L

AUDIO1B — B5 B4 B3 B2 — 3RJ+15F/[11NR6-403004-11]

CR50 22K/6 — CR51 22K/6 — CBC35 180P/4/N/50V — CBC36 180P/4/N/50V

LINE OUT SENSING
R>4K OHM=>POWER SPEAKER
4K OHM>R>400 OHM=>MICROPHONE
R<400 OHM=>HEADPHONE

FOR SUPPORT 6 CHANNEL, SURROUND OUT

CENTER OUT,LOW FREQUENCY EFFECT OUT

SUR_CEN — SUR_OUTL — SUR_OUTR

22 CENTER_OUT — CBC31 1U/6/Y/10V — H2X3/-PIN4 — CBC32 1U/6/Y/10V — LFE_OUT 22

LINE IN SENSING(當OUTPUT)
R>4K OHM=>POWER SPEAKER
4K OHM>R>400 OHM=>MICROPHONE
R<400 OHM=>HEADPHONE

LINE IN SENSING(當INPUT)
swing of input signal>-40dbv(10mv)===>line in device active
swing of input signal<-40dbv(10mv)===>unknown line in device

LINE-IN

FOR655 / FOR658

22 JD2 — CR64 0/6

22 JD4 — CR65 0/6/X — CBC59 4.7U/8/Y/10V — CR52 8.2K/6

22 LINE_IN_R — CR25 0/6

22 LINE_IN_L — CR26 0/6

AUDIO1C — C5 C4 C3 C2 — 3RJ+15F/[11NR6-403004-11]

CR28 22K/6 — CR29 22K/6 — CBC37 180P/4/N/50V — CBC38 180P/4/N/50V

MIC

22 MIC2

Only support ALC650 FOR LFE OUTPUT上CR61

CBC40 180P/4/N/50V

22 MIC1

FOR 650 CENTER OUT

22 AUD_REF — CR43 4.7K/6/X — CBC56 0.1U/6/Y/25V/X — FOR 650 LFE OUT — CR54 22K/6

AVDD — CBC39 1U/6/Y/10V/X — CR31 8.2K/6/X — CR30 1K/6/X — 22 AUD_REF — CBC41 0.1U/6/Y/25V — CR32 2.2K/6

CR56 8.2K/6/X — MIC — CBC57 180P/4/N/50V

22 JD0 — CR53 8.2K/6 — CBC60 4.7U/8/Y/10V

AUDIO1A — A5 A4 A3 A2 A1 — MH1 — 3RJ+15F/[11NR6-403004-11]

MICROPHONE IN SENSING(當INPUT)(利用vref 偏壓與CR43,CR32 並聯求出阻抗)
7.1k ohm>R>2.3k ohm===>microphone in
R<2.3k ohm or R>7.1k ohm===>unknown device

MICROPHONE IN SENSING(當OUTPUT)
R>4K OHM=>POWER SPEAKER
4K OHM>R>400 OHM=>MICROPHONE
R<400 OHM=>HEADPHONE

AUX IN DEFAULT NO POP

AUX_IN

22 AUX_L — 22 AUX_R — WF1x4/W

CBC46 1000P/4/X/50V/X — CBC47 1000P/4/X/50V/X

CD IN

CD_IN

22 CD_L — 22 CD_R — 22 CDGND — WF1x4/B

CBC43 — CBC44 — CBC45 — 1000P/4/X/50V/X

INTEL FRONT AUDIO

For EMI

CBC61 1000P/6/X/50V

22 FRONT_MIC — CR44 0/6S/X

MIC — CR45 0/6/X

AVDD — CBC49 0.1U/6/Y/25V/X

CR69 0/6/X — F_AUDIO

AVDD — CR40 8.2K/6 — CBC50 0.1U/6/Y/25V/X

FRONT_R — FRONT_L — BACK_R — BACK_L

CBC51 180P/4/X/50V/X — CBC52 180P/4/X/50V/X

VREFOUT3 22 — UAJ2_R 22

22 JD1 — 22 VREFOUT2 — H2X5/-8/BLACK

F_AUDIO — F_AUDIO — 22 UAJ2_L — 22 JD2

JU1X2/BLUE/[5-6]CLOSE — JU1X2/BLUE/[9-10]CLOSE

CR66 0/6/X — FRONT_MIC2 22 — CBC63 1000P/6/Y/25V/X

FOR658 CR70 8.2K/6/X — CR71 8.2K/6/X FOR658 — FOR 655

SPDIF_IO

VCC — SPDIF_IO

22 SPDIF — SPDIFI 22

PIN2X3-CUT2/RED

LINE IN(C)

LINE OUT(B)

MIC IN(A)

AUDIO PANEL

GIGABYTE CORP.

Title: AUDIO OUTPUT,GAME PORT

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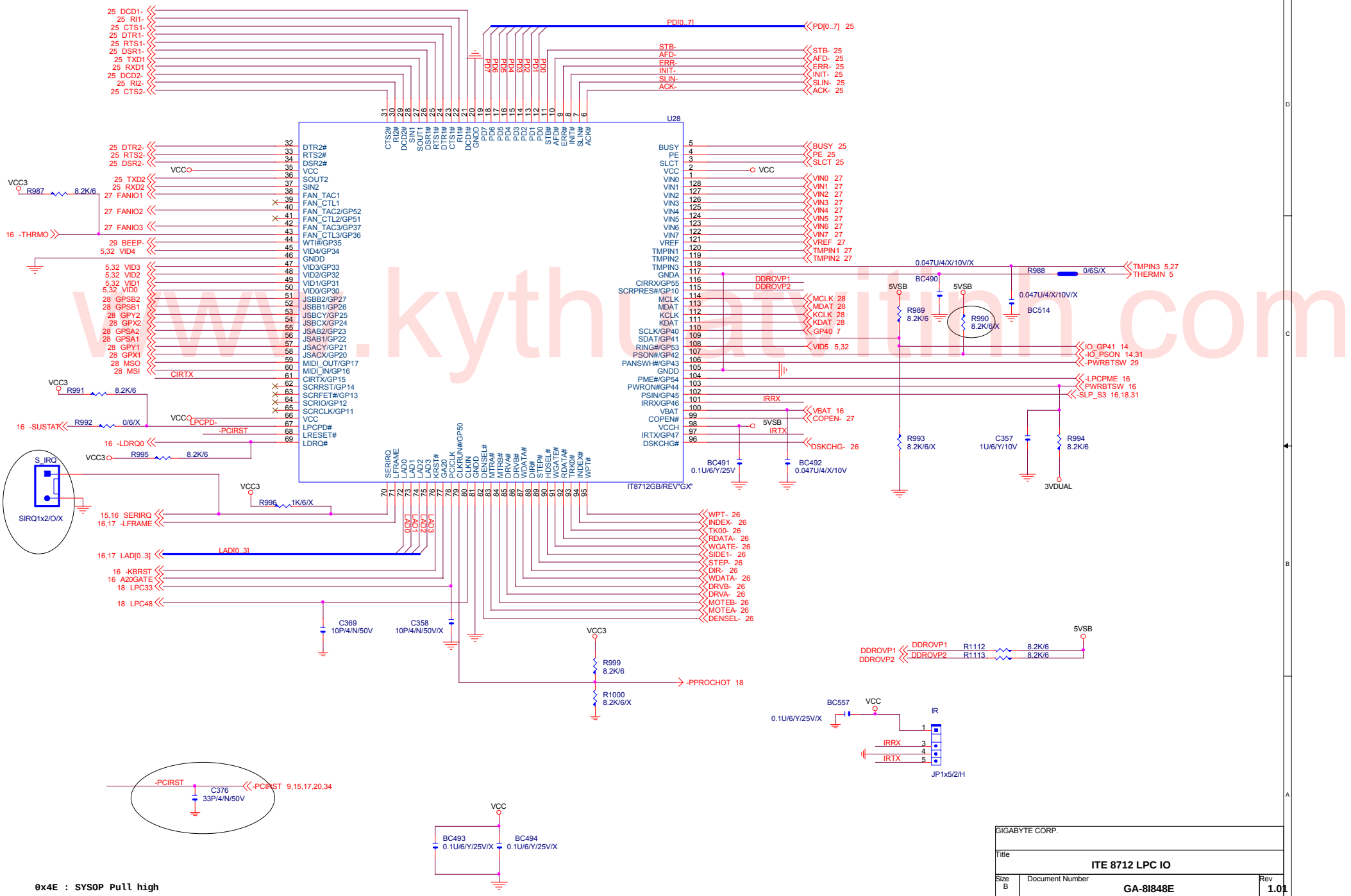

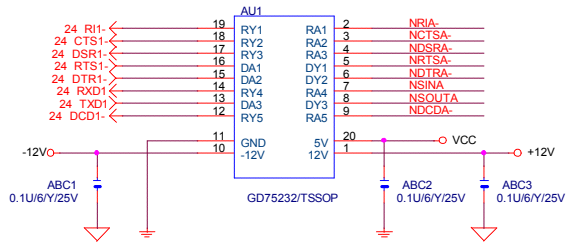

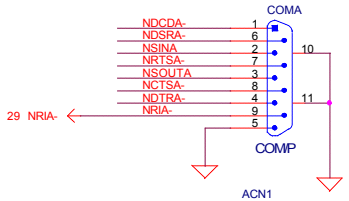

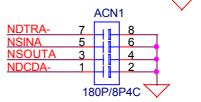

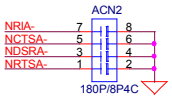

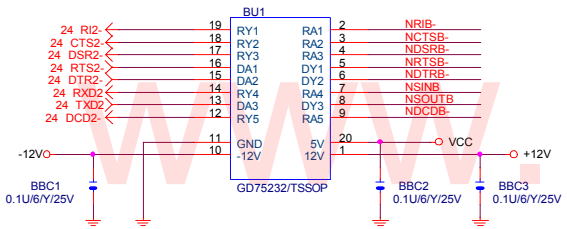

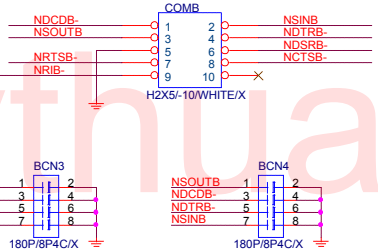

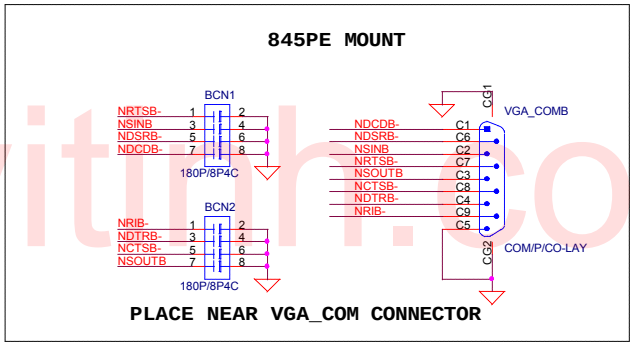

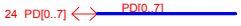

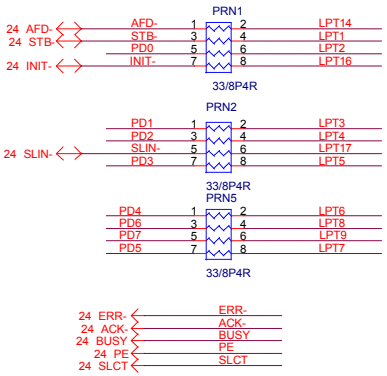

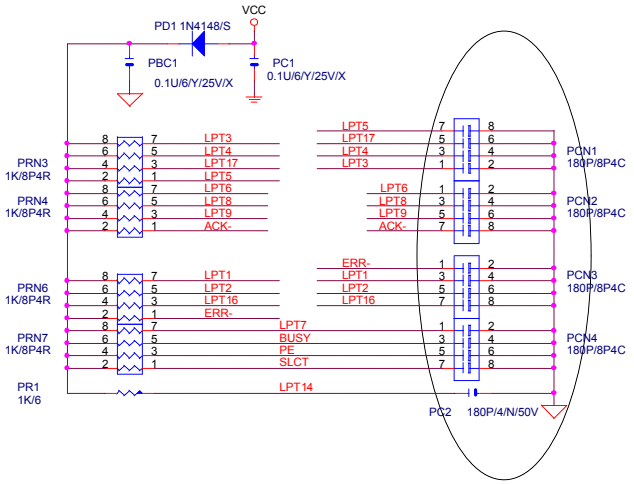

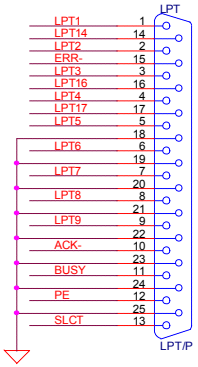

GIGABYTE CORP.

Title: COM & IR & LPT PORT &FLOOPY

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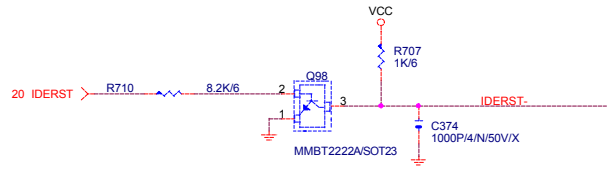
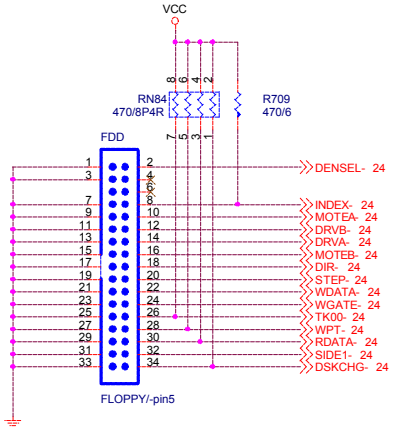
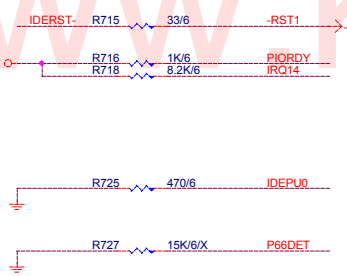
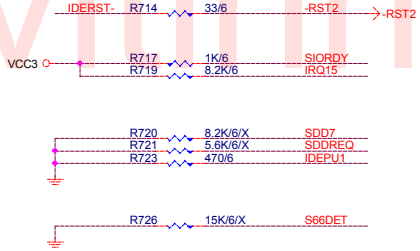
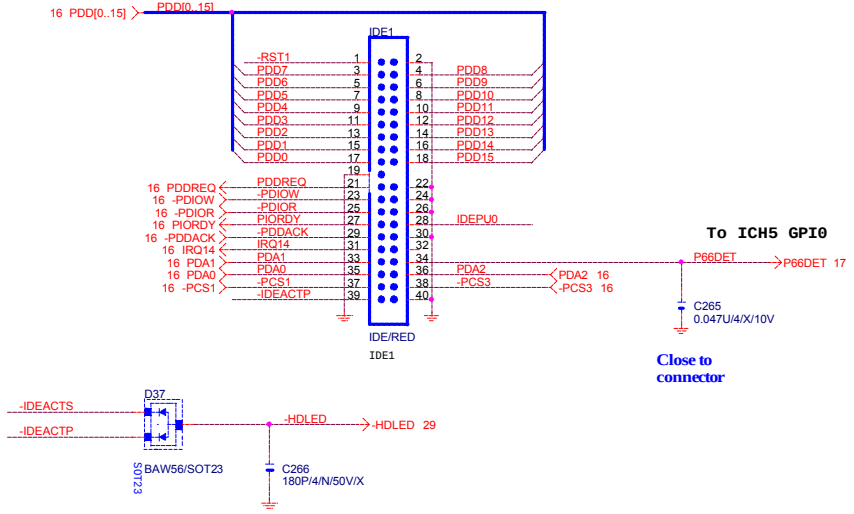




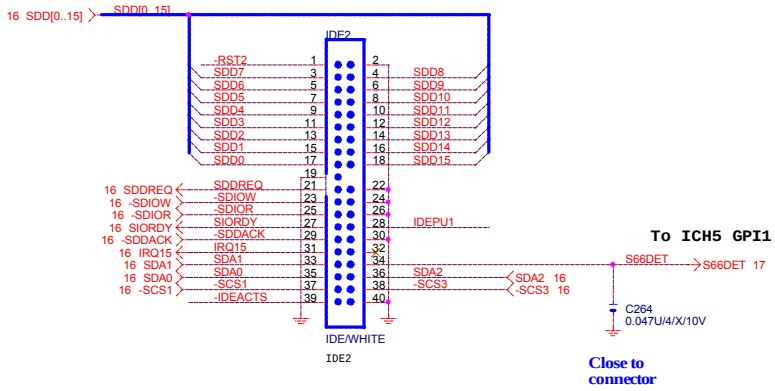




PRIMARY IDE CONNECTOR



SECONDARY IDE CONNECTOR



GIGABYTE CORP.		
Title: IDE CONNECTOR		
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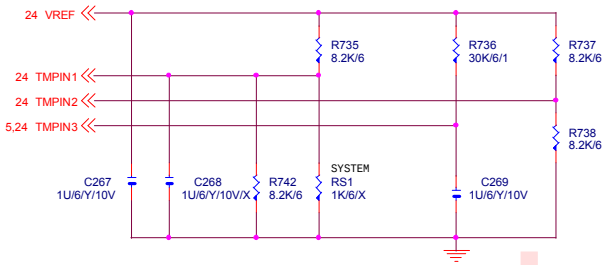
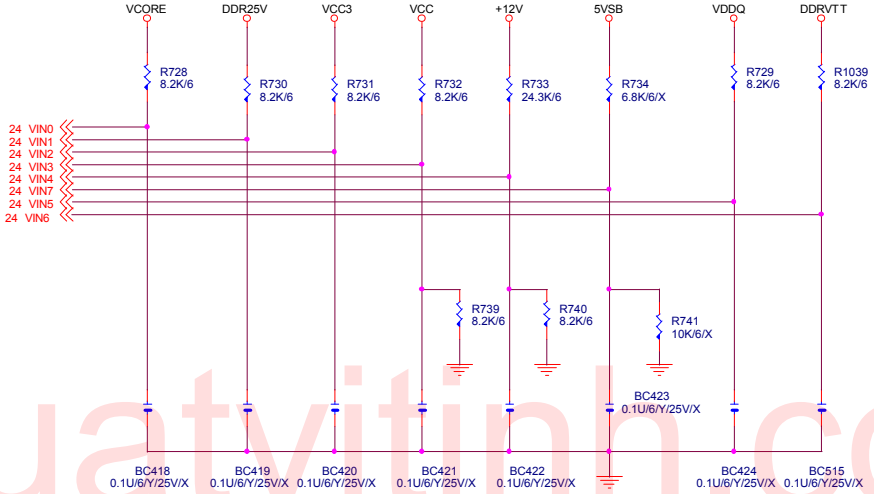
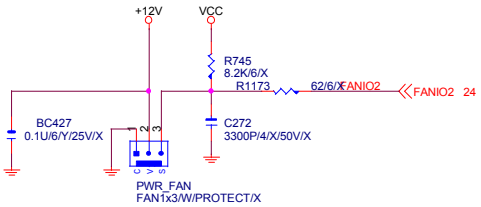
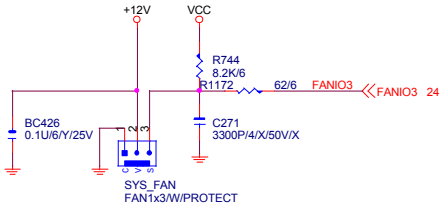
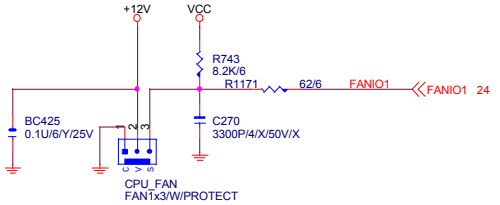
Hardware Monitor circuits

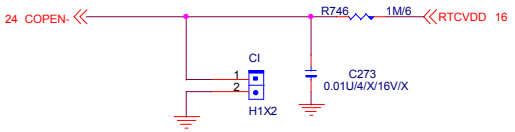












GIGABYTE CORP.		
Title: FAN/HWMO		
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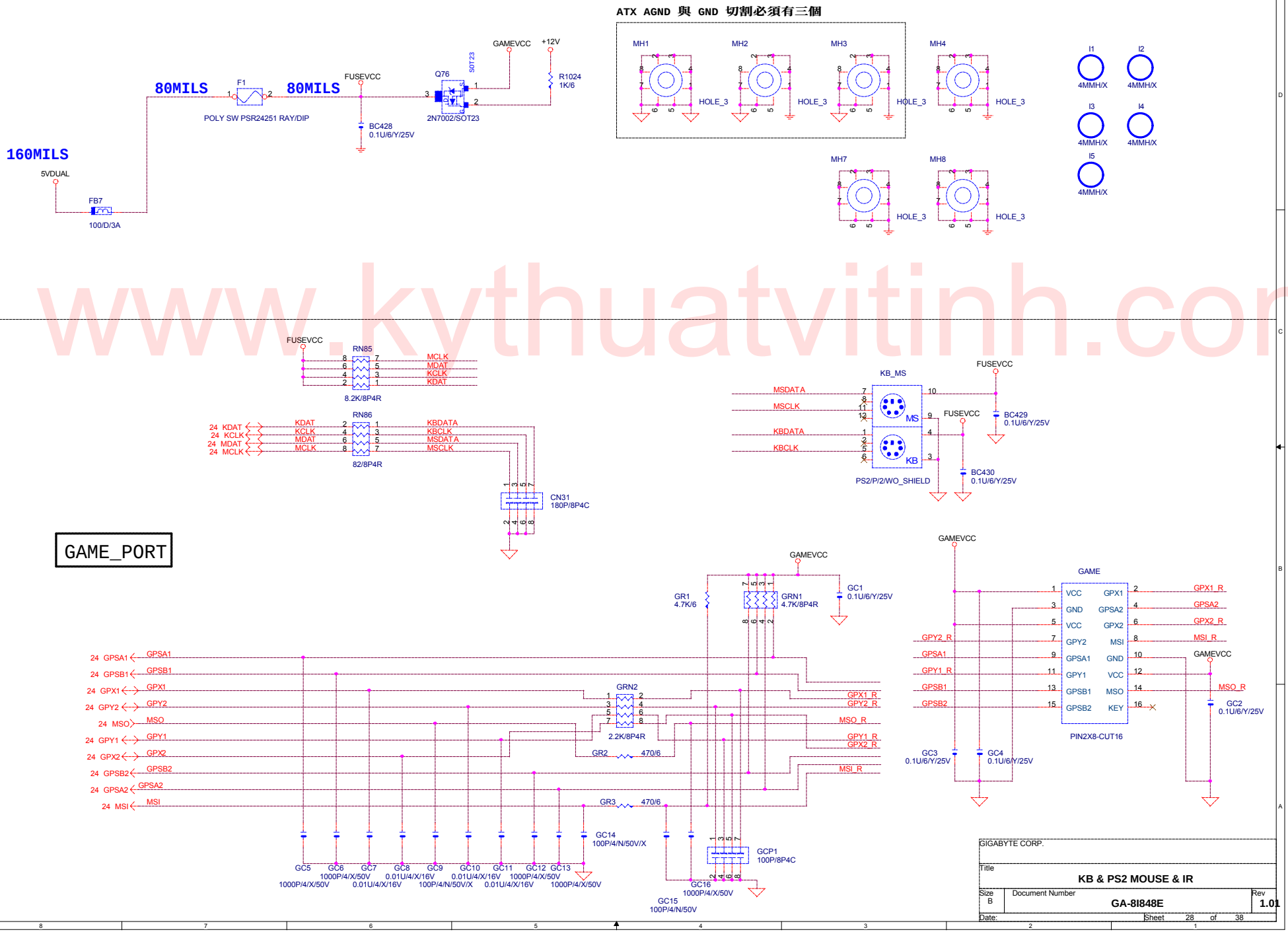
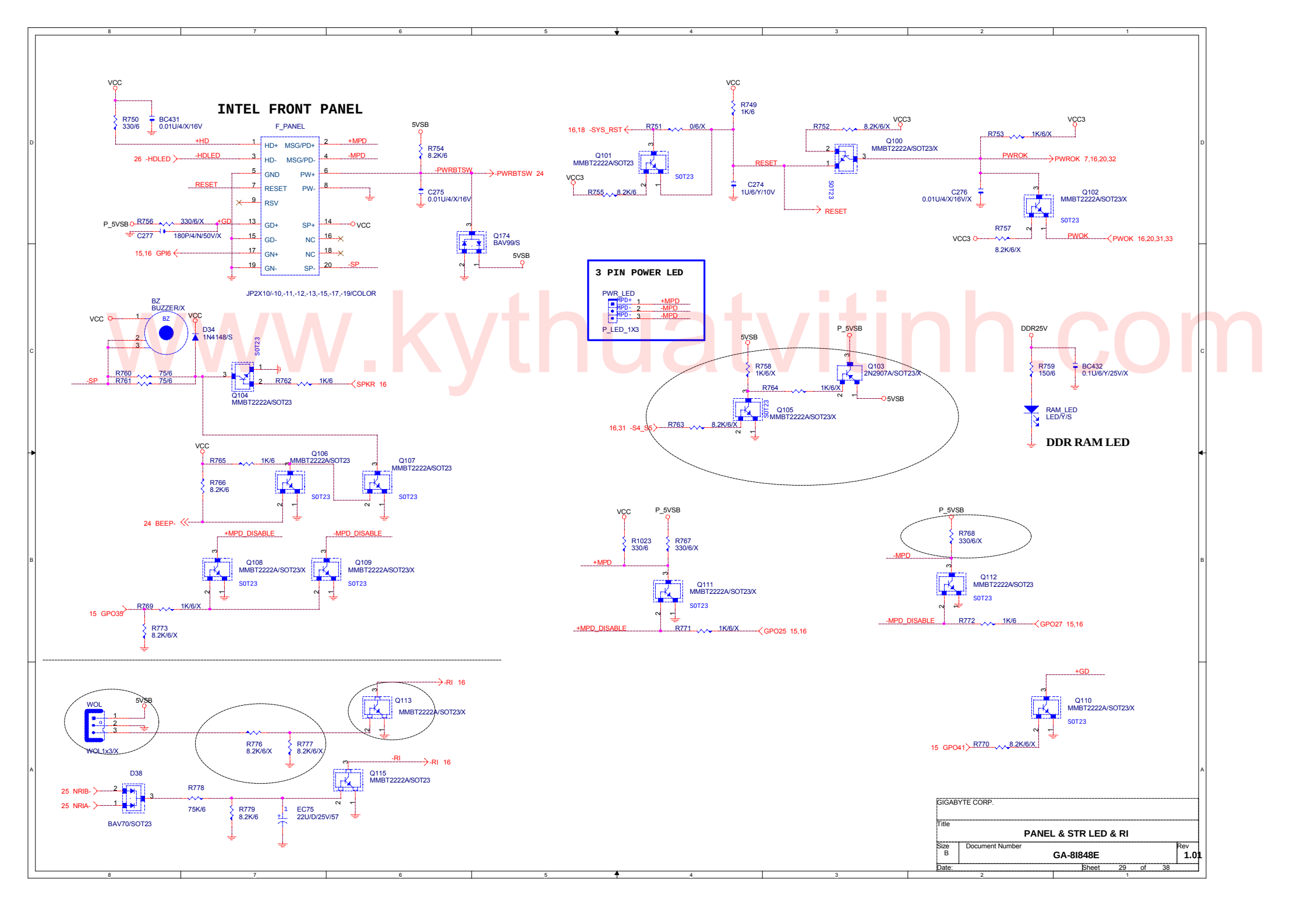
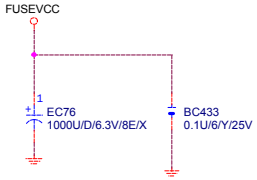
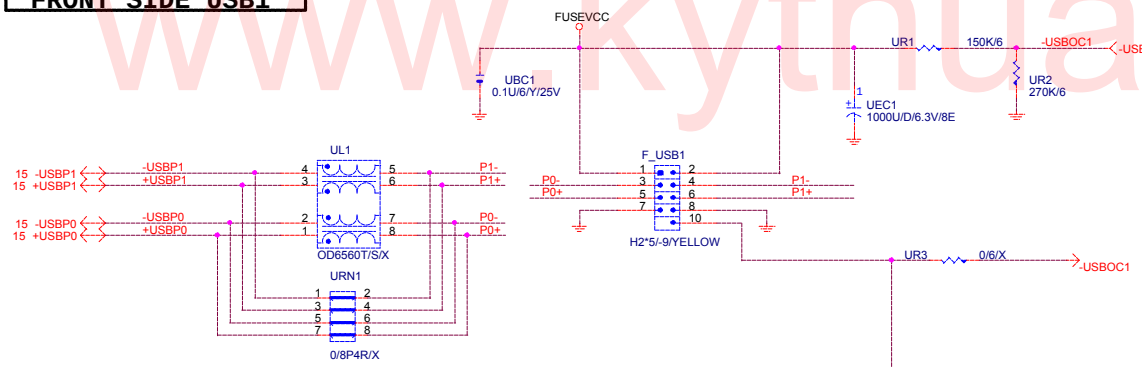


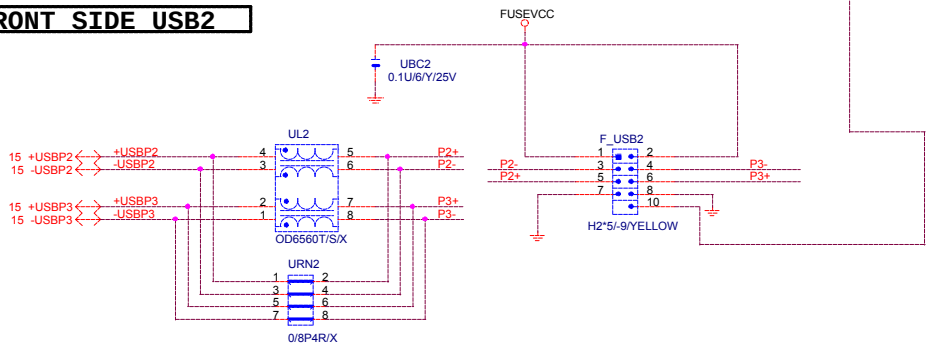




FRONT SIDE USB1



FRONT SIDE USB2



GIGABYTE CORP.		
Title: ICH USB PORT		
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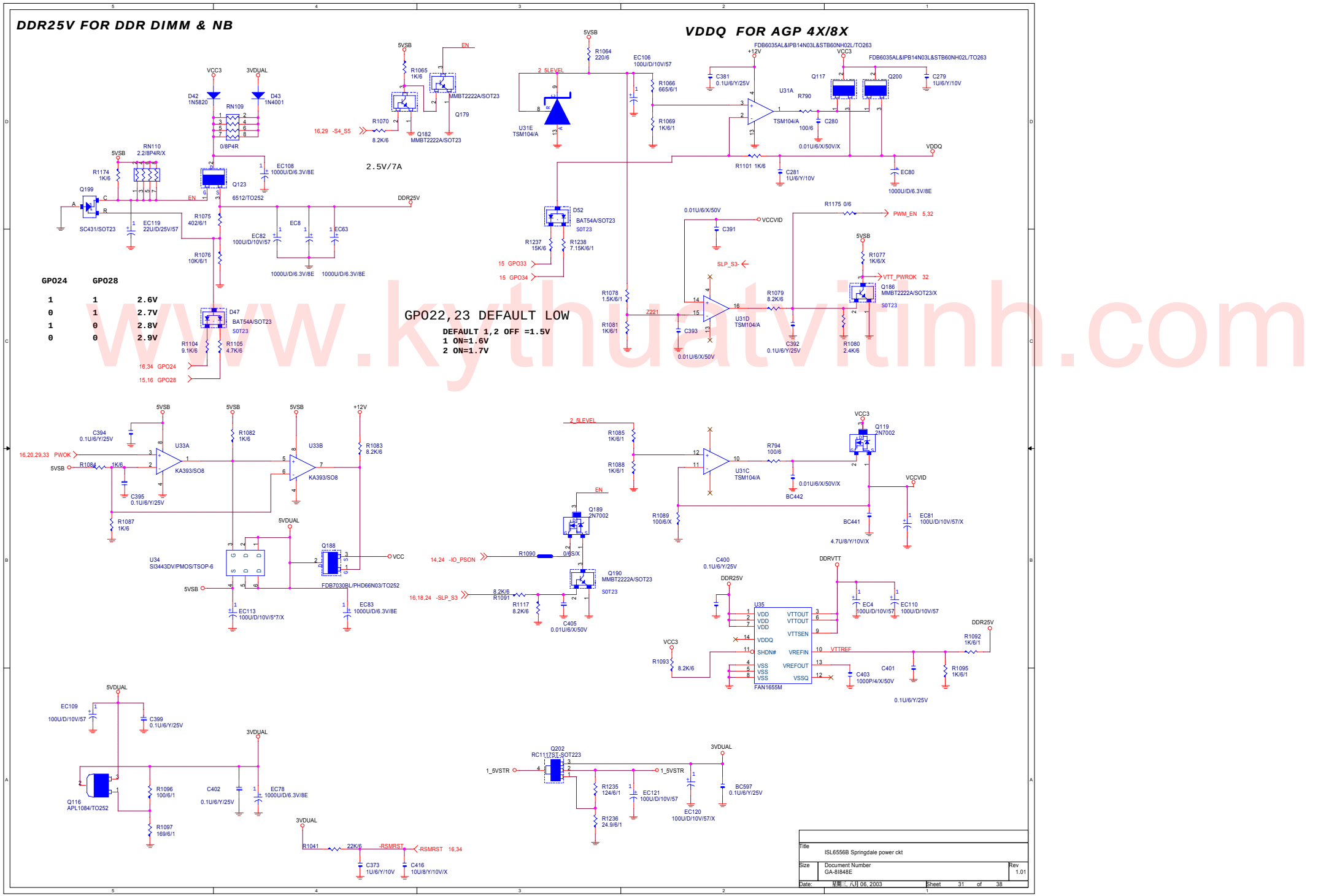
DDR25V FOR DDR DIMM & NB
VDDQ FOR AGP 4X/8X
2.5V/7A
GP024 GP028
1 1 2.6V
0 1 2.7V
1 0 2.8V
0 0 2.9V
GP022,23 DEFAULT LOW
DEFAULT 1,2 OFF =1.5V
1 ON=1.6V
2 ON=1.7V
www.kythuatvitinh.com
Title
ISL6556B Springdale power ckt
Size
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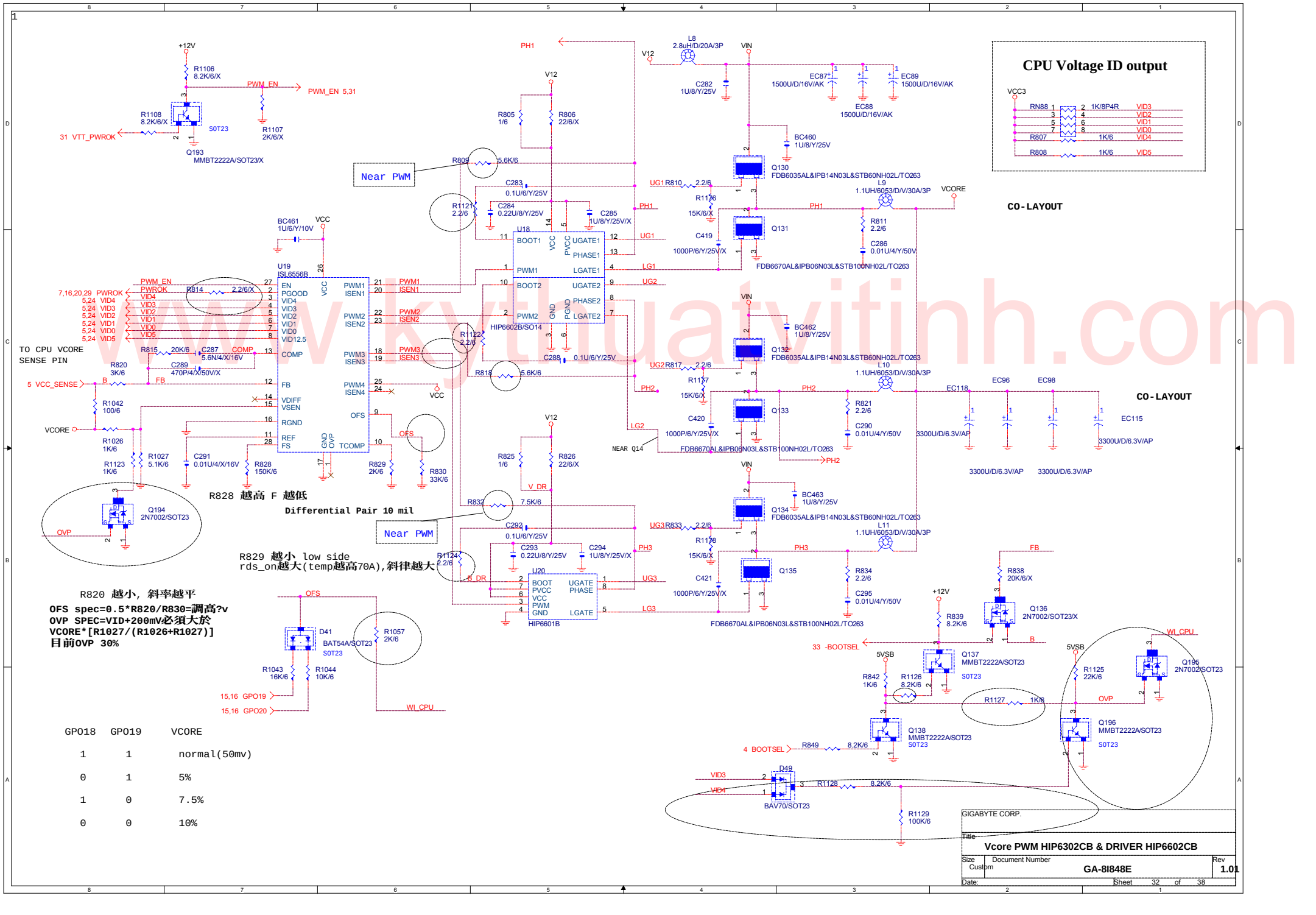
CPU Voltage ID output
CO-LAYOUT
CO-LAYOUT
Near PWM
Near PWM
TO CPU VCORE SENSE PIN
R828 越高 F 越低
Differential Pair 10 mil
R829 越小 low side rds_on越大(temp越高70A),斜律越大
R820 越小, 斜率越平
OFS spec=0.5*R820/R830=調高?v
OVP SPEC=VID+200mV必須大於 VCORE*[R1027/(R1026+R1027)]
目前OVP 30%
GPO18 GPO19 VCORE
1 1 normal(50mv)
0 1 5%
1 0 7.5%
0 0 10%
GIGABYTE CORP.
Vcore PWM HIP6302CB & DRIVER HIP6602CB
GA-8I848E
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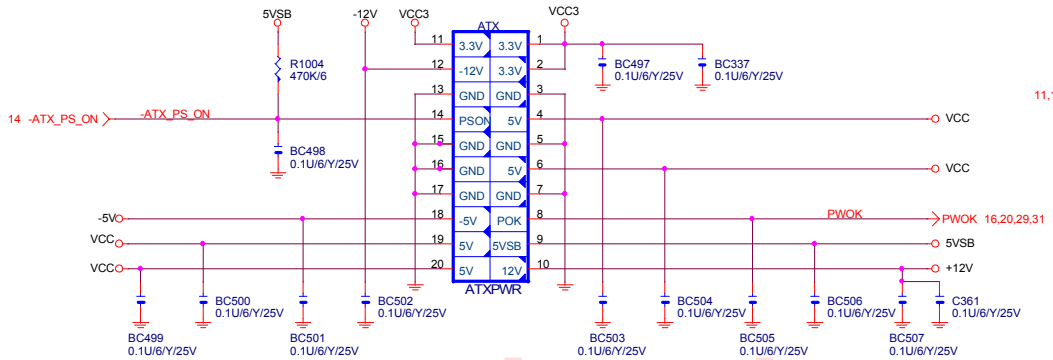
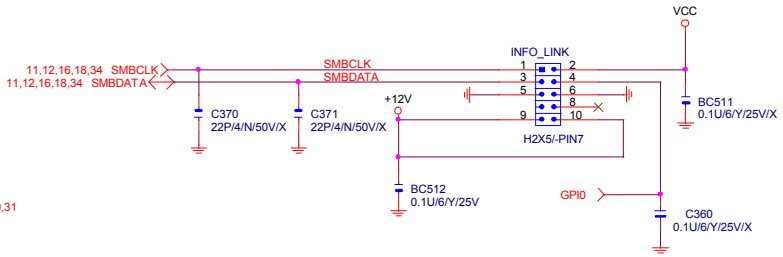
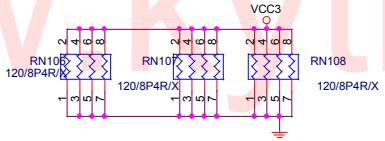
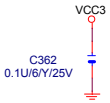
ATX POWER CONNECTOR



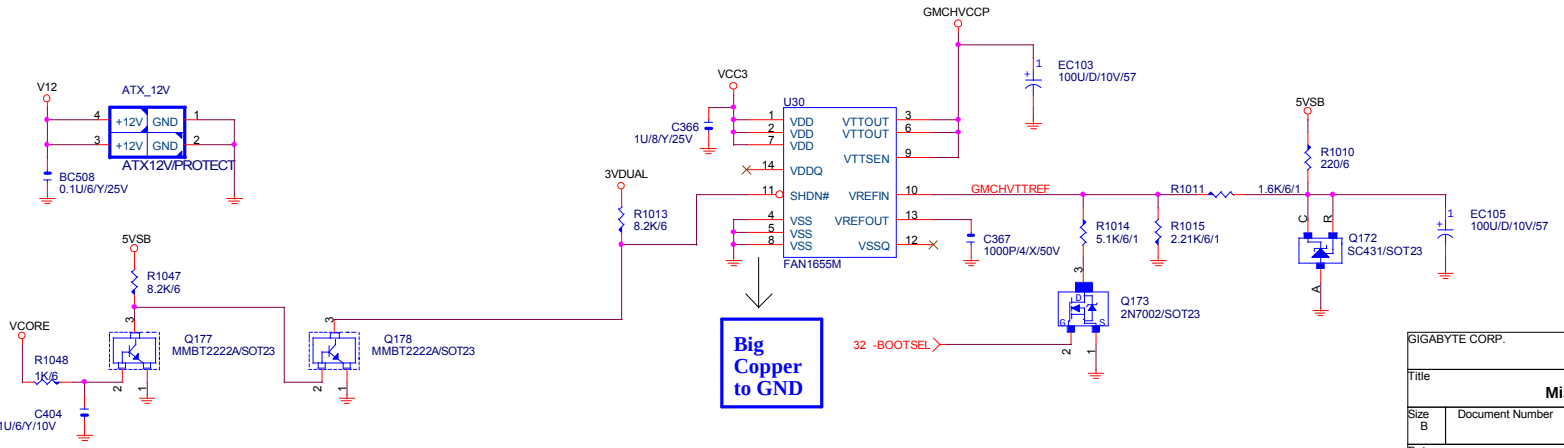
SMBUS CONN.







Northwood:+1.45V
Prescott:+1.225V



GIGABYTE CORP.		
Title: Misc. PWR & ATX CONN.		
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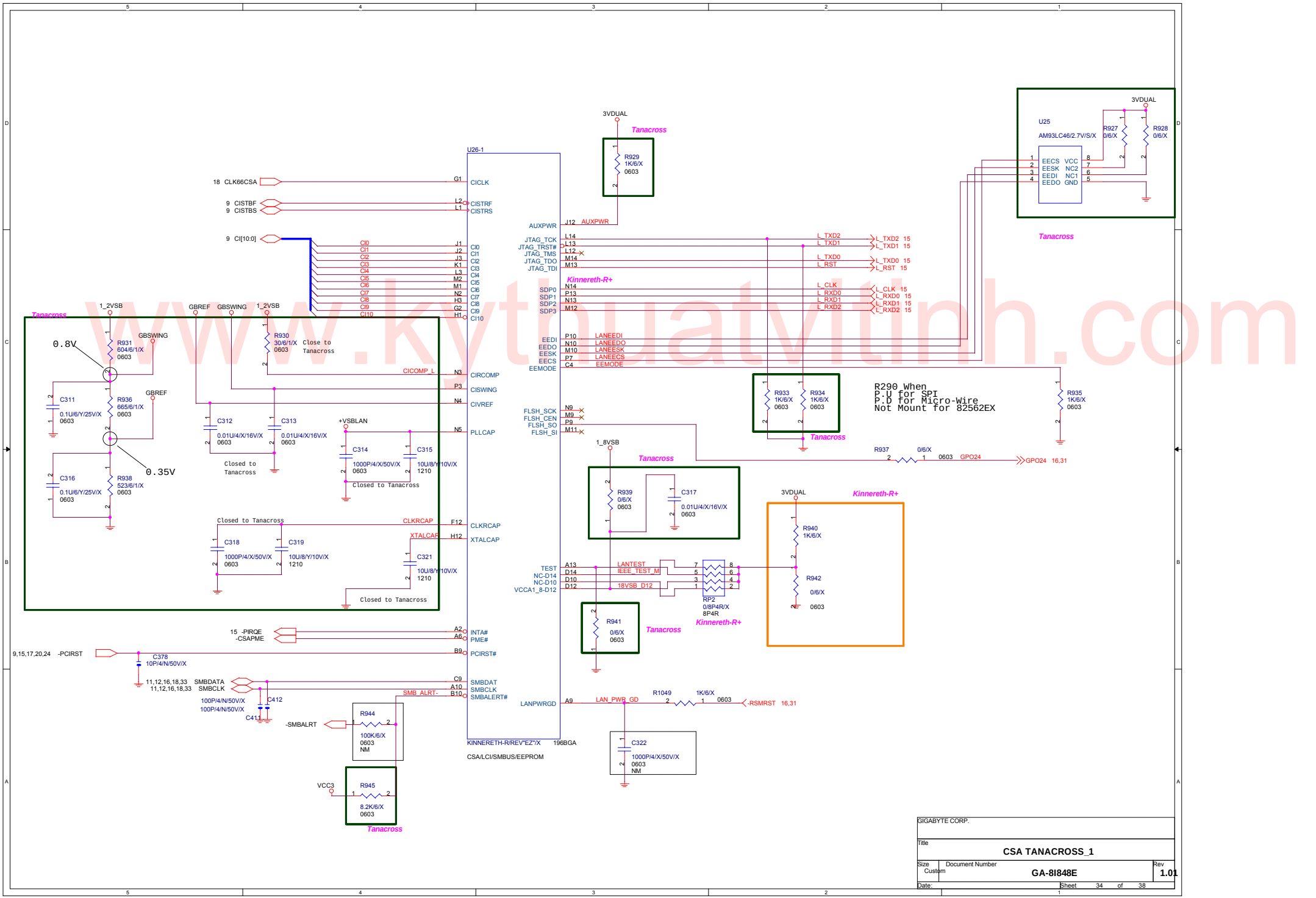

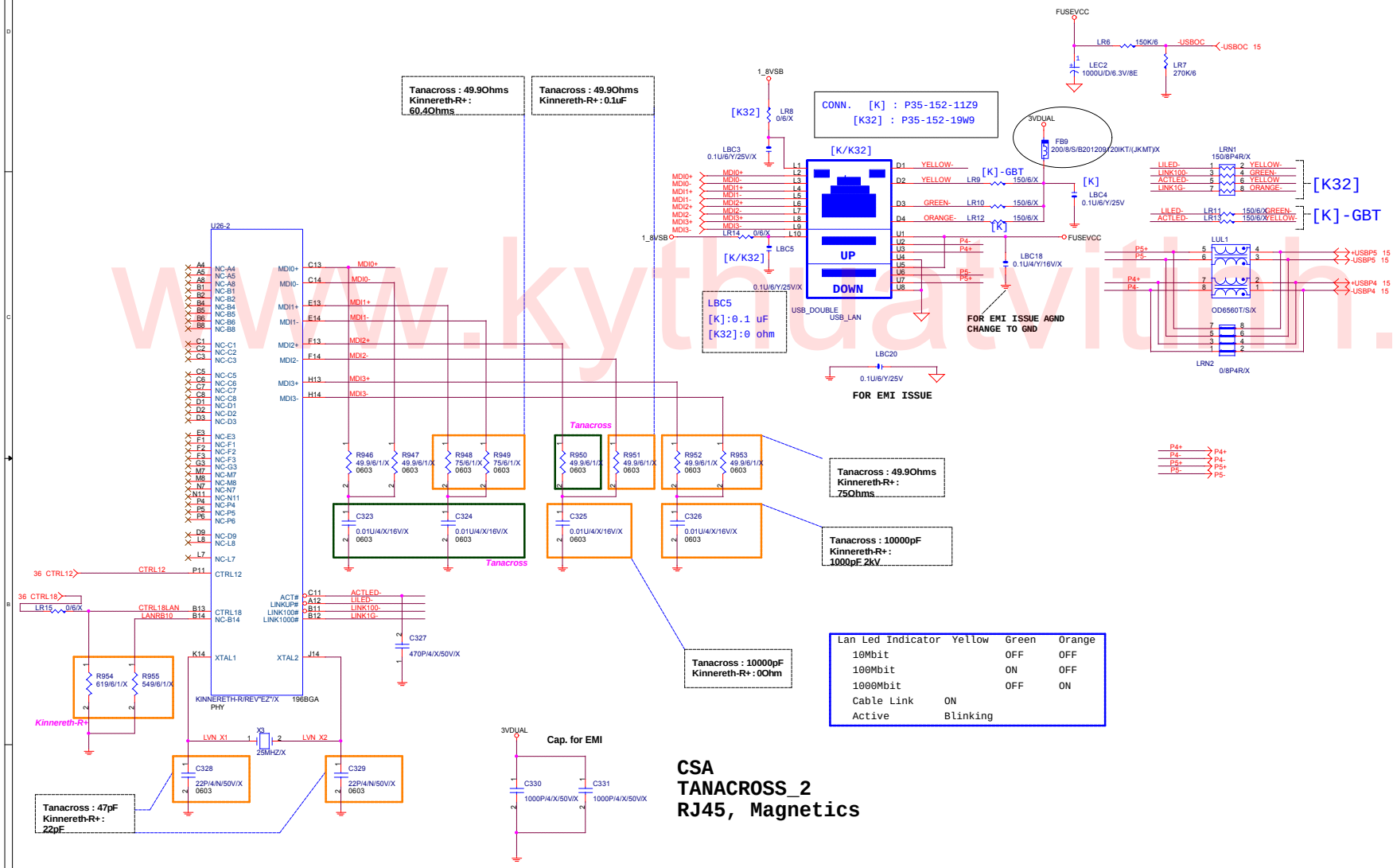

GIGABYTE CORP.			
Title	CSA TANACROSS_2		
Size C	Document Number GA-8I848E		Rev 1.01
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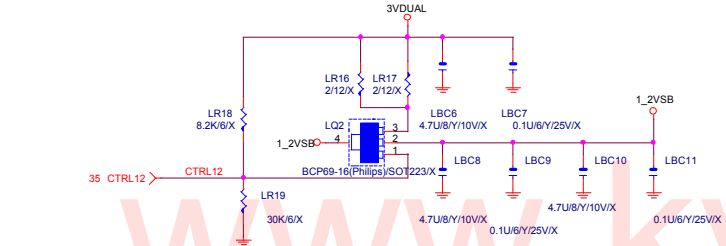
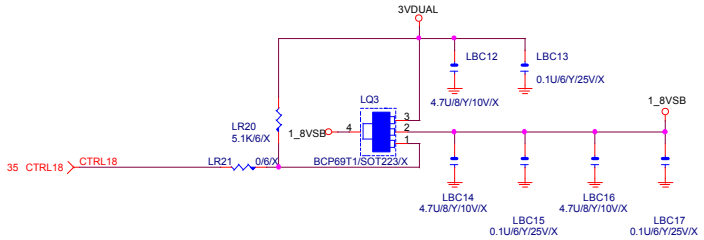
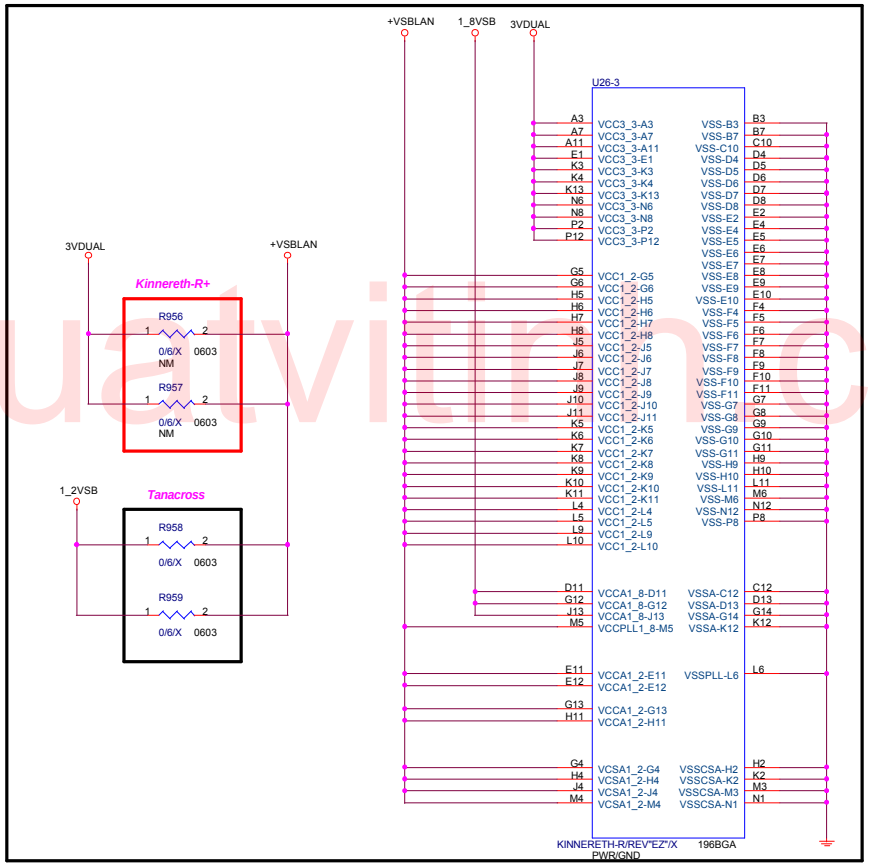


+1.2VSB
Max800mA
Typ500mA



+1.8VSB
Max500mA
Typ250mA

Tanacross



Tanacross

+VSBLAN **BYPASS Cap.**

C332 4.7U/8/Y/10V/X 1206
C333 4.7U/8/Y/10V/X 1206
C334 1U/6/Y/10V/X 0805
C335 0.1U/6/Y/25V/X 0603

+VSBLAN **BYPASS Cap.**

SC21 4.7U/8/Y/10V/B/X 1206
SC23 0.1U/6/Y/25V/B/X 0603
SC25 0.1U/6/Y/25V/B/X 0603

3VDUAL **BYPASS Cap.**

C343 0.1U/6/Y/25V/X 0603
C344 0.1U/6/Y/25V/X 0603
C345 0.1U/6/Y/25V/X 0603
C346 0.1U/6/Y/25V/X 0603
C347 4.7U/8/Y/10V/X 1206
C348 4.7U/8/Y/10V/X 1206

1_8VSB **BYPASS Cap.**

C338 4.7U/8/Y/10V/X 1206
C339 0.1U/6/Y/25V/X 0603
C340 0.1U/6/Y/25V/X 0603

Tanacross

GIGABYTE CORP.	
Title	CSA TANACROSS_3
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GIGABYTE GA-8I848E PCI ROUNTING LIST

PCI DEVICE	IDSEL	INT	CLOCK	REQ	GNT	
PCI SLOT1	16	C,F,G,A	PCLK0	REQ0-	GNT0-	
PCI SLOT2	17	F,G,A,C	PCLK1	REQ1-	GNT1-	
PCI SLOT3	18	G,A,C,F	PCLK2	REQ2-	GNT2-	
PCI SLOT4	19	A,C,F,G	PCLK3	REQ3-	GNT3-	
PCI SLOT5	20	C,F,G,A	PCLK4	REQ4-	GNT4-	
VIA 1394	21	F	PCICLK1394	REQ5-	GNT5-	

GIGABYTE CORP.

Title: PCI ROUNT LIST

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GIGABYTE GA-8I848E GPIO LIST

SHEET TITLE

GPIP	I/O	FUNCTION
GPI0/REQA-	I	PULL HIGH 8.2K to VCC3, SMB connector.
GPI1/REQ5-		PULL HIGH 8.2K to VCC, REQ5-.
GPI2/PIRQE-		PULL HIGH 8.2K to VCC3, PIRQE-.
GPI3/PIRQF-		PULL HIGH 8.2K to VCC3, PIRQF-.
GPI4/PIRQG-		PULL HIGH 8.2K to VCC, PIRQG-.
GPI5/PIRQH-	NA	PULL HIGH 8.2K to VCC
GPI6/AGPBUSY-	I	PULL 8.2K TO VCC3, PANEL GREEN_BUTTON
GPI7	I	DUAL BIOS FIRST BOOT SELECT.
GPI8	I	PULL 8.2K TO 3VDUAL, -CASPME.
GPI9/OC4-	NA	USB OC4-.
GPI10/OC5-	NA	USB OC5-.
GPI11/-SMBALRT	NA	PULL 8.2K TO 3VDUAL,-SMBALERT.
GPI12	I	PULL 8.2K TO VCC3,M/B REVERSION ID.
GPI13	I	LPC PME.
GPI14/OC6-	NA	USB OC6-.
GPI15/OC7-	NA	USB OC7-.
GPO16/GNTA-	NA	GPO16.
GPO17/GNT5-		GNT5-.
GPO18/STP_PCI-	NA	GPO18.
GPO19/SLP_S1-	O	DUAL BIOS.
GPO20/SLP_CPU-	O	DUAL BIOS.
GPO21/C3_SATA-	O	BLOCK TOP TABLE.
GPO22/CPUPERF-	O	PULL 8.2K TO VCC3,PANEL S3 POWER LED.

SHEET TITLE

GPIP	I/O	FUNCTION
GPO16		PULL 8.2K TO VCC3
GPO17		PULL 8.2K TO VCC3 (GNT5-)
GPO18		PULL 8.2K TO VCC3
GPO19		PULL 8.2K TO VCC3
GPO20		PULL 8.2K TO VCC3
GPO21		PULL 8.2K TO VCC3
GPO22		PULL 8.2K TO VCC3
GPO23		PULL 8.2K TO VCC3
GPO24		PULL 1K TO 3VDUAL (TOP BLOCK)
GPO25		PULL 4.7K TO 3VDUAL, LAN 100/10 DETECT.
GPO26		NOT IMPLEMENTED
GPO27		PULL 8.2K TO 3VDUAL, BIOS WRITE PROTECT.
GPO28		PULL 8.2K TO 3VDUAL

GIGABYTE CORP.

Title: GPIO LIST

Size: Custom | Document Number: GA-8I848E | Rev: 1.01

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